IsmailSabryy /
RISC-V-RV32I-Simulator

<> Code   Issues   Pull requests   Actions   Projects   Security   Insights   ⚙

Commits

main ▾   All users ▾

                                                               All time ▾

○— Commits on Apr 27, 2024

Test case 3. Minor bug fix to SH and SB   ...

ys1012002 committed 4 minutes ago

Test 1   ...

ys1012002 committed 3 hours ago

Code now executes in a step by step Fashion •••   ...

ys1012002 committed 5 hours ago

printing every step   ...

IsmailSabryy committed 5 hours ago

Editted the menu   ...

Abouheiba committed 6 hours ago

○— Commits on Apr 26, 2024

bonus of conversions   ...

IsmailSabryy committed yesterday

jal correction   ...

IsmailSabryy committed yesterday

Minor fix   ...

ys1012002 committed yesterday

Added showResult() function   ...

ys1012002 committed yesterday

Added the GUI but still requires implementation of functions

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Abouheiba committed yesterday

-o- Commits on Apr 25, 2024

JALR implemented. All Functions Done ! ···

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ys1012002 committed 2 days ago

Branching Done throughout code

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IsmailSabryy committed 2 days ago

Load and Store functions done ···

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ys1012002 committed 2 days ago

-o- Commits on Apr 23, 2024

sw and lw fixed

···

IsmailSabryy committed 4 days ago

SW v.2

···

ys1012002 committed 4 days ago

-o- Commits on Apr 22, 2024

SW attempt ···

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ys1012002 committed 5 days ago

LW changes

···

ys1012002 committed 5 days ago

-o- Commits on Apr 21, 2024

added the decimal to binary and decimal to hexa functions

···

Abouheiba committed last week

fixed X0 function also focused on how to relate to starting addresses

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IsmailSabryy committed last week

─○─ Commits on Apr 20, 2024

Added more function calls to main

ys1012002 committed last week   ...

minor bug fix

ys1012002 committed last week   ...

─○─ Commits on Apr 19, 2024

Updated more functions to read from file

ys1012002 committed last week   ...

─○─ Commits on Apr 17, 2024

Added functions to main

ys1012002 committed last week   ...

─○─ Commits on Apr 16, 2024

Shift and Logic functions done 💬

ys1012002 committed 2 weeks ago   ...

─○─ Commits on Apr 14, 2024

fixing an error

IsmailSabryy committed 2 weeks ago   ...

storing in a vector of pair and performing successful operations of some instructions

IsmailSabryy committed 2 weeks ago   ...

─○─ Commits on Apr 11, 2024

LW implementation

ys1012002 committed 2 weeks ago   ...

new

Abouheiba committed 2 weeks ago   ...

my part

Abouheiba committed 2 weeks ago   . . .

Reading testcase file, start compiling main: from file, handling expressions
                                                                                                   . . .
IsmailSabryy committed 2 weeks ago

Commits on Apr 7, 2024

Adding 12 RISCV instructions
                                                                                                   . . .
IsmailSabryy committed 3 weeks ago

Commits on Apr 6, 2024

6 placeholder functions ...
                                                                                                   . . .
ys1012002 committed 3 weeks ago

Adding conversion from decimal to binary to hexa and from binary to decimal
                                                                                                   . . .
IsmailSabryy committed 3 weeks ago

Initial commit
                                                                                                   . . .
IsmailSabryy committed 3 weeks ago