## Bangabandhu Sheikh Mujibur Rahman Science & Technology University Department of Computer Science & Engineering

2ndYear 1st Semester B.Sc. (Eng.) Examination- 2017 Course Code: MAT204 **Course Title: Matrices and Differential Equations** Time: 03 Hours Full Marks: 60 N.B. 1) Answer SIX questions, taking any THREE from each section. 2) All questions are of equal values. 3) Use separate answer script for each section. Section-A Define with example: (i) Matrix (ii) Rectangular matrix 1. (iii) Unitary matrix (iv) Diagonal matrix (v) Singular matrix. Define symmetric and skew-symmetric matrix. Prove that Every square matrix can be uniquely expressed as the sum of a symmetric matrix and a skew-symmetric matrix. Define Inverse of a matrix. Show that  $\begin{bmatrix} 1 & 2 & 3 \\ 2 & 5 & 7 \\ -2 & -4 & -5 \end{bmatrix}$  is the inverse of  $\begin{bmatrix} 3 & -2 & -1 \\ -4 & 1 & -1 \\ 2 & 0 & 1 \end{bmatrix}$ . 2. (a) Define idempotent and nilpotent matrix with an example, If and are non-singular matrices, then show that  $(AB)^{-1} = B^{-1}A^{-1}$ .

Define eigen values and eigen vectors. (a) 3. Find the Characteristic root of the matrix  $A = \begin{bmatrix} 1 & -3 & 3 \\ 3 & -5 & 3 \\ 6 & -6 & 4 \end{bmatrix}$ 

5 Verify Cayley-Hamilton theorem for the matrix  $A = \begin{bmatrix} 1 & 2 & 0 \\ 2 & -1 & 0 \\ 0 & 0 & -1 \end{bmatrix}$ 4. (a)

5 State and prove the Fundamental theorem of matrix.

Section-B

Define order and degree of differential equations. Find the differential equation of all circles 4 5. (a) of radius c. 3

Solve the initial value problem (y+2)dx + y(x+4)dy = 0 with y(-3) = -1. (b) 3

Solve the Ordinary Differential Equation,  $\frac{dy}{dx} + \frac{y}{x} = x^3$ . (c)

State and prove the necessary condition for an equation to be exact differential equation. 4 (a) 6. Solve the Ordinary Differential Equation

 $(2xy + 1)dx + (x^2 + 4y)dy = 0$ (b)

5 Solve the equation 7. (a)

 $x^2 \frac{d^2y}{dx^2} - 2x \frac{dy}{dx} + 2y = 0$ Solve the initial value problem  $\frac{d^2y}{dx^2} - 5\frac{dy}{dx} + 6y = 0$  subject to y(0) = 1 and y'(0) = 0. 5

Establish the general form of Charpit's method to solve the non-linear partial differential 5 (a) equations of order one. 5

Solve the differential equation  $\frac{d^2y}{dx^2} - y = x^2 \cos x$ . (b)

5

5

5

5

## Bangabandhu Sheikh Mujibur Rahman Science and Technology University Department of Computer Science and Engineering

2<sup>nd</sup> Year 1<sup>st</sup> Semester B.Sc. Engineering Final Examination 2017

Course Title: Industrial Management and Accountancy Course Code: ACC204 Time: 03 Hours Total Marks: 60

### N.B:

- i) Answer 06 (SIX) questions taking any 03 (THREE) from each section.
- ii) All Questions are of equal values.
- iii) Use separate answer script for each section.

### Section -A

1.	a) b) c)	What are t	rou mean by Business? the main functions of any Business? To classify Industry?	02 04 04
2.	a) b)	Characteri Do you su	artnership Business According to Partnership Act, 1932 with its stics apport that there are some basic differences between Partnership Business Hindu Family Business? If yes, What are the differences?	05 05
3.	۵)	Driefly des	cribe about the external Business Environment.	05
Э.	a)		a about the qualities of a successful businessman.	05
	b)	write down	about the quanties of a successful businessman.	
		XX71	- finitude of initiation of in	05
4.	a)	what are tr	ne features of joint stock company?	05
	b)	How do yo	u compare between private limited company and public limited company?	
			Section -B	
			Section 2	05
5.	a)	Discuss abo	out the objectives of Accounting in brief.	05
	b)	Who are th	e users of Accounting information?	03
6.		Mr Mizani	ur Rahman started his own consulting firm, Mizanur's consulting on July1, 2016.	40
υ.		The follow	ing transactions occurred during the july.	10
		July-1	Mizanur invested tk. 20,000 cash in the business.	
		July-2	Paid tk. 1,000 for office rent for the month.	
		July-3	Purchased tk. 800 of supplies on account.	
		July-5	Paid tk. 200 for advertisement expense.	
		July-9	Received tk. 3,000 cash for service provided.	
		July-12	Withdrew 1,000 cash for personal use.	
		July-15	Paid tk. 3,000 for employee salary.	
			Performed tk. 3,500 of services on account.	
		July-20	Paid for the supplies purchased on account on july-3.  Received a cash payment of tk. 2,000 for services provided on account on july-17.	
		July-23	Received a cash payment of tk. 2,000 for services provided at the servi	
			Borrowed tk. 5,000 from the bank on a note payable.	
		Iuly-29	Purchased office equipment for tk. 2,500 on account.	
		July-30	Paid tk. 300 for utilities.	

Instructions: Prepare a tabular analysis of the transactions.

Mr. Razu Started a business with investing Tk. 300000 on March 1, 2015. Other 7. transactions at the month are listed below: March 3, Purchased of goods Tk. 55000 from Mr. Rahim where Tk. 35000 has been paid in cash.

March 7, Sold of Goods Tk. 200000 in cash.

March 10, purchased equipment of Tk. 30000. March 15, Paid employee salaries for Tk. 25000

March 17, Incurred Advertising expenses in Tk. 15000 on account.

March 20, Received Tk. 35000 from Desh Auto Ltd. but services has not been

March 23, Mr. Razu withdraws Tk. 15000 for personal use.

March 25, Paid Tk. 20000 to Mr. Rahim.

March 30, Paid the bill of transaction occurred in January 17.

## What are the advantages of Trial Balance?

b)

# Incepta Pharmaceuticals Limited December 31, 2016

Serial No.	Accounts Title	Tk.
1.	Cash	1,16,600
2.	Accounts Receivable	1,54,000
3.	Supplies	15,000
4.	Prepaid Rent	28,000
5.	Office Equipment	3,40,000
6.	Accumulated Depreciation- Equipment	80,000
7.	Notes payable	1,10,600
8.	Accounts payable	60,000
9.	Supplies Expense	57,000
10.	Salaries payable	30,000
11.	Capital	2,50,000
12.	Drawing	1,00,000
13.	Service Revenue	8,80,000
14.	Advertising Expense	1,20,000
15.	Interest payable	5,000
16.	Depreciation Expense	40,000
17.	Rent Expense	50,000
18.	Salaries Expense	3,90,000
19.	Interest Expense	5,000

Instruction: Prepare a Trial Balance.



07

## Bangabandhu Sheikh Mujibur Rahman Science & Technology University Department of Computer Science and Engineering

2<sup>nd</sup> Year 1<sup>st</sup> Semester B.Sc. (Engg.) Final Examination-2017

Course No.: CSE200 Full Marks: 60

N.B.

Course Title: Introduction to Digital Electronics Time: 03 hours

i) Answer  $\boldsymbol{SIX}$  questions, taking any  $\boldsymbol{THREE}$  from each Section.

- ii) All questions are of equal values.
- iii) Use separate answer script for each section.

		Section-A	
1	. a	Define digital system with examples. What is Error-Detection Code? How can you detect the errors using Error-Detection Code during transmission?	4
	b'		
		i. (11001) <sub>2</sub> - (10001) <sub>2</sub>	4
		ii. (10001) <sub>2</sub> - (11001) <sub>2</sub>	
	c)		2
		of the order to th	2
2.	a)	a je uppijing be word a medicin as	4
	1.	many times as necessary and draw the logic diagrams for both functions F and F'.	
	b)	8 Trouble (BOT) form.	4
		i. (A+B'C) C	
	0)	ii. (A+C) (AB+AC)  Express the Boolean function F(A, B, C) A + B(C)	2
	c)	Express the Boolean function $F(A, B, C) = A + B'C$ in a sum of minterms.	2
3.	a)	Simplify the Boolean function $F(w, x, y, z) = \sum (1, 3, 7, 11, 15)$ and the don't-care conditions $D(w, x, y, z) = \sum (0, 2, 5)$ using K-map.	4
	b)	How many ways can you simplify the Boolean function? Explain with examples.	3
	c)	Write down the design procedure of digital logic.	3
		Sold and the sold	
4.	a)	Simplify the Boolean function $F = x'yz + x'yz' + xy'z' + xy'z$ using K-map.	4
	b)	Design a full adder circuit.	3
	c)	What is BCD code? Which of the following BCD codes are forbidden?	3
		1001, 1011, 1010, 0100, 1111, 1101	
		Section-B	4
5.	a)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and	4
5.		Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.	4
5.	b)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.	
5.		Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even	4
5.	b)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.	4 2
	b) c)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.	4 2
<ol> <li>5.</li> <li>6.</li> </ol>	b)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with	4 2
	b) c)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table.	4 2 4
	b) c) a)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table.	4 2
	b) c)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table.  Sketch the electronic logic circuit for the following Boolean function:  E(A B.C) = (AB + BC)'	4 2 4
	b) c) a) b)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table.  Sketch the electronic logic circuit for the following Boolean function:  E(A B.C) = (AB + BC)'	4 2 4
	b) c) a)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table. Draw and explain the circuit diagram of a transistor astable multivibrator. Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity. Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table. Sketch the electronic logic circuit for the following Boolean function: $F(A,B,C) = (AB+BC)'$ What is propagation delay and noise margin?	4 2 4 2
6.	b) c) a) b) c)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table.  Sketch the electronic logic circuit for the following Boolean function:  F(A,B,C) = (AB + BC)'  What is propagation delay and noise margin?	4 2 4 2 4
	b) c) a) b) c)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table.  Sketch the electronic logic circuit for the following Boolean function:  F(A,B,C) = (AB + BC)'  What is propagation delay and noise margin?	4 2 4 2
6.	b) c) a) b) c) a) b)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table.  Sketch the electronic logic circuit for the following Boolean function:  F(A,B,C) = (AB + BC)'  What is propagation delay and noise margin?  What is totem-pole output? Draw the electronic circuit diagram with totem-pole output.  Draw the electronic circuit diagram of resistor-transistor logic (RTL) NOR gate with	4 2 4 4 4
6.	b) c) a) b) c) a) b)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table.  Sketch the electronic logic circuit for the following Boolean function:  F(A,B,C) = (AB + BC)'  What is propagation delay and noise margin?  What is totem-pole output? Draw the electronic circuit diagram with totem-pole output.  Draw the electronic circuit diagram of resistor-transistor logic (RTL) NOR gate with	4 2 4 2 4
6.	b) c) a) b) c) c)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table.  Sketch the electronic logic circuit for the following Boolean function:  F(A,B,C) = (AB + BC)'  What is propagation delay and noise margin?  What is totem-pole output? Draw the electronic circuit diagram with totem-pole output.  Draw the electronic circuit diagram of resistor-transistor logic (RTL) NOR gate with function and truth table.  Show the circuit of a two-input NOR gate using MOS transistors.	4 2 4 4 4
6.	b) c) a) b) c) a) b) c) c)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table.  Sketch the electronic logic circuit for the following Boolean function:  F(A,B,C) = (AB + BC)'  What is propagation delay and noise margin?  What is totem-pole output? Draw the electronic circuit diagram with totem-pole output.  Draw the electronic circuit diagram of resistor-transistor logic (RTL) NOR gate with function and truth table.  Show the circuit of a two-input NOR gate using MOS transistors.	4 2 4 2 4 4 2
6.	b) c) a) b) c) a) b) c) a) a) a) a) b) c) a) a) b) c) a) a) b) b) c) a) a) b) b) c) a) b) b) b) c) b)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table.  Sketch the electronic logic circuit for the following Boolean function:  F(A,B,C) = (AB + BC)'  What is propagation delay and noise margin?  What is totem-pole output? Draw the electronic circuit diagram with totem-pole output.  Draw the electronic circuit diagram of resistor-transistor logic (RTL) NOR gate with function and truth table.  Show the circuit of a two-input NOR gate using MOS transistors.	4 2 4 2 4 4 2
6. 7.	b) c) a) b) c) a) b) c) a) b)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table.  Sketch the electronic logic circuit for the following Boolean function:  F(A,B,C) = (AB + BC)'  What is propagation delay and noise margin?  What is totem-pole output? Draw the electronic circuit diagram with totem-pole output.  Draw the electronic circuit diagram of resistor-transistor logic (RTL) NOR gate with function and truth table.  Show the circuit of a two-input NOR gate using MOS transistors.  Compare TTL, ECL and CMOS logic families with respect to propagation delay, speed-power product, fan-out, noise margin and maximum clock rate.  How does CMOS internal circuitry differ from N-MOS?	4 2 4 2 4 4 2 5
6. 7.	b) c) a) b) c) a) b) c) a) b)	Draw the electronic circuit diagram of diode logic AND and OR gate with function and truth table.  Draw and explain the circuit diagram of a transistor astable multivibrator.  Write down the number 0 to 13 using weights 8421. Then make these numbers in even parity.  Draw the electronic circuit diagram of diode-transistor logic (DTL) NAND gate with function and truth table.  Sketch the electronic logic circuit for the following Boolean function:  F(A,B,C) = (AB + BC)'  What is propagation delay and noise margin?  What is totem-pole output? Draw the electronic circuit diagram with totem-pole output.  Draw the electronic circuit diagram of resistor-transistor logic (RTL) NOR gate with function and truth table.  Show the circuit of a two-input NOR gate using MOS transistors.	4 2 4 4 2 4 4 2 5 3

# Bangabandhu Sheikh Mujibur Rahman Science & Technology University Department of Computer Science and Engineering

2<sup>nd</sup> Year 1<sup>st</sup> Semester B.Sc. (Engg.) Final Examination-2017

Course No.: STA204 Full Marks: 60

Course Title: Theory of Statistics Time: 03 hours

N.B.

- i) Answer SIX questions, taking any THREE from each Section.
- ii) All questions are of equal values.
- iii) Use separate answer script for each section.

## Section-A

1. a) Define chi-square variate and chi-square distribution.

b) Write down the applications of chi-square distribution.

3

c) Find the mean and variance of chi-square distribution.

- 2. a) Define statistic, estimator, unbiased estimator and most efficient estimator. What are the sufficient conditions for consistency?
  - b) If  $X_1, X_2, ..., X_n$  are random observations on a Bernoulli variate X taking the value 1 with 4 probability p and the value 0 with probability (1-p), show that  $\bar{X}$  is a consistent estimator of p.
- 3. a) What is statistical inference? State two important problems in statistical inference.
- 2
- b) What are the characteristics of a good estimator? State the invariance property of consistent
- c) Suppose that  $X_1$ ,  $X_2$ , and  $X_3$  is a random sample of size 3 from a population with mean  $\mu$  6 and variance  $\sigma^2$ .  $T_1$ ,  $T_2$ , and  $T_3$  are the estimators used to estimate mean value  $\mu$ , where,

$$T_1 = X_1 + X_2 - X_3$$

$$T_2 = 2X_1 + 3X_3 - 4X_2$$
 and

$$T_3 = \frac{1}{3}(\lambda X_1 + X_2 + X_3)$$

- i. Are  $T_1$  and  $T_2$  unbiased estimators of  $\mu$ ?
- ii. Find the value of  $\lambda$  such that  $T_3$  is unbiased estimator of  $\mu$ ?
- iii. Which is the best estimator?

- 4. a) Define efficiency and amount of efficiency of an estimator. b) Let,  $X_1, X_2, ..., X_n$  is a random sample from a normal population  $N(\mu, 1)$ . Show that t = $\frac{1}{n}\sum_{i=1}^{n}x_{i}^{2}$  is an unbiased estimator of  $\mu^{2}+1$ .
  - Find the maximum likelihood estimator for the parameter  $\lambda$  of a Poisson distribution on the basis of sample size n. Given that the probability mass function of Poisson distribution is -

$$f(x) = \frac{e^{-\lambda}\lambda^x}{x!}; \quad x = 0, 1, 2, \dots$$

## Section-B

Define level of significance and power of test.

- Explain which of the following is a two-tailed test, a left-tailed test, or a right-tailed test.
- 2

- $H_0$ :  $\mu = 45$ ,  $H_1$ :  $\mu > 45$
- $H_0$ :  $\mu = 23$ ,  $H_1$ :  $\mu \neq 23$ ii.
- $H_0$ :  $\mu = 75$ ,  $H_1$ :  $\mu < 75$

Show the rejection and non-rejection regions for each of these cases by drawing a sampling distribution curve for the sample mean, assuming that it is normally distributed.

- In Microsoft corporation 100 workers are working, 25 of them are skilled. In another industry say for example IBM, there are 18 skilled workers out of 125 workers. Are the skilled workers similar in both industries?  $(Z_{0.025} = 1.96)$
- a) A researcher claims that the average wind speed in a certain city is  $\mu_0$  miles per hour. A 5 sample of n days has an average wind speed of  $\bar{x}$  miles per hour. The known standard deviation of the population is  $\sigma_0$  mile per hour. At  $\alpha\%$  level of significance, is there enough evidence to reject the claim? Write the steps of the test.

b) The number of computer engineers coming from two different universities A and B at employed in different organizations to do job related to computer. The numbers are give for different years as follows:

University A: 18 16 15 20 18 15 12

University B: 20 14 12 22 16 14 15 10 12 18 10

Assume that variances of number of graduates employed in computer related job are 7 an 16 respectively for university A and B. Do you think employment facility for both th universities are similar?  $(Z_{0.025} = 1.96)$ 

- 7. a) Discuss the tests of regression coefficient and correlation coefficient.
  - b) The following are the heights (x in mm) and weights (y in gm) of some animals

Height (x): 155 158 155 152 159 158 154 154 152 154 Weights (y): 65 62 56 56 60 58 58 55 56 60

- i. Is the weight of animals significantly increased due to the increase in height?
- ii. Test the significance of correlation of height and weight.  $(t_{0.05, 8} = 2.302)$
- 8. a) How do you test the significance of a sample regression coefficient?
  - b) What is non-parametric test? What are the advantages and disadvantages of non-parametric test?
  - c) Describe two sample sign test.