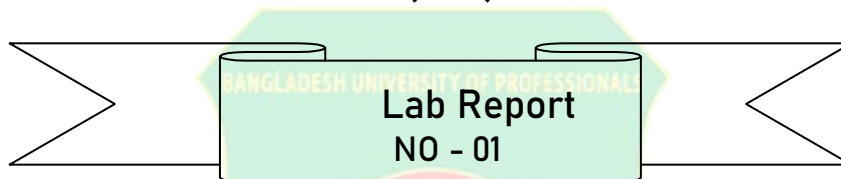




**BANGLADESH UNIVERSITY OF  
PROFESSIONALS**  
FACULTY OF SCIENCE & TECHNOLOGY  
DEPT. OF COMPUTER SCIENCE & ENGINEERING  
(CSE)



**COURSE NAME:** Microprocessors, Microcontrollers, and Assembly Language Laboratory

**COURSE CODE:** CSE-3106

**TITLE :** Introduction to 8086 Microprocessor Architecture &  
Assembly Language

**SUBMITTED BY:**

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**SECTION** : A

**LEVEL/TERM** : 3<sup>rd</sup>

**SEMESTER** : 1<sup>st</sup>

**DATE OF SUBMISSION:** November 3, 2024

## **Experiment No : 01**

**Name of the Experiment :** Introduction to 8086 Microprocessor Architecture & Assembly Language

### **Lab Questions :**

1. Write an assembly program that initializes the AX register with the value 1234H and the BX register with 0F12H. Then, add BX to AX and subtract 567H from the result. Store the final result in the CX register.
2. Write an assembly program that initializes the AX register with 1234H. Then, triple the value in AX by using addition instructions. Finally, store the result in the BX register.
3. Create a program that initializes AX with 1000H, BX with 2000H, and CX with 3000H. Calculate the sum of these three registers and store the result in the DX register.

### **Code :**

#### **1. Solution :**

```
CODE SEGMENT
    ASSUME CS:CODE, DS:CODE
    MOV AX, 1234H
    MOV BX, 0F12H
    ADD AX, BX
    SUB AX, 567H
    MOV CX, AX
HLT
CODE ENDS
END
```

#### **2. Solution :**

```
CODE SEGMENT
    ASSUME CS:CODE, DS:CODE
    MOV AX, 1234H
    ADD AX, AX
    ADD AX, AX
    MOV BX, AX
```

```

HLT
CODE ENDS
END

```

### 3. Solution :

```

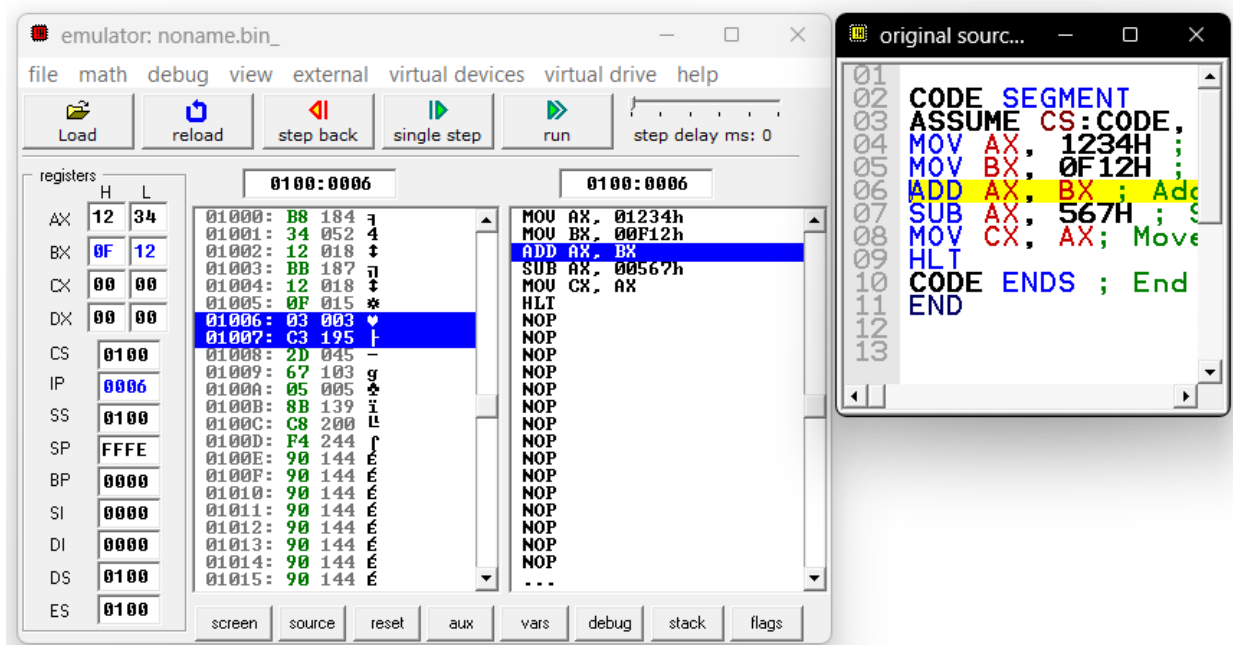
CODE SEGMENT
    ASSUME CS:CODE, DS:CODE
    MOV AX, 1000H
    MOV BX, 2000H
    MOV CX, 3000H
    ADD BX, AX ; BX = AX + BX
    ADD CX, BX ; CX = CX + BX
    MOV DX, CX
HLT
CODE ENDS
END

```

**Output :** The attached images are conveyed in the following manner : Value Initialization , Halfway of Operations and finally Final Output.

#### Solution 1 :

**AX = 1234H, BX = 0F12H**



$$AX = AX + BX - 567H = 1BDFH$$

emulator: noname.bin\_

file math debug view external virtual devices virtual drive help

Load reload step back single step run step delay ms: 0

registers

	H	L
AX	1B	DF
BX	0F	12
CX	00	00
DX	00	00
CS	0100	
IP	000B	
SS	0100	
SP	FFFE	
BP	0000	
SI	0000	
DI	0000	
DS	0100	
ES	0100	

0100:000B

01000: B8 184	MOV AX, 01234h
01001: 34 052	MOV BX, 00F12h
01002: 12 018	ADD AX, BX
01003: BB 187	SUB AX, 00567h
01004: 12 018	MOV CX, AX
01005: 0F 015	HLT
01006: 03 003	NOP
01007: C3 195	NOP
01008: 2D 045	NOP
01009: 67 103	NOP
0100A: 05 005	NOP
0100B: 8B 139	NOP
0100C: C8 200	NOP
0100D: F4 244	NOP
0100E: 90 144	NOP
0100F: 90 144	NOP
01010: 90 144	NOP
01011: 90 144	NOP
01012: 90 144	NOP
01013: 90 144	NOP
01014: 90 144	NOP
01015: 90 144	NOP

original sourc...

```

01
02 CODE SEGMENT
03 ASSUME CS:CODE,
04 MOV AX, 1234H ;
05 MOV BX, 0F12H ;
06 ADD AX, BX ; Add
07 SUB AX, 567H ; S
08 MOV CX, AX ; Move
09 HLT
10 CODE ENDS ; End
11 END
12
13

```

$$CX = AX = 1BDFH$$

emulator: noname.bin\_

file math debug view external virtual devices virtual drive help

Load reload step back single step run step delay ms: 0

registers

	H	L
AX	1B	DF
BX	0F	12
CX	1B	DF
DX	00	00
CS	0100	
IP	000B	
SS	0100	
SP	FFFE	
BP	0000	
SI	0000	
DI	0000	
DS	0100	
ES	0100	

0100:000B

01000: B8 184	MOV AX, 01234h
01001: 34 052	MOV BX, 00F12h
01002: 12 018	ADD AX, BX
01003: BB 187	SUB AX, 00567h
01004: 12 018	MOV CX, AX
01005: 0F 015	HLT
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01007: C3 195	NOP
01008: 2D 045	NOP
01009: 67 103	NOP
0100A: 05 005	NOP
0100B: 8B 139	NOP
0100C: C8 200	NOP
0100D: F4 244	NOP
0100E: 90 144	NOP
0100F: 90 144	NOP
01010: 90 144	NOP
01011: 90 144	NOP
01012: 90 144	NOP
01013: 90 144	NOP
01014: 90 144	NOP
01015: 90 144	NOP

original sourc...

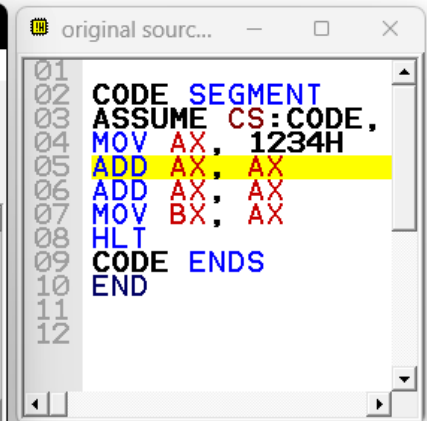
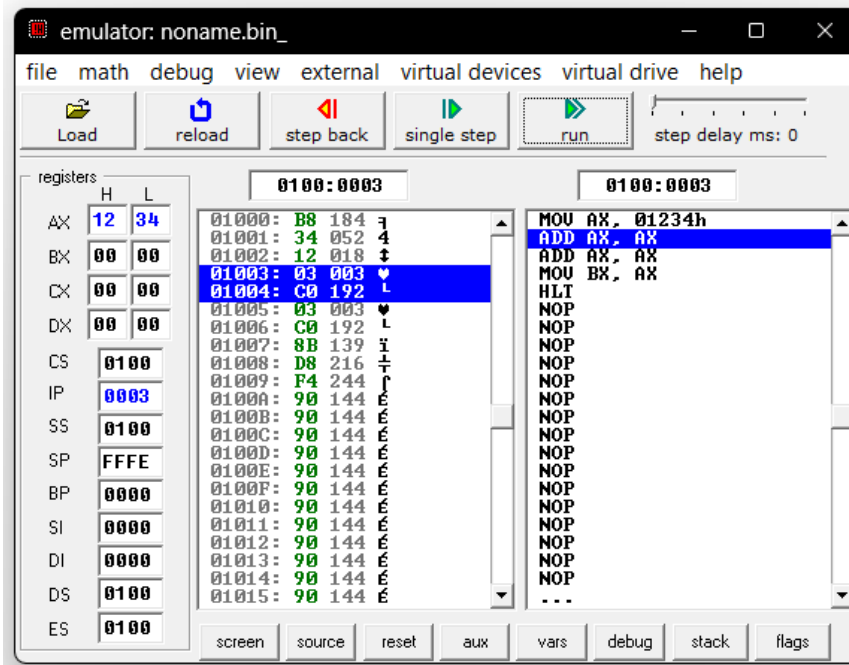
```

01
02 CODE SEGMENT
03 ASSUME CS:CODE,
04 MOV AX, 1234H ;
05 MOV BX, 0F12H ;
06 ADD AX, BX ; Add
07 SUB AX, 567H ; S
08 MOV CX, AX ; Move
09 HLT
10 CODE ENDS ; End
11 END
12
13

```

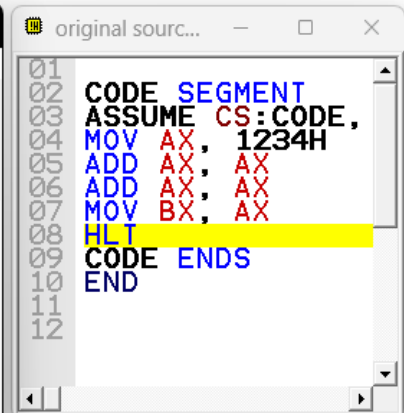
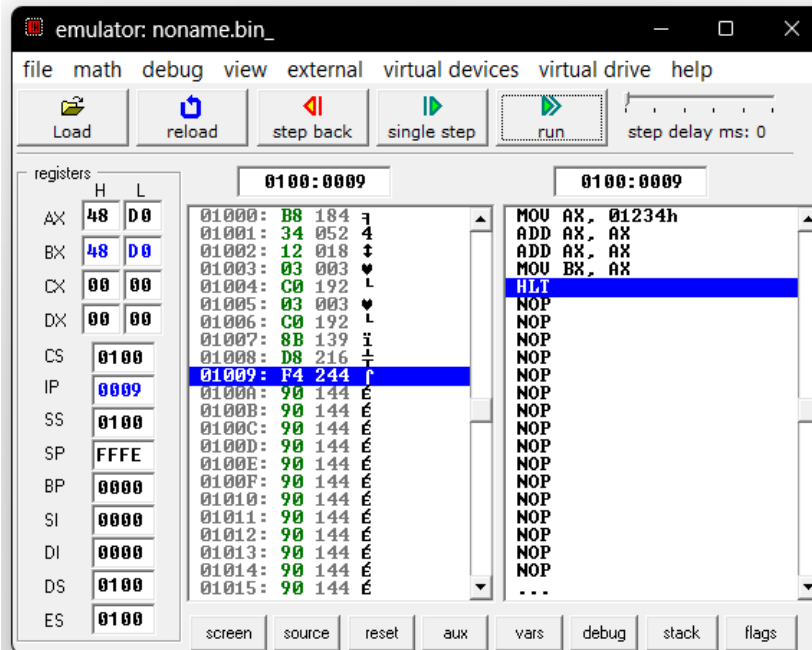
## Solution 2 :

AX = 1234H



AX = AX + AX + AX = 3AX = 48D0H

BX = AX = 48D0H



### Solution 3 :

AX = 1000H , BX = 2000H, CX = 3000H

emulator: noname.bin\_

file math debug view external virtual devices virtual drive help

Load reload step back single step run step delay ms: 0

registers

	H	L
AX	10	00
BX	20	00
CX	30	00
DX	00	00
CS	0100	
IP	0009	
SS	0100	
SP	FFFE	
BP	0000	
SI	0000	
DI	0000	
DS	0100	
ES	0100	

0100:0009

Address	Hex	Dec	Comment
01000: B8	184		
01001: 00	000		NULL
01002: 10	016		
01003: BB	187		
01004: 00	000		NULL
01005: 20	032		SPA
01006: B9	185		
01007: 00	000		NULL
01008: 30	048		
01009: 03	003		
0100A: D8	216		
0100B: 03	003		
0100C: CB	203		
0100D: 8B	139		
0100E: D1	209		
0100F: F4	244		
01010: 90	144		
01011: 90	144		
01012: 90	144		
01013: 90	144		
01014: 90	144		
01015: 90	144		

MOV AX, 01000h  
MOV BX, 02000h  
MOV CX, 03000h  
ADD BX, AX  
ADD CX, BX  
MOV DX, CX  
HLT  
CODE ENDS  
END

$BX = BX + AX = 3000H$

$CX = CX + BX = 6000H$

emulator: noname.bin\_

file math debug view external virtual devices virtual drive help

Load reload step back single step run step delay ms: 0

registers

	H	L
AX	10	00
BX	30	00
CX	60	00
DX	00	00
CS	0100	
IP	000D	
SS	0100	
SP	FFFE	
BP	0000	
SI	0000	
DI	0000	
DS	0100	
ES	0100	

0100:000D

Address	Hex	Dec	Comment
01000: B8	184		
01001: 00	000		NULL
01002: 10	016		
01003: BB	187		
01004: 00	000		NULL
01005: 20	032		SPA
01006: B9	185		
01007: 00	000		NULL
01008: 30	048		
01009: 03	003		
0100A: D8	216		
0100B: 03	003		
0100C: CB	203		
0100D: 8B	139		
0100E: D1	209		
0100F: F4	244		
01010: 90	144		
01011: 90	144		
01012: 90	144		
01013: 90	144		
01014: 90	144		
01015: 90	144		

MOV AX, 01000h  
MOV BX, 02000h  
MOV CX, 03000h  
ADD BX, AX  
ADD CX, BX  
MOV DX, CX  
HLT  
CODE ENDS  
END

**DX = CX**

