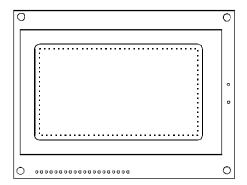


PRODUCT SPECIFICATION HDM64GS12

128 x 64 GRAPHICS LCD DISPLAY MODULE



HANTRONIX, INC.
10080 BUBB RD.
CUPERTINO, CA 95014

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MECHANICAL DATA

(1) Part Name (2) Module Size	9 9 1	HDM 64GS12 F 93.0(W)mm X 70.0(H)mm X MAX8.5(D)mm (W/0,EL B/L) 93.0(W)mm X 70.0(H)mm X MAX14.0(D)mm (5.2mm LED B/L ,LMC97X005X) 93.0(W)mm X 70.0(H)mm X MAX12.5(D)mm (4.0mm LED B/L ,LMC97X005X8)
(3) Dot Size(4) Dot Pitch(5) Number of Dots(6) Duty	6	0.48 (W)mm x 0.48 (H)mm 0.52 (W)mm x 0.52 (H)mm 128 (W) x 64 (H)Dots 1/64
(7) LCD Display Mode	FSTN: I	□ Gray Mode □ Yellow Mode □ Blue Mode □ Black and White(Normal White/Positive Image) □ Black and White(Normal Black/Negative Image) □ Reflective □ Transflective □ Transmissive
(8) Viewing Direction(9) Backlight(10) Weight	! !	□ 6 O'clock □ 12 O'clock □O'clock □ W/O □ EL B/L □ LED B/L W/O B/L: 53.0 g EL B/L: 57.5 g LED B/L: 78.5 g

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ELECTRICAL CHARACTERISTICS

 $(VDD = 5V\pm10\%)$

ITEM	SYMBOL	COND	CONDITION		TYP.	MAX.	UNIT	
lanut Valtaga	VIH	H level		0.7VDD		VDD	V	
Input Voltage	VIO	L le	evel	0	_	0.3VDD	٧	
Recommended		Duty=	0,0	-	13.3	13.8		
LC Driving Voltage (Normal Temp. LCM)	VDD-VO	Bias=	25℃	11.7	12.5	13.1	٧	
		1/9	50°C	10.8	11.4	_ :		
Recommended		Duty=			12.8			
LC Driving Voltage (Wide Temp. LCM)	VDD-VO	1/64 Bias= 1/9	25℃	11.2	12.0	13.0	V	
			70°C	10.4	11.1	_		
Power Supply Current	IDD	FLM=: VDD=: VDDVO: PATTER	5.0 V =12.5 V		10.0	_	m A	
EL Power Supply Current	i _{EL}	V _{BL} = 1 (R _{BL} =	10 Vrms 00 Hz 0 Ω)	_	_	5.0	mA _{rms}	
LED Power Supply Current (LMC97X005X)	LED	V _{BL} =		_	200		m A	
LED Power Supply Current (LMC97X005X5)	LED	1	10.0 V 10 Ω)		200	_		

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OPTICAL CHARACTERISTICS

Optical Char. of Normal Temp. Mode

AT VOP

	ITEM	Cr(Contra	st Ratio)	θ(Viewin	g Angle)	Ø(Viewir	ig Angle)
		25	5°C	25	2.C	2:	5°C
MODE		MIN.	TYP.	MIN.	TYP.	MIN.	TYP.
	A	3.5	4.5	50	75	20	30
R	С	6.0	9.0	60	85	20	35
	J	4.5	7.5	55	80	20	35
	А	3.0	4.2	50	75	20	30
s	С	5.0	8.0	55	85	20	35
	J	4.0	7.0	50	75	20	35
Т	Ė						
	G						
no	te	NOTE6			NO.	TE5	

AT Ø=0° θ=0°

iTEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
	+	0°C		600	1200		
Response Time (rise)	Tr	25°C	_	110	220	ms .	NOTE 2
		50°C	_	50	100		
7, (2,1)		0.0		900	1500		
Response Time (fall)	Tf	25°C	_	250	360	ms	NOTE 2
		50℃	-	100	150	•	

note:

R: REFLECTIVE

S: TRANSFLECTIVE T: TRANSMISSIVE

A: GRAY

C: YELLOW

E: BLUE

G: NORMALLY BLACK

J: NORMALLY WHITE

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Optical Char. of Wide Temp. Mode

AT VOP

ITEM		Cr(Contrast Ratio)		<i>⊕</i> (Viewind	g Angle)	Ø(Viewing Angle)	
		25°C		25°C		25℃	
MODE		MIN.	TYP.	MIN.	TYP.	MIN.	TYP.
	A	3.5	4.2	50	68	20	30
R	С	5.0		50		30	35
	J	6.0	8.0	50	70	20	38
	Α	3.5	4.0	50	65	20	30
S	С	5.0		50		25	35
	J	5.0	7.0	50		25	35
Т	Е						
	G						Ì
note		NOTE(3		NO	TE5	

AT Ø=0° θ=0°

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
		-10°C	_	600	1200		NOTE 2
Response Time (rise)	Tr	25°C	-	90	200	ms	
		70°C	_	40	100		
		-10°C	_	1200	2400		
Response Time (fall)	Ţf	25°C	_	180	360	ms	NOTE 2
		70°C	_	60	120		

note:

R: REFLECTIVE

S: TRANSFLECTIVE T: TRANSMISSIVE

A: GRAY

C: YELLOW

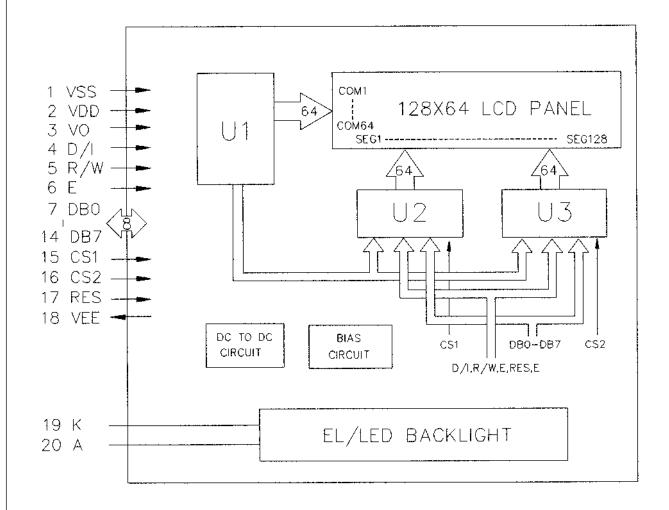
E: BLUE

G: NORMALLY BLACK

J: NORMALLY WHITE

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BLOCK DIAGRAM



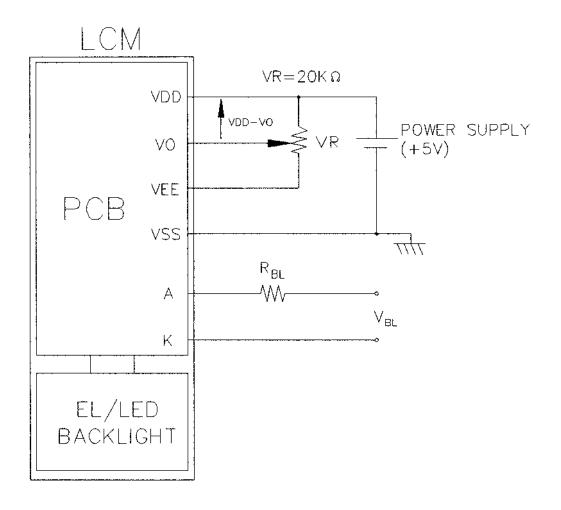
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INTERNAL PIN CONNECTION

Pin No.	Symbol	Level	Fu	nction			
1	Vss		0V	Power Supply			
2	V dd	-	+5V	rower suppry			
3	Vo		OPERATING DRIVING	VOLTAGE FOR LCD			
4	D/I	H/L	H: DATA INP L: INSTRUCTI	UT ON CODE INPUT			
5	R/W	H/L	H: DATA REA L: DATA WRI	AD (LCM TO MPU) TE (MPU TO LCM)			
6	E	H,H->	ENABLE SIG	NAL			
7	DB0	H/L					
8	DB1	H/L					
9	DB2	H/L					
10	DB3	H/L	DATA	BUS LINE			
11	DB4	H/L	DATE	UUU LINE			
12	DB5	H/L					
13	DB6	H/L					
14	DB7	H/L					
15	CS1	H	CHIP SELE	CT FOR IC1			
16	CS2		CHIP SELE	CT FOR IC2			
17	RES	Ĺ	RESET AC	TIVE "L"			
18	VEE		NEGATIVE	VOLTAGE OUTPUT			
19	K		CATHODE F	OR EL/LED BACKLIGHT			
20	Α	_	ANODE FOR	EL/LED BACKLIGHT			

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POWER SUPPLY



Recommended Value for R_{BL} and V_{BL}

Bock		R _{BL}			V _{BL}	
Light Interface	EL	LED		EŁ	LED	
19,20 PIN	0Ω	5Ω	10ព	110 Vac 400Hz	5Vpc	10Vpc

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TIMING CHARACTERISTICS

INTERFACE TIMING

- Item	Symbol	Test condition	Min.	Тур.	Max.	Unit
Enable cycle time	tcyc	Fig.a , Fig.b	1000		-	ns
E high level width	Pwen	Fig.a , Fig.b	450		-	ns
E low level width	Pwel	Fig.a , Fig.b	450		_	nş
E rise/fall time	tr.ti	Fig.a , Fig.b	-		25	ns
Address set up time	tas	Fig.a , Fig.b	140	_	<u> </u>	ns
Address hold time	lah	Fig.a , Fig.b	10			ns
Data delay time	todr	Fig. b	-	_	320	ns
Data set up time	tosw	Fig. a	200	-		ns
Data hold time (WR)	tonw	Fig. a	10		_	ns
Data hold time (RD)	tour :	Fig. b	20	_	-	ns

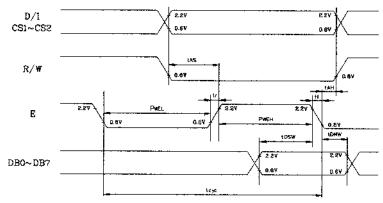


Fig . a Interface timing (data write)

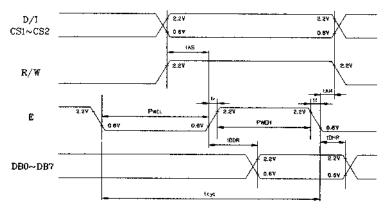
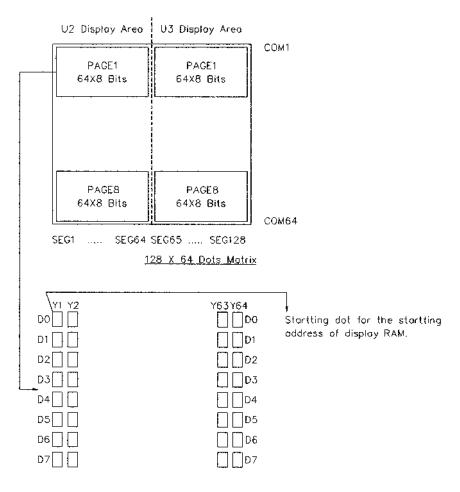


Fig . b Interface timing (data read)

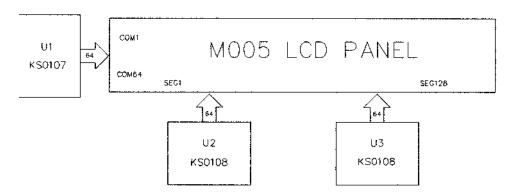
Fig. b Interface timing (data read)

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DISPLAY PATTERN



Each segment driver has 8 pages RAM , and each page has 64 x 8 bits RAM . D0~D7 are 8 bits transmitted data , where D0 is LSB and D7 is MSB .



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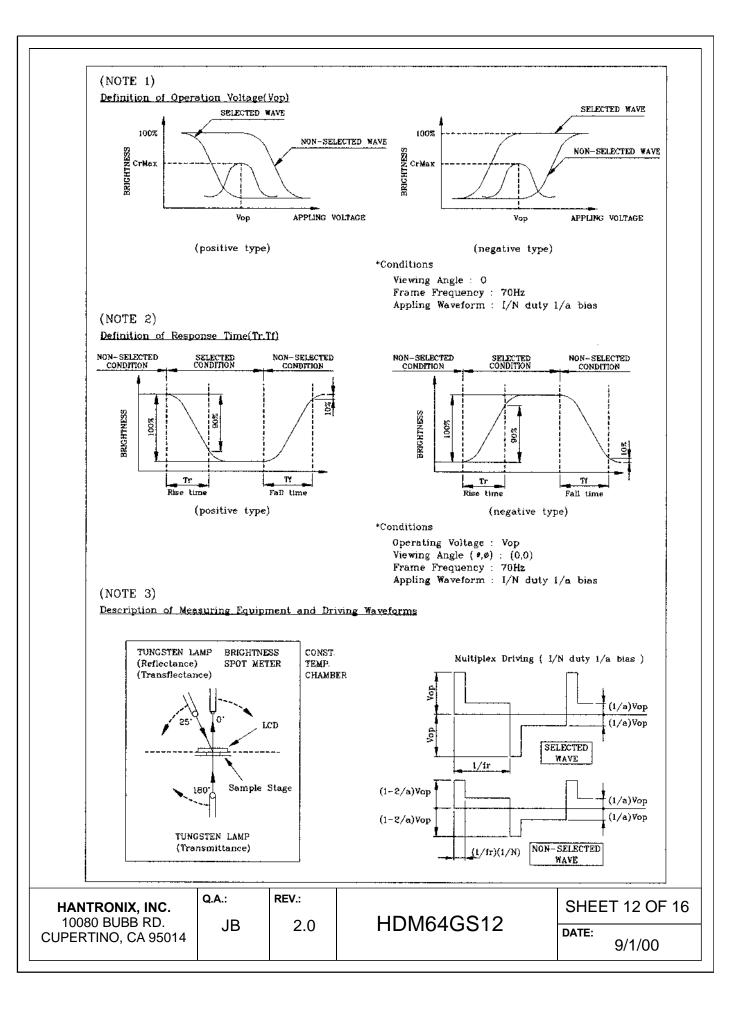
DATE:

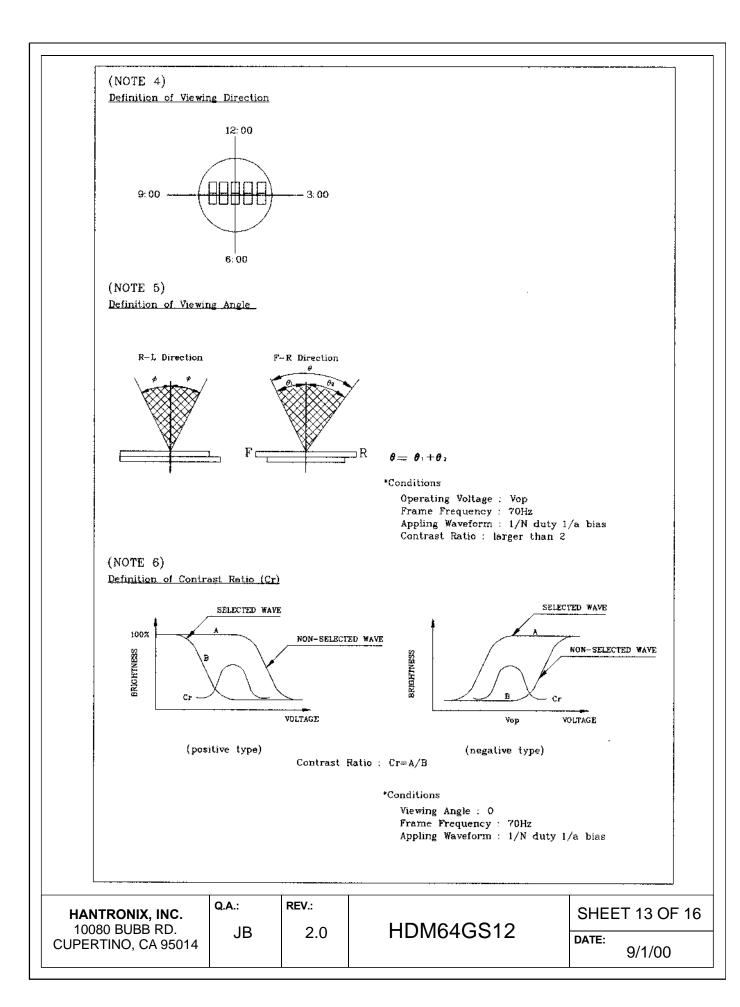
9/1/00

RELIABILITY TEST

NO	ITEM	CONDITION		STANDARD	NOTE	
1	High Temp. Leaving	70°C	120HR		Appearance without defect	
2	Low Temp. Leaving	-20°C	120HR		Appearance without defect	
3	High Temp. & High Humi. Leaving	40°C 90%RH	120HR		Appearance without defect	
4	Thermal Shock	-20°C,30min -25°C,5min -60°C,30min -25°C,5min (1cycle)		Appearance without defect	5 cycles	

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(2) NOTE:

SAFETY

- 1.If the LCD panel breaks, be careful not to get the liquid crystal to touch
- 2.If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

HANDLING

- 1. Avoid static electricity which can damage the CMOS ESI.
- 2.Do not remove the panel or frame from the module.
- 3. The polarizing plate of the display is very fragile. So, please handle it very carefully.
- 4.Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.Do not use ketonics solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.

STORAGE

- 1.Store the panel or module in a dark place where the temperature is 25°C±5°C and the humidity is below 65% RH.
- 2.Do not place the module near organics solvents or corrosive gases.
- 3.Do not crush, shake, or jolt the module.

· TERMS OF WARRANT

1.Acceptance inspection period

The period is within one month after the arrival of contracted commodity at the buyer's factory site.

2. Applicable warrant period

The period is within twelve months since the date of shipping out under normal using and storage conditions.

. THE OPERATING LIFE TIME OF BACK LIGHT

LED : 50,000HR EL: 5,000HR CCFT: 10,000HR

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