

Current state

State 0

State 1

State 2

State 3

State 4

State 5

State 6

State 7

State 8

State 9

State 10

مخطط البنية الداخلية للـ CPU

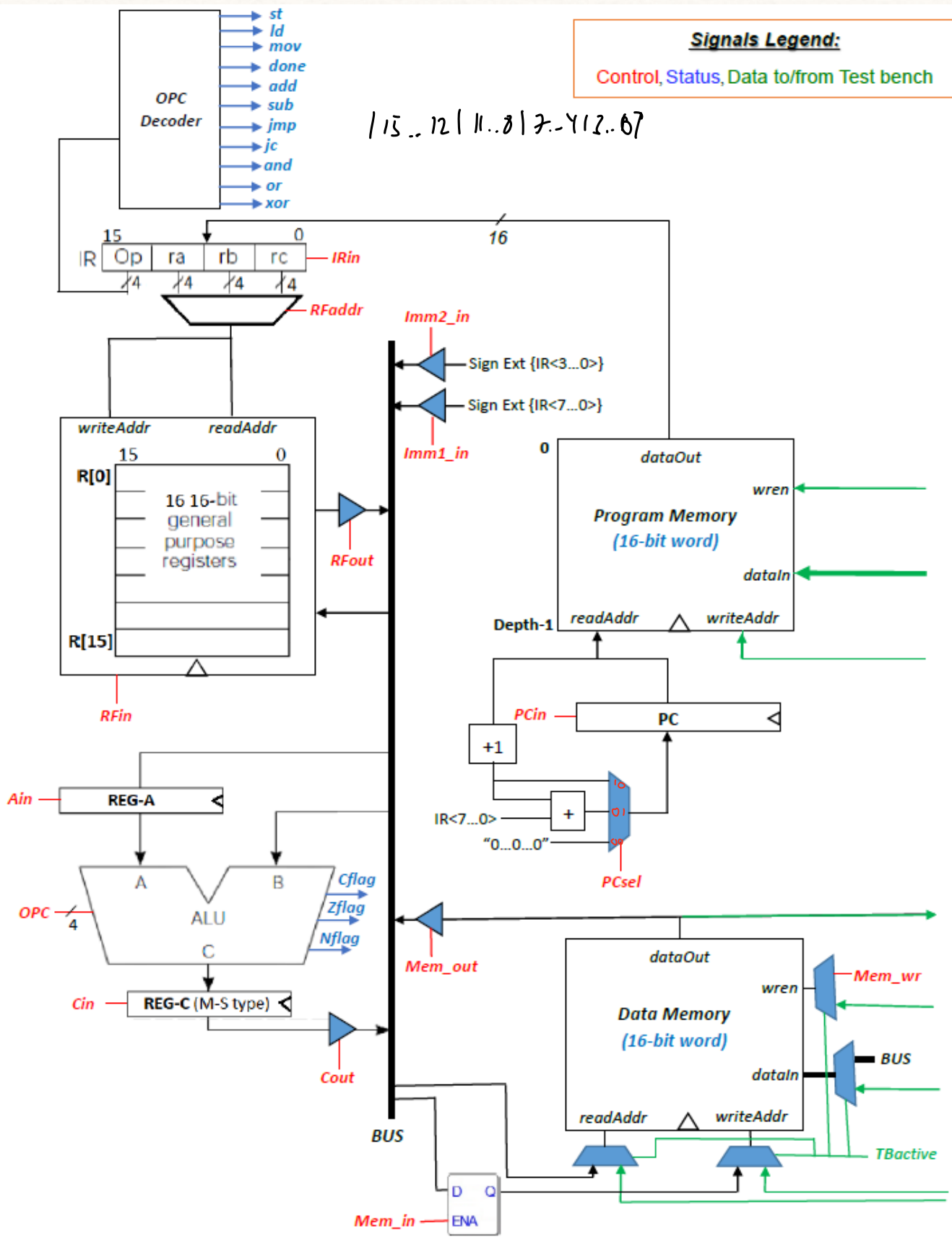
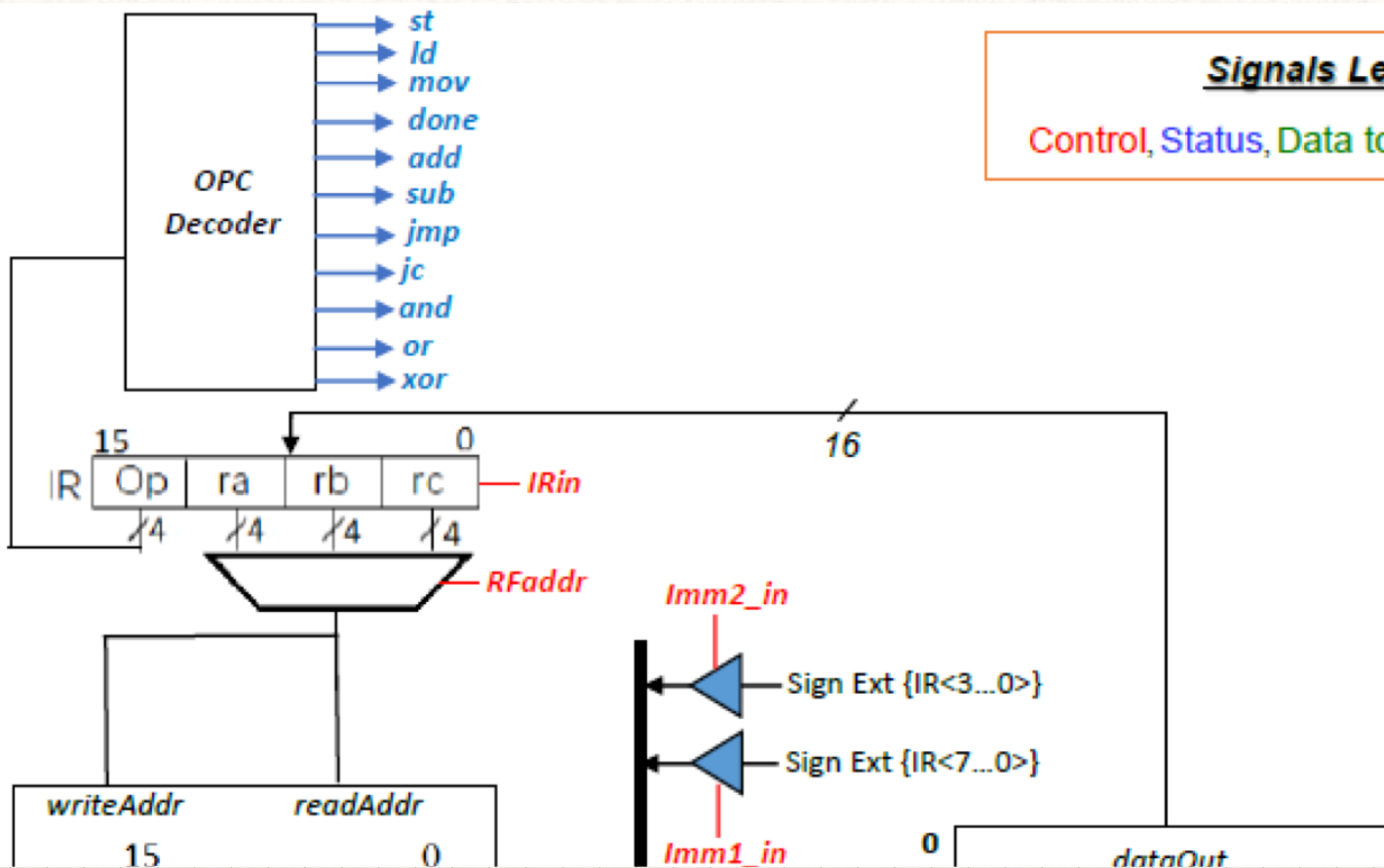


Figure 2: Datapath structure

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נוהל : ע. ארר





**Signals Legend:**  
 Control, Status, Data to/from Test bench

RFAddr :

00	$\sqrt{c}$	→
01	$\sqrt{c}$	→
10	$\sqrt{c}$	→