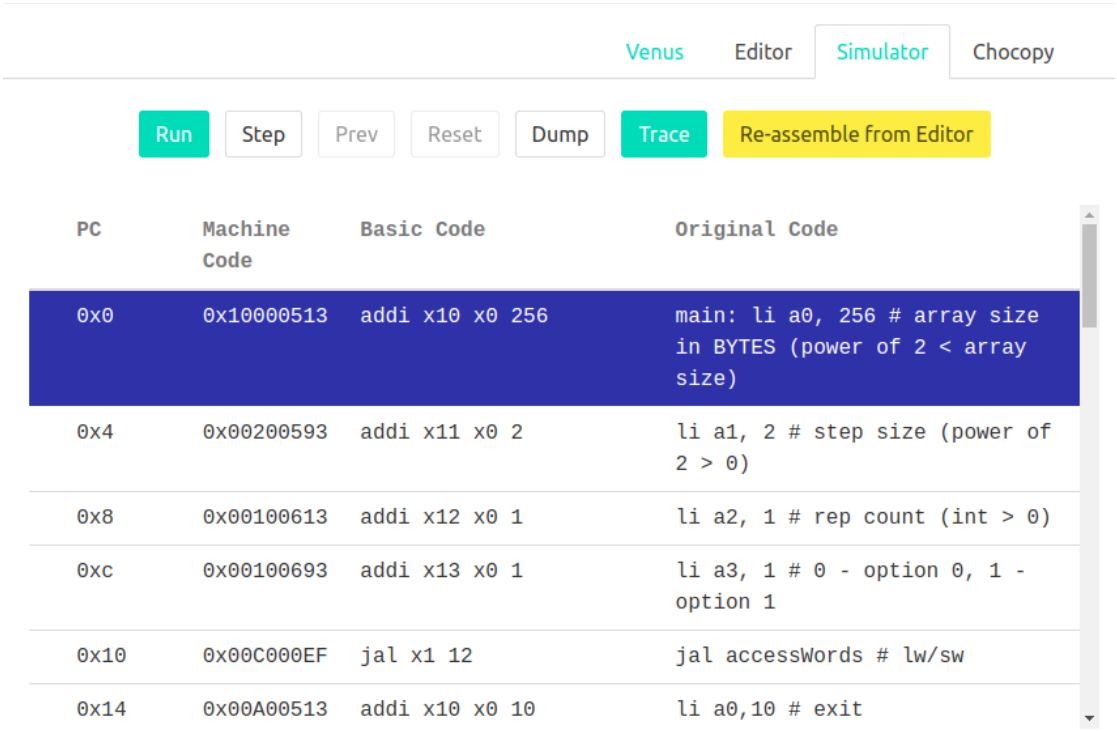
Task 1:

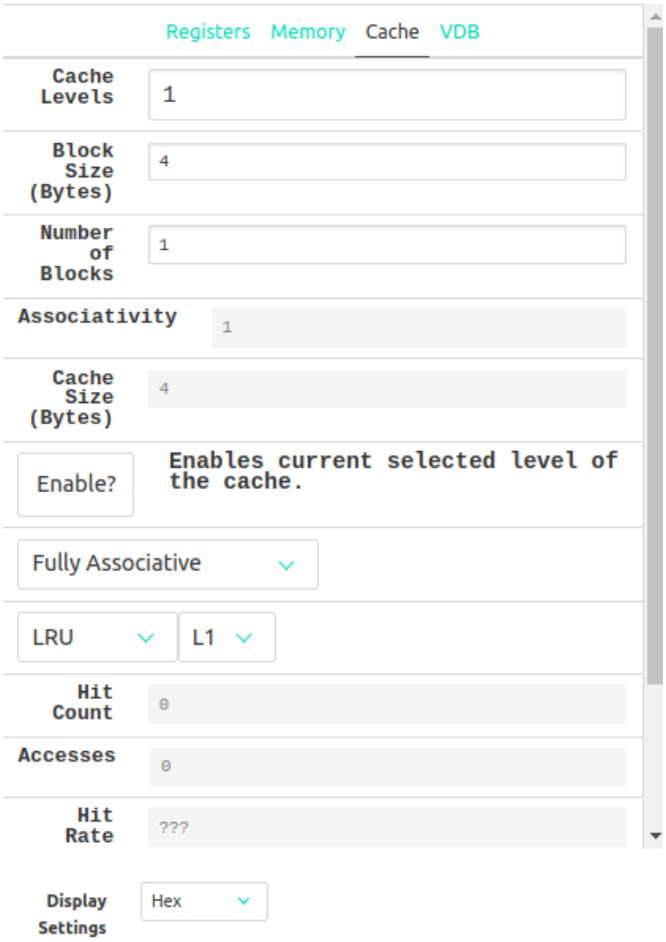
1): Venus Setup:

I have copied and pasted the provided code of cache.s into editor tab of venus. Then I assemble from the editor and a part of screenshot is shown below:



2): The output of running cache.s:

After running the status of registers and cache can be seen from the part of the screenshot below:



And the status of the registers are:


Registers
Memory
Cache
VDB
Integer (R)
Floating (F)
zero 0x00000000
ra (x1) 0x00000000
sp (x2) 0x7FFFFFDC
gp (x3) 0x10000000
tp (x4) 0x00000000
t0 (x5) 0x00000000
t1 (x6) 0x00000000
t2 (x7) 0x00000000
s0 (x8) 0x00000000
s1 (x9) 0x00000000
a0 (x10) 0x00000001
a1 (x11) 0x7FFFFFDC
a2 (x12) 0x00000000
a3 (x13) 0x00000000
a4 (x14) 0x00000000
Display Settings
Hex