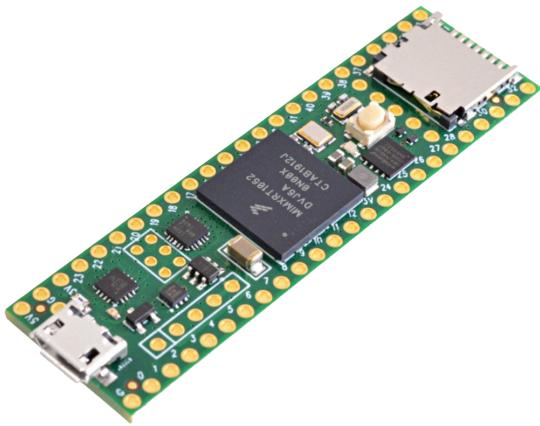


TEENSY 4.1

Development board



The Teensy 4.1 is a high-performance microcontroller, equipped with a 600 MHz ARM Cortex-M7 processor, which is characterized by its enormous computing power and versatility. This version has an integrated Ethernet port for reliable and fast network connections, ideal for IoT and industrial applications.

In addition, the lock function offers the option of permanently protecting the firmware against readout and unauthorized access, which makes the Teensy 4.1 particularly suitable for safety-critical projects and the protection of intellectual property.

For more information, you can visit the manufacturer's website:

<https://www.pjrc.com/store/teensy41.html>

MAIN FEATURES

| | |
|--------------|---|
| Processor | IMXRT1062DVJ6 Cortex-M7 |
| FPU | 32 & 64 bit |
| Rated Speed | 600 MHz |
| Flash memory | 7936 kB |
| Bandwidth | 66 MB/sec |
| Cache | 65536 B |
| RAM | 1024 kB |
| EEPROM | 1080 B (emu) |
| Ethernet | 10 / 100 Mbit DP83825 PHY |
| Security | Lock function for permanent firmware protection |

FURTHER SPECIFICATIONS

| | |
|------------------------|---|
| Supply voltage | 3.6 - 5.5 V |
| Logic Level | 3.3 V |
| Digital pins | 55 |
| Analog inputs | 18 |
| max current per IO pin | 10 mA |
| Communication | USB, Serial, SPI, I2C, CAN, Digital Audio IN/OUT, SD Card, Ethernet |

FURTHER INFORMATION

| | |
|-----------------------|---------------|
| MPN | TEENSY41_LOCK |
| Weight | 15 g |
| Article number | TEENSY41-L |
| EAN | 4250236829395 |
| Customs tariff number | 85437090990 |

TEENSY 4.1

Pinout

