

Ritankar Das

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📍 Kolkata, India

in Ritankar Das

Education

B.Tech in Electronics And Telecommunication, Indian 2021 – 2025
Institute of Engineering Science and Technology, Shibpur

CGPA-8.39 (Upto 5th Sem)

Senior Secondary Education (ISC), Salt Lake School 2018 – 2020
Percentage: 90.75%

Secondary Education (ICSE), Salt Lake School 2016 – 2018
Percentage: 93%

Experiences

UDGAM'23 IIT GUWAHATI

Participated in design hackathon D-FRAME held during UDGAM'23: The Annual Entrepreneurship Summit of IIT Guwahati, where I wireframed an engaging prototype of a website using figma.

TATA Hackathon 2023

Framed a solution on accurate head count in a public premises during emergency with the help of RFID Technology.

Projects

Verilog projects

I have used Verilog code to build a traffic signal controller, a 1011 sequence detector using Moore FSM, a Mod N counter and a 16-bit adder.

Projects

- Design of 3 bit arbitrary counter using J-K flip-flop
- Design of controlled up/down synchronous counter using J-K flip-flop

Web development projects

Contributed to the development of the official college departmental webpage using React js.

Skills

Verilog



Arduino



C/C++ programming



HTML, CSS



React, Node.js, Javascript

