```
Project Summary x N bit comparator v x N bit comp tb.v
/home/itzzinfinity/Cozy Drive/100daysofRTL/day 021/project 1/project 1.srcs/sources 1/new/N bit comparator.v
   `timescale 1ns / 1ps
// Engineer: Anian Prasad
   // Create Date: 10/12/2024 06:40:15 AM
   // Module Name: N bit comparator
   8
9
    `timescale 1ns / 1ps
10
11 🖨
   module N bit comparator(a, b, Lesser, Greater, Equal);
12
      parameter N = 8;
13
      input [N-1:0] a, b;
14
      output reg Lesser, Greater, Equal;
15
16 🖨
      always @(*) begin
17
         Lesser = (a < b);
18
         Greater = (a > b);
19
         Equal = (a == b);
20 🛆
      end
21
   endmodule
23
```

```
Project Summary x N bit comparator v x N bit comp tb.v
/home/itzzinfinity/Cozy Drive/100daysofRTL/day 021/project 1/project 1.srcs/sim 1/new/N bit comp tb.v
   `timescale 1ns / 1ps
// Engineer: Anjan Prasad
   // Create Date: 10/12/2024 06:40:15 AM
   // Module Name: N bit comparator tb
   module N bit comp tb;
10
   reg [7:0] a,b;
   wire Lesser, Greater, Equal;
    N bit comparator DUT (a,b,Lesser,Greater,Equal);
13
    initial begin
14
          $display("in between a and b
                                    A lessThan B A greaterThan B A_equalTo_B ");
15
          $monitor("
                                    %b
                                                            %b ", a, b,Lesser,Greater,Equal);
                         %d
                                %d
                                               %b
   repeat(10) begin
17
   #10
18
      a= $random %8 ;
         b= $random %8;
20
   end
   $finish;
23
   end
24
   endmodule
```

```
Tcl Console x Messages Log
# run 1000ns
  in between a and b
                         A lessThan B A greaterThan B A equalTo B
                     249
            249
                     251
                     250
                     254
                     250
  $finish called at time: 100 ns: File "/home/itzzinfinity/Cozy Drive/100daysofRTL/day 021/project_1/project_1.srcs/sim_1/new/N_bit_comp_tb.v" Line 36
  INFO: [USF-XSim-96] XSim completed. Design snapshot 'N bit comp tb behav' loaded.
  INFO: [USF-XSim-97] XSim simulation ran for 1000ns
🛆 launch simulation: Time (s): cpu = 00:00:09 ; elapsed = 00:00:06 . Memory (MB): peak = 9184.488 ; gain = 8.000 ; free physical = 205 ; free virtual = 1534
```

Type a Tel command here

