EP-Lab02-2021

Olaru Gabriel Iulian, 342C2



EX1.

Experiment 1:

Baseline:

disk		reac	Sy and take a loo	k at the code.	vvnat is the di	nerence permit	es inemods do	append() and	I0	
	total	merged (e)	sectors		total	merged	sectors			
loop0		Use vm0tat to r	monitor the reemory ι				In the main nethod, o			
loop1				0						
loop2		■ 0 he do	_ <i>append</i> m <mark>€0</mark> nod on i	t's own (se 0 Expe	eriment 1)0					
loop3		= 0 he do	_allocate m@hod on	it's own (s🕦 Expe	eriment 2)0					
loop4		■ 0 oth m	ethods as slown in t	he Experin 0 :nt 3 a	rea in th@code.					
Loop5		= 0oth m	ethods as slown in t	he <i>Experin</i> 0 nt 4 a	area in th⊕code.					
Loop6										
.oop7			etation for the obtain							
vme0n1	26836	sam15267 ^{per}	formi3802257 ^{ent}	3 or Exp22004 ^{1.4}		4239	345314	49654		29
da	10712	14102 ython	GC918336ce cycle	360640	24451	37335	1872048	520628		
.oop0										
.oop1		00 [00]	0							
.oop2		02. [40p] :	Swap space							
.oop3										
.oop4		-0								
Loop5		U	sefore starting this tas			your progess. If				
Loop6		O It	might prove tricky to	do so alte ₀ vards.						
Loop7										
vme0n1	26836	15267	3802257	22004		4239	345314	49654		29

disk		read	ds	8		wri	tes		I0	
	total	merged	sectors		total	merged	sectors		cur	sec
loop0										
loop1										
loop2				0	0	0	0	0		
loop3										
loop4										
loop5										
loop6				EX1. Experiment 1:						
loop7				Baseline						
nvme0n1	27072	15454	3858433	25009	7308	4298	347050	51684		29
sda	10725	14133	918832	361613	24802	37695	1889072	533567		186
loop0										
loop1										
loop2										
loop3					0					
loop4										
loop5										
loop6				0	0	0	0			
loop7				With pr@esses						
nvme0n1	27072	15454	3858433	25009		4298	347050	51684		29
sda	10725	14133	918832	361613	24806	37708	1889448	534515		186

Experiment 2:

Baseline:

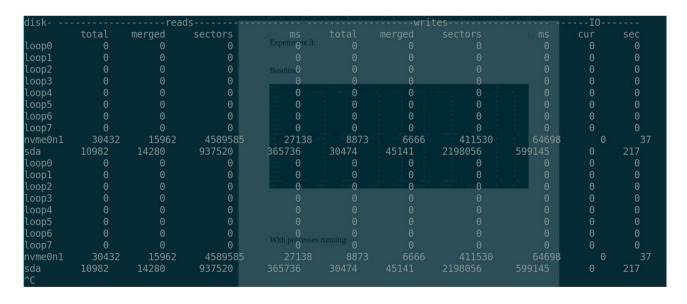
disk- EXL		reads reads			wri	tes		IO	
ex	^{©1.} total [©]	merged returnsectors	ms	total	merged	sectors	ms	cur	sec
loop0									0
loop1									0
loop2									0
loop3									0
loop4									0
loop5									0
loop6									0
loop7									0
nvme0n1	27155	15547 3862689	25418	7839	4559	363338	56575		32
sda	10894	14280 929800	363246	26630	39567	1957384	557087		198
loop0									0
loop1									0
loop2									0
loop3									0
loop4									0
loop5									0
loop6									0
loop7									0
nvme0n1	27155	15547 3862689	25418	7839	4559	363338	56575		32
sda	10894	14280 929800	363246	26630	39567	1957384	557087	0	198

isk		rea	ads			wri	tes		IO	
	total	merged	sectors	ms	total	merged	sectors	ms	cur	sec
oop0										
oop1										
oop2										
oop3										
oop4										
oop5										
oop6										
oop7										
vme0n1	30029	² 15933	Experi 4543905	26055	8268	4745	371450	59765		34
da	10911	14280	932544	363545	27713	40536	2049000	571526		208
oop0										
oop1										
oop2										
оор3										
oop4										
oop5										
op6										
oop7										
/me0n1	30029	4 15933	esult14543905nd(size) 26055	8271	4747	371490	59766		
da	10911	14280	esult 9 32544 ^{l locati}	363545	27713	40536	2049000	571526	0	208

Experiment 3:

Baseline:

disk- ·	X- 🖣 18801.BV 1U-	read	S sage	e object ad" a	-(-i,-)-*-100£	wri	tes		I0	
	xı total	merged [e	sectors	ms	total	merged	sectors		cur	sec
loop0										
loop1										
loop2										
loop3										
loop4										
loop5										
loop6										
loop7										
nvme0n1	1 30402	15962	4588633	26825	8735	6590	409034	63491		36
sda	10979	14280 #	937448	365671	29941	44482	2175232	592244		216
loop0										
loop1										
loop2										
loop3										
loop4										
loop5										
loop6										
loop7										
nvme0ni	1 30402	15962	4588633	26825	8735	6590	409034	63491		36
sda	10979	14280	937448	365671	29941	44482	2175232	592244	0	216



Experiment 4:

Baseline:

disk- EX1 -					wri	tes		I0	
0	¹ total "	merged returnsectors	ms	total	merged	sectors	ms	cur	sec
loop0									0
loop1									0
loop2									0
loop3									0
loop4									0
loop5									0
loop6									0
loop7									0
nvme0n1	30651	15962 4620657	29469	9270	9998	449274	66020		37
sda	11012	14307 938024	366221	31165	46076	2231464	602502		219
loop0									0
loop1									0
loop2									0
loop3									0
loop4									0
loop5									0
loop6									0
loop7									0
nvme0n1	30651	415962 4620657	29469	9270	9998	449274	66020		37
sda ^C	11012	14307 result1938024pend(siz	366221	31168	46118	2231824	602525	0	219

isk		read	S			wri	tes			IO	
	total	merged	sectors		total	merged	sectors			cur	sec
oop0				Baseline.							
oop1											
oop2				0	0	0	0				
oop3											
oop4											
oop5											
oop6											
oop7											
vme0n1	30750	15962	4623961	30144	9358	10055	450682		66471		38
da	11015	14307	938048	366222	31420	46393	2245584	6033	317		220
oop0											
oop1											
oop2				0	0	0	0				
оор3											
oop4											
oop5				With pr@esses							
oop6											
oop7											
vme0n1	30757	15962	4624049	30194	9358	10055	450682		66471		38
da C	11015	14307	938048	366222	31440	46435	2246312	6033	356		220

Task:

A reference cycle means one or more objects referencing each other. This, combined with the way the python garbage collector handles them(https://rushter.com/blog/python-garbage-collector/), is what causes the discrepancy in memory accesses between the two experiments. To prevent this, we can simply disable the python garbage collector with gc.disable().

Experiment 3:

disk- ^{y EXL} -		rea	dS- result			wri	tes		I0	
0	"total	merged	sectors	ms	total	merged	sectors	ms	cur	sec
loop0										
loop1										
loop2										
loop3			esult[i]= mossage ime.sleep(00001)							
loop4										
loop5										
loop6										
loop7										
nvme0n1	81845	34158	disab 6530025	42418	15004	291568	2776826	76287		57
sda	13199	20319	1131296	401751	36550	51297	2423552	641456		261
loop0										
loop1										0
loop2										
loop3										
loop4										0
loop5										
loop6										
loop7										
nvme0n1	81845	34158	6530025	42418	15004	291568	2776826	76287		57
sda	13199	20319	1131296	401751	36550	51297	2423552	641456		261

Experiment 4:

disk- ^{∨ ⊑⊻}		reads			wri	tes		I0	
0	"total"	merged sectors	ms	total	merged	sectors	ms	cur	sec
loop0		13 0 result=size*[NOne]							
loop1									
loop2									
loop3		16							
loop4									
loop5									
loop6									
loop7									
nvme0n1	82547	235589 disab6569233	42713	15207	291635	2779490	76684		57
sda	13230	20441 1133360	401896	36912	51707	2437896	642281		262
loop0									
loop1									
loop2									
loop3									
loop4									
loop5									
loop6									
loop7									
nvme0n1	82547	35589 6569233	42713	15207	291635	2779490	76684		57
sda ^C	13230	20441 # 1133360 # do append(size)	401896	36912	51707	2437896	642281	0	262

EX2:

Decrease in free memory alter python allocation:

Before and after closing python shell:

Task:

```
NAME TYPE SIZE USEDHYPRIO 1
/swapfile1 file 4G 0B 10
/swapfile2 file 4G $0Bree 20
```

sudo swapoff -a

sudo dd if=/dev/zero of=/swapfile bs=1024 count=\$((4 * 1024 * 1024)) sudo chmod 600 /swapfile sudo mkswap /swapfile sudo swapon /swapfile

swapon -p <pri>priority> /path/to/swap

Advantages:

- It is very easy to change the swap file size
- There is a slight performance benefit, because the swap area is in the middle of the normal file area, so head movements will be smaller Disatvantages:
- With the speed of SSDs fragmentation that can happen to the swapfile could be a thing of the pas
- requires configuration

EX3:

oro	CS		- 6 - 6	memory	' ₂₅₅₂	6579 6579	- 14507 SW	vap-⊸	i	Q -1923	-Syst	em - 🖏	gg		cpu-	
r		swpd		free	inact	657activ	e14483i	° 50	°bi	0 18bo	4611 in	4CS	us	sy		st
0				235 1	3537	6579 365	4 14450 0			0 171 0	2104	2451		1		0
0				235	3537	6579 365	4 14433 0			0 139 0	4506	4072		1	97	0
0				236 i	3537	6579 365	3 14396 0			0 1328	4911	3682		1		0
2				237	3537	6579 36 5	2 12437 0	0		0 185 0	5164	4687	2	1		0
1				237	3537	6579 365	212437 0			52 171 0	4911	4069		1	97	0
0				236	3537	6579 36 5	2 12434 0			168	5913	4841	2 °	1		0
1				228	3543	365	5 0	0	0	0	2360	4110	4	1	95	0
0				229	3540	365				108	1577	1920				0
0				228	3542	365		the	O.	20	2963	5096	imos by			0
0				230	3540	365	4 0			200	5760	4826			95	0
2				nel and the ac	ive memory returns	365					5264	4453				0
0				ever233t to m	ake use 3537 in y	our own ex365	5ents, re01		adjust0he		5158	4210	Wa2ng		97	0
0				en 233anning	to man3537es a	it once col365			harm toan	gc108s	1545	1781				0
^C			particu	lar system.												

EX4:

```
$ patch <u>src/minspect.cpp patches/Task-A.patch</u>
patching file src/minspect.cpp
```

```
and ebx, 0x20 | dev
test ebx, ebx
inz 0x55f935blcca8
"test eax" eax eax
iz 0x55f935blcca9 ex
test r12, r12 wind the
mov eax, 0xfffffff
mjz0 x55f935blccd8ad p
mjbpp rbx memory etc. Norm
wpop rbx memory etc. Norm
wpop rby now under the pret
pop r12
                                                                                                                                                                                           55f935b1cc8d
55f935b1cc8f
55f935b1cc91
                                                                                                                                                                                         55f935b1cc93
:55f935b1cc95
:55f935b1cc98
:55f935b1cc98
                                                                                                                                                                                           55f935b1cc9f<sub>de</sub>
55f935b1cca0<sub>e</sub>
                                                                                                                                                                                         55f935b1cca1
55f935b1cca3
                                                                                                                                                                                    Jz 0x55f935b13ccc
call 0x55f935b0a7f0
mov rdi, qword ptr [rip+0x2135ed]
call 0x55f935b1c70

ext: 2052b25spac:7ff779a0e7f0 -- sub rsi,
pxt: 1051b25spac:7ff779a0e7f0 -- sub rsi,
pxt: 1051b25spac:7ff779a0e7f4 -- jea rdx,
pxt: 1051b25spac:7ff779a0e804 -- lea rdx,
pxt: 1051b25spac:7ff779a0e804 -- lea rdx,
pxt: 1051b25spac:7ff779a0e806 -- sub rsi,
pxt: 1051b25spac:7ff779a0e806 -- sub rax,
pxt: 1051b25spac:7ff779a0e806 -- sub rax,
pxt: 1051b25spac:7ff779a0e811 -- sub rax,
pxt: 1051b25spac:7ff779a0e814 -- cmp rax,
pxt: 1051b25spac:7ff779a0e814 -- cmp rax,
pxt: 1051b25spac:7ff779a0e816 -- mov rdi,
pxt: 1051b25spac:7ff779a0e819 -- mov edx,
0x1
axt: 1051b25spac:7ff779a0e819 -- mov rdi,
pxt: 1051b25spac:7
:55f935b13cb4
:55f935b13cb6
:55f935b13ccc
                                                                                                                mov.edx, dword.ptr [rip+0x35ff4], 0x0 dec dword ptr [rip+0x36317d], 0x0 dec dword ptr [rip+0x363032]

mov.rax,agword.ptr[rip+0x3630fb], 0x0.arax,agword.ptr[fi]+0x3630fb], 0x0.arax,agword.ptr[fi]+0x18]

mov.rax, gword.ptr[fi]+0x10]

mov.eax,agword.ptr[fi]+0x10]

mov.eax,agword.ptr[fi]+0x10]

mov.eax,agword.ptr[fi]+0x10]
                                                                                                                  mov rax, qword ptr [rsp+0x28]
xor rax, qword ptr fs:[0x28]
pop rbx abulated each time and passed to
                                                                                                           ret
mov eax, dword ptr [rbx]
mov rdx, qword ptr [rbx+0x88]
mov r8, qword ptr fs:[0x10]
cmp qword ptr [rdx+0x8], r8
cmp dword ptr [rip+0x3725c0], 0x0
cmpxchg dword ptr [rdx], esi
mov rdx, qword ptr [rbx+0x88]
mov eax, dword ptr [rbx]
add dword ptr [rbx]
add dword ptr [rdx]
mov ebp, dword ptr [rdi]
cmp qword ptr [rdi]
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      00
92
00
                                                                                                           ret
test byte ptr [rbx+0x74], 0x20
mov rbp, qword ptr [rbx+0x88]
call qword ptr [rbp+0x88]
mov edi, dword ptr [rdi+0x70]
```

```
s./third_party/pin-3-13/pin_et_obj_intel64/minspect.so -- ls -l l>/dev/null
—ghostpants@ghostmachine in ~/bocuments/UPB_IV_C2_2020-2021/EP/lab02/ex4/minspect on EP-2021 * (origin/EP-2021)

This is only a minor addition. Namely, we want to add a command line option -i that can be used multiple times to specify multiple image names (e.g.: ls, libc.so.6, etc.) The tool must forego instrumentation for any instruction that is
```

EX5:

Feedback Performance Evaluation

Your response has been recorded.

Submit another response

This content is neither created nor endorsed by Google. Report Abuse - Terms of Service - Privacy Policy

Google Forms