# Static Call Graph for image .\Lab2.axf

#<CALLGRAPH># ARM Linker, 5060960: Last Updated: Wed Feb 03 16:35:08 2021

### Maximum Stack Usage = 28 bytes + Unknown(Functions without stacksize, Cycles, Untraceable Function Pointers)

### Call chain for Maximum Stack Depth:

\_\_rt\_entry\_main ⇒ main ⇒ UART\_OutSDec ⇒ UART\_OutUDec ⇒ UART\_OutUDec (Cycle)

### Functions with no stack information

* [Reset\_Handler](#gjdgxs)
* [DisableInterrupts](#49gfa85)
* [EnableInterrupts](#2olpkfy)
* [StartCritical](#13qzunr)
* [EndCritical](#3nqndbk)
* [WaitForInterrupt](#22vxnjd)
* [\_\_user\_initial\_stackheap](#i17xr6)

### Mutually Recursive functions

* [NMI\_Handler](#30j0zll)   ⇒   [NMI\_Handler](#30j0zll)
* [HardFault\_Handler](#1fob9te)   ⇒   [HardFault\_Handler](#1fob9te)
* [MemManage\_Handler](#3znysh7)   ⇒   [MemManage\_Handler](#3znysh7)
* [BusFault\_Handler](#2et92p0)   ⇒   [BusFault\_Handler](#2et92p0)
* [UsageFault\_Handler](#tyjcwt)   ⇒   [UsageFault\_Handler](#tyjcwt)
* [SVC\_Handler](#3dy6vkm)   ⇒   [SVC\_Handler](#3dy6vkm)
* [DebugMon\_Handler](#1t3h5sf)   ⇒   [DebugMon\_Handler](#1t3h5sf)
* [PendSV\_Handler](#4d34og8)   ⇒   [PendSV\_Handler](#4d34og8)
* [SysTick\_Handler](#2s8eyo1)   ⇒   [SysTick\_Handler](#2s8eyo1)
* [ADC0Seq0\_Handler](#17dp8vu)   ⇒   [ADC0Seq0\_Handler](#17dp8vu)

### [UART\_OutUDec](#4jpj0b3)   ⇒   [UART\_OutUDec](#4jpj0b3) Function Pointers

* + [ADC0Seq0\_Handler](#17dp8vu) from startup.o(RESET) referenced from startup.o(RESET)
  + [ADC0Seq1\_Handler](#3rdcrjn) from startup.o(RESET) referenced from startup.o(RESET)
  + [ADC0Seq2\_Handler](#26in1rg) from startup.o(RESET) referenced from startup.o(RESET)
  + [ADC0Seq3\_Handler](#lnxbz9) from startup.o(RESET) referenced from startup.o(RESET)
  + [ADC1Seq0\_Handler](#35nkun2) from startup.o(RESET) referenced from startup.o(RESET)
  + [ADC1Seq1\_Handler](#1ksv4uv) from startup.o(RESET) referenced from startup.o(RESET)
  + [ADC1Seq2\_Handler](#44sinio) from startup.o(RESET) referenced from startup.o(RESET)
  + [ADC1Seq3\_Handler](#2jxsxqh) from startup.o(RESET) referenced from startup.o(RESET)
  + [BusFault\_Handler](#2et92p0) from startup.o(RESET) referenced from startup.o(RESET)
  + [CAN0\_Handler](#z337ya) from startup.o(RESET) referenced from startup.o(RESET)
  + [CAN1\_Handler](#3j2qqm3) from startup.o(RESET) referenced from startup.o(RESET)
  + [CAN2\_Handler](#1y810tw) from startup.o(RESET) referenced from startup.o(RESET)
  + [Comp0\_Handler](#4i7ojhp) from startup.o(RESET) referenced from startup.o(RESET)
  + [Comp1\_Handler](#2xcytpi) from startup.o(RESET) referenced from startup.o(RESET)
  + [Comp2\_Handler](#1ci93xb) from startup.o(RESET) referenced from startup.o(RESET)
  + [DebugMon\_Handler](#1t3h5sf) from startup.o(RESET) referenced from startup.o(RESET)
  + [Ethernet\_Handler](#3whwml4) from startup.o(RESET) referenced from startup.o(RESET)
  + [ExtBus\_Handler](#2bn6wsx) from startup.o(RESET) referenced from startup.o(RESET)
  + [FPU\_Handler](#qsh70q) from startup.o(RESET) referenced from startup.o(RESET)
  + [Fan0\_Handler](#3as4poj) from startup.o(RESET) referenced from startup.o(RESET)
  + [FlashCtl\_Handler](#1pxezwc) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortA\_Handler](#49x2ik5) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortB\_Handler](#2p2csry) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortC\_Handler](#147n2zr) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortD\_Handler](#3o7alnk) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortE\_Handler](#23ckvvd) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortF\_Handler](#ihv636) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortG\_Handler](#32hioqz) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortH\_Handler](#1hmsyys) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortJ\_Handler](#41mghml) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortK\_Handler](#2grqrue) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortL\_Handler](#vx1227) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortM\_Handler](#3fwokq0) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortN\_Handler](#1v1yuxt) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortP1\_Handler](#4f1mdlm) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortP2\_Handler](#2u6wntf) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortP3\_Handler](#19c6y18) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortP4\_Handler](#3tbugp1) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortP5\_Handler](#28h4qwu) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortP6\_Handler](#nmf14n) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortP7\_Handler](#37m2jsg) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortP\_Handler](#1mrcu09) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortQ1\_Handler](#46r0co2) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortQ2\_Handler](#2lwamvv) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortQ3\_Handler](#111kx3o) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortQ4\_Handler](#3l18frh) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortQ5\_Handler](#206ipza) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortQ6\_Handler](#4k668n3) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortQ7\_Handler](#2zbgiuw) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortQ\_Handler](#1egqt2p) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortR\_Handler](#3ygebqi) from startup.o(RESET) referenced from startup.o(RESET)
  + [GPIOPortS\_Handler](#2dlolyb) from startup.o(RESET) referenced from startup.o(RESET)
  + [HardFault\_Handler](#1fob9te) from startup.o(RESET) referenced from startup.o(RESET)
  + [Hibernate\_Handler](#sqyw64) from startup.o(RESET) referenced from startup.o(RESET)
  + [I2C0\_Handler](#3cqmetx) from startup.o(RESET) referenced from startup.o(RESET)
  + [I2C1\_Handler](#1rvwp1q) from startup.o(RESET) referenced from startup.o(RESET)
  + [I2C2\_Handler](#4bvk7pj) from startup.o(RESET) referenced from startup.o(RESET)
  + [I2C3\_Handler](#2r0uhxc) from startup.o(RESET) referenced from startup.o(RESET)
  + [I2C4\_Handler](#1664s55) from startup.o(RESET) referenced from startup.o(RESET)
  + [I2C5\_Handler](#3q5sasy) from startup.o(RESET) referenced from startup.o(RESET)
  + [I2S0\_Handler](#25b2l0r) from startup.o(RESET) referenced from startup.o(RESET)
  + [LPC0\_Handler](#kgcv8k) from startup.o(RESET) referenced from startup.o(RESET)
  + [MemManage\_Handler](#3znysh7) from startup.o(RESET) referenced from startup.o(RESET)
  + [NMI\_Handler](#30j0zll) from startup.o(RESET) referenced from startup.o(RESET)
  + [PECI0\_Handler](#34g0dwd) from startup.o(RESET) referenced from startup.o(RESET)
  + [PWM0Fault\_Handler](#1jlao46) from startup.o(RESET) referenced from startup.o(RESET)
  + [PWM0Generator0\_Handler](#43ky6rz) from startup.o(RESET) referenced from startup.o(RESET)
  + [PWM0Generator1\_Handler](#2iq8gzs) from startup.o(RESET) referenced from startup.o(RESET)
  + [PWM0Generator2\_Handler](#xvir7l) from startup.o(RESET) referenced from startup.o(RESET)
  + [PWM0Generator3\_Handler](#3hv69ve) from startup.o(RESET) referenced from startup.o(RESET)
  + [PWM1Fault\_Handler](#1x0gk37) from startup.o(RESET) referenced from startup.o(RESET)
  + [PWM1Generator0\_Handler](#4h042r0) from startup.o(RESET) referenced from startup.o(RESET)
  + [PWM1Generator1\_Handler](#2w5ecyt) from startup.o(RESET) referenced from startup.o(RESET)
  + [PWM1Generator2\_Handler](#1baon6m) from startup.o(RESET) referenced from startup.o(RESET)
  + [PWM1Generator3\_Handler](#3vac5uf) from startup.o(RESET) referenced from startup.o(RESET)
  + [PendSV\_Handler](#4d34og8) from startup.o(RESET) referenced from startup.o(RESET)
  + [Quadrature0\_Handler](#2afmg28) from startup.o(RESET) referenced from startup.o(RESET)
  + [Quadrature1\_Handler](#pkwqa1) from startup.o(RESET) referenced from startup.o(RESET)
  + [Quadrature2\_Handler](#39kk8xu) from startup.o(RESET) referenced from startup.o(RESET)
  + [Reset\_Handler](#gjdgxs) from startup.o(RESET) referenced from startup.o(RESET)
  + [SSI0\_Handler](#1opuj5n) from startup.o(RESET) referenced from startup.o(RESET)
  + [SSI1\_Handler](#48pi1tg) from startup.o(RESET) referenced from startup.o(RESET)
  + [SSI2\_Handler](#2nusc19) from startup.o(RESET) referenced from startup.o(RESET)
  + [SSI3\_Handler](#1302m92) from startup.o(RESET) referenced from startup.o(RESET)
  + [SVC\_Handler](#3dy6vkm) from startup.o(RESET) referenced from startup.o(RESET)
  + [SysCtl\_Handler](#3mzq4wv) from startup.o(RESET) referenced from startup.o(RESET)
  + [SysTick\_Handler](#2s8eyo1) from startup.o(RESET) referenced from startup.o(RESET)
  + [Timer0A\_Handler](#2250f4o) from startup.o(RESET) referenced from startup.o(RESET)
  + [Timer0B\_Handler](#haapch) from startup.o(RESET) referenced from startup.o(RESET)
  + [Timer1A\_Handler](#319y80a) from startup.o(RESET) referenced from startup.o(RESET)
  + [Timer1B\_Handler](#1gf8i83) from startup.o(RESET) referenced from startup.o(RESET)
  + [Timer2A\_Handler](#40ew0vw) from startup.o(RESET) referenced from startup.o(RESET)
  + [Timer2B\_Handler](#2fk6b3p) from startup.o(RESET) referenced from startup.o(RESET)
  + [Timer3A\_Handler](#upglbi) from startup.o(RESET) referenced from startup.o(RESET)
  + [Timer3B\_Handler](#3ep43zb) from startup.o(RESET) referenced from startup.o(RESET)
  + [Timer4A\_Handler](#1tuee74) from startup.o(RESET) referenced from startup.o(RESET)
  + [Timer4B\_Handler](#4du1wux) from startup.o(RESET) referenced from startup.o(RESET)
  + [Timer5A\_Handler](#2szc72q) from startup.o(RESET) referenced from startup.o(RESET)
  + [Timer5B\_Handler](#184mhaj) from startup.o(RESET) referenced from startup.o(RESET)
  + [UART0\_Handler](#3s49zyc) from startup.o(RESET) referenced from startup.o(RESET)
  + [UART1\_Handler](#279ka65) from startup.o(RESET) referenced from startup.o(RESET)
  + [UART2\_Handler](#meukdy) from startup.o(RESET) referenced from startup.o(RESET)
  + [UART3\_Handler](#36ei31r) from startup.o(RESET) referenced from startup.o(RESET)
  + [UART4\_Handler](#1ljsd9k) from startup.o(RESET) referenced from startup.o(RESET)
  + [UART5\_Handler](#45jfvxd) from startup.o(RESET) referenced from startup.o(RESET)
  + [UART6\_Handler](#2koq656) from startup.o(RESET) referenced from startup.o(RESET)
  + [UART7\_Handler](#zu0gcz) from startup.o(RESET) referenced from startup.o(RESET)
  + [USB0\_Handler](#3jtnz0s) from startup.o(RESET) referenced from startup.o(RESET)
  + [UsageFault\_Handler](#tyjcwt) from startup.o(RESET) referenced from startup.o(RESET)
  + [WDT\_Handler](#1yyy98l) from startup.o(RESET) referenced from startup.o(RESET)
  + [WideTimer0A\_Handler](#4iylrwe) from startup.o(RESET) referenced from startup.o(RESET)
  + [WideTimer0B\_Handler](#2y3w247) from startup.o(RESET) referenced from startup.o(RESET)
  + [WideTimer1A\_Handler](#1d96cc0) from startup.o(RESET) referenced from startup.o(RESET)
  + [WideTimer1B\_Handler](#3x8tuzt) from startup.o(RESET) referenced from startup.o(RESET)
  + [WideTimer2A\_Handler](#2ce457m) from startup.o(RESET) referenced from startup.o(RESET)
  + [WideTimer2B\_Handler](#rjefff) from startup.o(RESET) referenced from startup.o(RESET)
  + [WideTimer3A\_Handler](#3bj1y38) from startup.o(RESET) referenced from startup.o(RESET)
  + [WideTimer3B\_Handler](#1qoc8b1) from startup.o(RESET) referenced from startup.o(RESET)
  + [WideTimer4A\_Handler](#4anzqyu) from startup.o(RESET) referenced from startup.o(RESET)
  + [WideTimer4B\_Handler](#2pta16n) from startup.o(RESET) referenced from startup.o(RESET)
  + [WideTimer5A\_Handler](#14ykbeg) from startup.o(RESET) referenced from startup.o(RESET)
  + [WideTimer5B\_Handler](#3oy7u29) from startup.o(RESET) referenced from startup.o(RESET)
  + [uDMA\_Error](#243i4a2) from startup.o(RESET) referenced from startup.o(RESET)
  + [uDMA\_Handler](#j8sehv) from startup.o(RESET) referenced from startup.o(RESET)

Global Symbols**Reset\_Handler** (Thumb, 0 bytes, Stack size unknown bytes, startup.o(RESET))  
  
[Calls]

* + [>>](#338fx5o)   \_\_main

**NMI\_Handler** (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))  
  
[Calls]

* + [>>](#30j0zll)   NMI\_Handler

[Called By]

* + [>>](#30j0zll)   NMI\_Handler

[Address Reference Count : 1]

* + startup.o(RESET)

**HardFault\_Handler** (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))  
  
[Calls]

* + [>>](#1fob9te)   HardFault\_Handler

[Called By]

* + [>>](#1fob9te)   HardFault\_Handler

[Address Reference Count : 1]

* + startup.o(RESET)

**MemManage\_Handler** (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))  
  
[Calls]

* + [>>](#3znysh7)   MemManage\_Handler

[Called By]

* + [>>](#3znysh7)   MemManage\_Handler

[Address Reference Count : 1]

* + startup.o(RESET)

**BusFault\_Handler** (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))  
  
[Calls]

* + [>>](#2et92p0)   BusFault\_Handler

[Called By]

* + [>>](#2et92p0)   BusFault\_Handler

[Address Reference Count : 1]

* + startup.o(RESET)

**UsageFault\_Handler** (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))  
  
[Calls]

* + [>>](#tyjcwt)   UsageFault\_Handler

[Called By]

* + [>>](#tyjcwt)   UsageFault\_Handler

[Address Reference Count : 1]

* + startup.o(RESET)

**SVC\_Handler** (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))  
  
[Calls]

* + [>>](#3dy6vkm)   SVC\_Handler

[Called By]

* + [>>](#3dy6vkm)   SVC\_Handler

[Address Reference Count : 1]

* + startup.o(RESET)

**DebugMon\_Handler** (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))  
  
[Calls]

* + [>>](#1t3h5sf)   DebugMon\_Handler

[Called By]

* + [>>](#1t3h5sf)   DebugMon\_Handler

[Address Reference Count : 1]

* + startup.o(RESET)

**PendSV\_Handler** (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))  
  
[Calls]

* + [>>](#4d34og8)   PendSV\_Handler

[Called By]

* + [>>](#4d34og8)   PendSV\_Handler

[Address Reference Count : 1]

* + startup.o(RESET)

**SysTick\_Handler** (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))  
  
[Calls]

* + [>>](#2s8eyo1)   SysTick\_Handler

[Called By]

* + [>>](#2s8eyo1)   SysTick\_Handler

[Address Reference Count : 1]

* + startup.o(RESET)

**ADC0Seq0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
  
[Calls]

* + [>>](#17dp8vu)   ADC0Seq0\_Handler

[Called By]

* + [>>](#17dp8vu)   ADC0Seq0\_Handler

[Address Reference Count : 1]

* + startup.o(RESET)

**ADC0Seq1\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**ADC0Seq2\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**ADC0Seq3\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**ADC1Seq0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**ADC1Seq1\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**ADC1Seq2\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**ADC1Seq3\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**CAN0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**CAN1\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**CAN2\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Comp0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Comp1\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Comp2\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Ethernet\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**ExtBus\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**FPU\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Fan0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**FlashCtl\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortA\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortB\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortC\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortD\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortE\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortF\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortG\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortH\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortJ\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortK\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortL\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortM\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortN\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortP1\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortP2\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortP3\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortP4\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortP5\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortP6\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortP7\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortP\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortQ1\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortQ2\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortQ3\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortQ4\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortQ5\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortQ6\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortQ7\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortQ\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortR\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**GPIOPortS\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Hibernate\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**I2C0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**I2C1\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**I2C2\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**I2C3\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**I2C4\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**I2C5\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**I2S0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**LPC0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**PECI0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**PWM0Fault\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**PWM0Generator0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**PWM0Generator1\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**PWM0Generator2\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**PWM0Generator3\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**PWM1Fault\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**PWM1Generator0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**PWM1Generator1\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**PWM1Generator2\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**PWM1Generator3\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Quadrature0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Quadrature1\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Quadrature2\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**SSI0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**SSI1\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**SSI2\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**SSI3\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**SysCtl\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Timer0A\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Timer0B\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Timer1A\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Timer1B\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Timer2A\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Timer2B\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Timer3A\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Timer3B\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Timer4A\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Timer4B\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Timer5A\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**Timer5B\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**UART0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**UART1\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**UART2\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**UART3\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**UART4\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**UART5\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**UART6\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**UART7\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**USB0\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**WDT\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**WideTimer0A\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**WideTimer0B\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**WideTimer1A\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**WideTimer1B\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**WideTimer2A\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**WideTimer2B\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**WideTimer3A\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**WideTimer3B\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**WideTimer4A\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**WideTimer4B\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**WideTimer5A\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**WideTimer5B\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**uDMA\_Error** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**uDMA\_Handler** (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))  
[Address Reference Count : 1]

* + startup.o(RESET)

**\_\_main** (Thumb, 8 bytes, Stack size 0 bytes, \_\_main.o(!!!main))  
  
[Calls]

* + [>>](#1idq7dh)   \_\_scatterload
  + [>>](#ymfzma)   \_\_rt\_entry

[Called By]

* + [>>](#gjdgxs)   Reset\_Handler

**\_\_scatterload** (Thumb, 0 bytes, Stack size unknown bytes, \_\_scatter.o(!!!scatter))  
  
[Called By]

* + [>>](#338fx5o)   \_\_main

**\_\_scatterload\_rt2** (Thumb, 44 bytes, Stack size unknown bytes, \_\_scatter.o(!!!scatter), UNUSED)  
  
[Calls]

* + [>>](#ymfzma)   \_\_rt\_entry

**\_\_scatterload\_rt2\_thumb\_only** (Thumb, 0 bytes, Stack size unknown bytes, \_\_scatter.o(!!!scatter), UNUSED)**\_\_scatterload\_null** (Thumb, 0 bytes, Stack size unknown bytes, \_\_scatter.o(!!!scatter), UNUSED)**\_\_scatterload\_copy** (Thumb, 26 bytes, Stack size unknown bytes, \_\_scatter\_copy.o(!!handler\_copy), UNUSED)  
  
[Calls]

* + [>>](#3gnlt4p)   \_\_scatterload\_copy

[Called By]

* + [>>](#3gnlt4p)   \_\_scatterload\_copy

**\_\_scatterload\_zeroinit** (Thumb, 28 bytes, Stack size unknown bytes, \_\_scatter\_zi.o(!!handler\_zi), UNUSED)**\_\_rt\_lib\_init** (Thumb, 0 bytes, Stack size unknown bytes, libinit.o(.ARM.Collect$$libinit$$00000000))  
  
[Called By]

* + [>>](#4hr1b5p)   \_\_rt\_entry\_li

**\_\_rt\_lib\_init\_alloca\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000002E))**\_\_rt\_lib\_init\_argv\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000002C))**\_\_rt\_lib\_init\_atexit\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000001B))**\_\_rt\_lib\_init\_clock\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000021))**\_\_rt\_lib\_init\_cpp\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000032))**\_\_rt\_lib\_init\_exceptions\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000030))**\_\_rt\_lib\_init\_fp\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000002))**\_\_rt\_lib\_init\_fp\_trap\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000001F))**\_\_rt\_lib\_init\_getenv\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000023))**\_\_rt\_lib\_init\_heap\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000000A))**\_\_rt\_lib\_init\_lc\_collate\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000011))**\_\_rt\_lib\_init\_lc\_ctype\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000013))**\_\_rt\_lib\_init\_lc\_monetary\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000015))**\_\_rt\_lib\_init\_lc\_numeric\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000017))**\_\_rt\_lib\_init\_lc\_time\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000019))**\_\_rt\_lib\_init\_preinit\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000004))**\_\_rt\_lib\_init\_rand\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000000E))**\_\_rt\_lib\_init\_return** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000033))**\_\_rt\_lib\_init\_signal\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000001D))**\_\_rt\_lib\_init\_stdio\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000025))**\_\_rt\_lib\_init\_user\_alloc\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000000C))**\_\_rt\_lib\_shutdown** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown.o(.ARM.Collect$$libshutdown$$00000000))  
  
[Called By]

* + [>>](#qbtyoq)   \_\_rt\_exit\_ls

**\_\_rt\_lib\_shutdown\_cpp\_1** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$00000004))**\_\_rt\_lib\_shutdown\_fini\_1** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$00000002))**\_\_rt\_lib\_shutdown\_fp\_trap\_1** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$00000009))**\_\_rt\_lib\_shutdown\_heap\_1** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$00000011))**\_\_rt\_lib\_shutdown\_return** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$00000012))**\_\_rt\_lib\_shutdown\_signal\_1** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$0000000C))**\_\_rt\_lib\_shutdown\_stdio\_1** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$00000006))**\_\_rt\_lib\_shutdown\_user\_alloc\_1** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$0000000E))**\_\_rt\_entry** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry.o(.ARM.Collect$$rtentry$$00000000))  
  
[Called By]

* + [>>](#42ddq1a)   \_\_scatterload\_rt2
  + [>>](#338fx5o)   \_\_main

**\_\_rt\_entry\_presh\_1** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry2.o(.ARM.Collect$$rtentry$$00000002))**\_\_rt\_entry\_sh** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry4.o(.ARM.Collect$$rtentry$$00000004))  
  
[Stack]

* + Max Depth = 8 + Unknown Stack Size
  + Call Chain = \_\_rt\_entry\_sh ⇒ \_\_user\_setup\_stackheap

[Calls]

* + [>>](#2gb3jie)   \_\_user\_setup\_stackheap

**\_\_rt\_entry\_li** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry2.o(.ARM.Collect$$rtentry$$0000000A))  
  
[Calls]

* + [>>](#4fsjm0b)   \_\_rt\_lib\_init

**\_\_rt\_entry\_postsh\_1** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry2.o(.ARM.Collect$$rtentry$$00000009))**\_\_rt\_entry\_main** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry2.o(.ARM.Collect$$rtentry$$0000000D))  
  
[Stack]

* + Max Depth = 28 + Unknown Stack Size
  + Call Chain = \_\_rt\_entry\_main ⇒ main ⇒ UART\_OutSDec ⇒ UART\_OutUDec ⇒ UART\_OutUDec (Cycle)

[Calls]

* + [>>](#vgdtq7)   exit
  + [>>](#2yutaiw)   main

**\_\_rt\_entry\_postli\_1** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry2.o(.ARM.Collect$$rtentry$$0000000C))**\_\_rt\_exit** (Thumb, 0 bytes, Stack size unknown bytes, rtexit.o(.ARM.Collect$$rtexit$$00000000))  
  
[Called By]

* + [>>](#vgdtq7)   exit

**\_\_rt\_exit\_ls** (Thumb, 0 bytes, Stack size unknown bytes, rtexit2.o(.ARM.Collect$$rtexit$$00000003))  
  
[Calls]

* + [>>](#2rrrqc1)   \_\_rt\_lib\_shutdown

**\_\_rt\_exit\_prels\_1** (Thumb, 0 bytes, Stack size unknown bytes, rtexit2.o(.ARM.Collect$$rtexit$$00000002))**\_\_rt\_exit\_exit** (Thumb, 0 bytes, Stack size unknown bytes, rtexit2.o(.ARM.Collect$$rtexit$$00000004))  
  
[Calls]

* + [>>](#2tq9fhf)   \_sys\_exit

**DisableInterrupts** (Thumb, 0 bytes, Stack size unknown bytes, startup.o(.text), UNUSED)**EnableInterrupts** (Thumb, 0 bytes, Stack size unknown bytes, startup.o(.text), UNUSED)**StartCritical** (Thumb, 0 bytes, Stack size unknown bytes, startup.o(.text), UNUSED)**EndCritical** (Thumb, 0 bytes, Stack size unknown bytes, startup.o(.text), UNUSED)**WaitForInterrupt** (Thumb, 0 bytes, Stack size unknown bytes, startup.o(.text), UNUSED)**\_\_user\_initial\_stackheap** (Thumb, 0 bytes, Stack size unknown bytes, startup.o(.text))  
  
[Called By]

* + [>>](#2gb3jie)   \_\_user\_setup\_stackheap

**\_\_use\_two\_region\_memory** (Thumb, 2 bytes, Stack size 0 bytes, heapauxi.o(.text), UNUSED)**\_\_rt\_heap\_escrow$2region** (Thumb, 2 bytes, Stack size 0 bytes, heapauxi.o(.text), UNUSED)**\_\_rt\_heap\_expand$2region** (Thumb, 2 bytes, Stack size 0 bytes, heapauxi.o(.text), UNUSED)**\_\_user\_setup\_stackheap** (Thumb, 74 bytes, Stack size 8 bytes, sys\_stackheap\_outer.o(.text))  
  
[Stack]

* + Max Depth = 8 + Unknown Stack Size
  + Call Chain = \_\_user\_setup\_stackheap

[Calls]

* + [>>](#1ulbmlt)   \_\_user\_perproc\_libspace
  + [>>](#i17xr6)   \_\_user\_initial\_stackheap

[Called By]

* + [>>](#1xrdshw)   \_\_rt\_entry\_sh

**exit** (Thumb, 18 bytes, Stack size 8 bytes, exit.o(.text))  
  
[Stack]

* + Max Depth = 8 + Unknown Stack Size
  + Call Chain = exit

[Calls]

* + [>>](#2b6jogx)   \_\_rt\_exit

[Called By]

* + [>>](#1c1lvlb)   \_\_rt\_entry\_main

**\_\_user\_libspace** (Thumb, 8 bytes, Stack size 0 bytes, libspace.o(.text), UNUSED)**\_\_user\_perproc\_libspace** (Thumb, 0 bytes, Stack size 0 bytes, libspace.o(.text))  
  
[Called By]

* + [>>](#2gb3jie)   \_\_user\_setup\_stackheap

**\_\_user\_perthread\_libspace** (Thumb, 0 bytes, Stack size 0 bytes, libspace.o(.text), UNUSED)**\_sys\_exit** (Thumb, 8 bytes, Stack size 0 bytes, sys\_exit.o(.text))  
  
[Called By]

* + [>>](#1pgrrkc)   \_\_rt\_exit\_exit

**\_\_I$use$semihosting** (Thumb, 0 bytes, Stack size 0 bytes, use\_no\_semi.o(.text), UNUSED)**\_\_use\_no\_semihosting\_swi** (Thumb, 2 bytes, Stack size 0 bytes, use\_no\_semi.o(.text), UNUSED)**Average** (Thumb, 6 bytes, Stack size 0 bytes, lab2.o(i.Average))  
  
[Called By]

* + [>>](#2yutaiw)   main

**\_\_semihosting\_library\_function** (Thumb, 0 bytes, Stack size 0 bytes, indicate\_semi.o(.text), UNUSED)**FtoC** (Thumb, 6 bytes, Stack size 0 bytes, lab2.o(i.FtoC))  
  
[Called By]

* + [>>](#2yutaiw)   main

**IsMonotonic** (Thumb, 6 bytes, Stack size 0 bytes, lab2.o(i.IsMonotonic))  
  
[Called By]

* + [>>](#2yutaiw)   main

**PLL\_Init** (Thumb, 120 bytes, Stack size 0 bytes, pll.o(i.PLL\_Init))  
  
[Called By]

* + [>>](#2yutaiw)   main

**UART\_Init** (Thumb, 136 bytes, Stack size 0 bytes, uart.o(i.UART\_Init))  
  
[Called By]

* + [>>](#2yutaiw)   main

**UART\_OutChar** (Thumb, 20 bytes, Stack size 0 bytes, uart.o(i.UART\_OutChar))  
  
[Called By]

* + [>>](#4jpj0b3)   UART\_OutUDec
  + [>>](#1zpvhna)   UART\_OutString
  + [>>](#3kkl7fh)   UART\_OutSDec
  + [>>](#2yutaiw)   main

**UART\_OutSDec** (Thumb, 24 bytes, Stack size 8 bytes, uart.o(i.UART\_OutSDec))  
  
[Stack]

* + Max Depth = 20
  + Call Chain = UART\_OutSDec ⇒ UART\_OutUDec ⇒ UART\_OutUDec (Cycle)

[Calls]

* + [>>](#4jpj0b3)   UART\_OutUDec
  + [>>](#10kxoro)   UART\_OutChar

[Called By]

* + [>>](#2yutaiw)   main

**UART\_OutString** (Thumb, 24 bytes, Stack size 4 bytes, uart.o(i.UART\_OutString))  
  
[Stack]

* + Max Depth = 4
  + Call Chain = UART\_OutString

[Calls]

* + [>>](#10kxoro)   UART\_OutChar

[Called By]

* + [>>](#2yutaiw)   main

**UART\_OutUDec** (Thumb, 42 bytes, Stack size 12 bytes, uart.o(i.UART\_OutUDec))  
  
[Stack]

* + Max Depth = 12 + In Cycle
  + Call Chain = UART\_OutUDec ⇒ UART\_OutUDec (Cycle)

[Calls]

* + [>>](#4jpj0b3)   UART\_OutUDec
  + [>>](#10kxoro)   UART\_OutChar

[Called By]

* + [>>](#4jpj0b3)   UART\_OutUDec
  + [>>](#3kkl7fh)   UART\_OutSDec
  + [>>](#2yutaiw)   main

**main** (Thumb, 736 bytes, Stack size 8 bytes, main.o(i.main))  
  
[Stack]

* + Max Depth = 28
  + Call Chain = main ⇒ UART\_OutSDec ⇒ UART\_OutUDec ⇒ UART\_OutUDec (Cycle)

[Calls]

* + [>>](#4jpj0b3)   UART\_OutUDec
  + [>>](#1zpvhna)   UART\_OutString
  + [>>](#3kkl7fh)   UART\_OutSDec
  + [>>](#10kxoro)   UART\_OutChar
  + [>>](#2lfnejv)   UART\_Init
  + [>>](#46ad4c2)   PLL\_Init
  + [>>](#1maplo9)   IsMonotonic
  + [>>](#375fbgg)   FtoC
  + [>>](#280hiku)   Average

[Called By]

* + [>>](#1c1lvlb)   \_\_rt\_entry\_main

Local SymbolsUndefined Global Symbols