

## **Department of Computer Engineering**

**Academic Term : Jan-May 23-24**

**Class : T.E. (Computer)**

**Subject Name : System Programming and Compiler Construction**

**Subject Code : (CPC601)**

<b>Practical No:</b>	6
<b>Title:</b>	Target Code Generator
<b>Date of Performance:</b>	
<b>Date of Submission:</b>	
<b>Roll No:</b>	9601
<b>Name of the Student:</b>	Ivan Dsouza

### **Evaluation:**

<b>Sr. No</b>	<b>Rubric</b>	<b>Grade</b>
<b>1</b>	<b>Time Line (2)</b>	
<b>2</b>	<b>Output(3)</b>	
<b>3</b>	<b>Code optimization (2)</b>	
<b>4</b>	<b>Postlab (3)</b>	

**Signature of the Teacher :**

## Experiment No 6

**Aim :** Generate a target code for the optimized code.

### **Algorithm:**

The final phase in compiler model is the code generator. It takes as input an intermediate representation of the source program and produces as output an equivalent target program.

The code generation algorithm takes as input a sequence of three address statements constituting a basic block. For each three address statement of the form  $x=y \text{ op } z$  we perform following function:

1. Invoke a function `getreg` to determine the location  $L$  where the result of computation  $y \text{ op } z$  should be stored. ( $L$  can be a register or memory location).
2. Consult the address descriptor for  $y$  to determine  $y$ , the current locations of  $y$ . Prefer the register for  $y$  if the value of  $y$  is currently both in memory and register. If value of  $y$  is not already in  $L$ , generate the instruction `MOV y, L` to place a copy of  $y$  in  $L$ .
3. Generate instruction `po z, L` where  $z$  is a current location of  $z$ . Again address descriptor of  $x$  to indicate that  $x$  is in location  $L$ . if  $L$  is a register, update its descriptor to indicate that it contains the value of  $x$ , and remove  $x$  from all other register descriptor.
4. If the current values of  $y$  and  $z$  have no next uses, are not live on exit from the block, and are in registers alter the register descriptor to indicate that, after execution of  $x=y \text{ op } z$ , those register no longer will contain  $y$  and  $z$ , respily.

### **The function `getreg`:**

The function `getreg` returns the location  $L$  to hold the values of  $x$  for the assignment  $x= y \text{ op } z$ .

- 1.If the name  $y$  is in a reg that holds the value of no other names, and  $y$  is not live and has no next use after execution of  $x= y \text{ op } z$ , then return the register of  $y$  for  $L$ . Update the address descriptor of  $y$  to indicate that  $y$  is no longer in  $L$ .
2. Failing (1), return an empty register for  $L$  if there one.
3. Failing (2), if  $X$  has a next use in the block, or  $op$  is an operator, such as indexing, that requires a register find an occupied register  $R$ . Store the values of  $R$  into a memory location (`MOV R, M`) if it is not already in the proper memory location  $M$ , update the address descriptor for  $M$ , and return  $R$ . if  $R$  holds the value of several variables, a `MOV`

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instruction must be generated for each variable that needs to be stored. A suitable register might be one whose data is referenced furthest in the future, or one whose value is also in memory. We leave the exact choice unspecified, since there is no one proven best way to make the selection.

4. If  $x$  is not used in the block, or no suitable occupied register can be found, select the memory location of  $x$  as  $L$ .

**Conclusion:**

**Postlab:**

**Explain design issues of code generator phase?**

**What are basic blocks? State their properties**

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```
import re

operatorTable = {
    "+": "ADD",
    "-": "SUB",
    "*": "MUL",
    "/": "DIV",
}

registerTable = {}

def getRegisterByIndex(index):
    for [key, value] in enumerate(registerTable):
        if value == 'R' + index:
            return key

    return -1

def getRegisterByOperand(operand):
    if operand not in registerTable.keys():
        registerTable[operand] = "R" + str(len(registerTable))
```

```
return registerTable[operand]
```

```
def parseLine(line):
```

```
    line = line.strip().replace(" ", "")
```

```
    print(line)
```

```
    # Check for equals
```

```
    if '=' not in line:
```

```
        return
```

```
    expression = line.split("=")
```

```
    operands = re.split(r'[-+*/()]', expression[1])
```

```
    operators = re.findall(r'[-+*/()]', expression[1])
```

```
    # print(operands)
```

```
    # print(operators)
```

```
    setVarCode = ""
```

```
    operationCode = ""
```

```
    for op in operators:
```

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```
if op not in operatorTable:
    raise "Invalid Operator " + op

operationCode += operatorTable[op] + " "

# Get Operand 1 and its register
operand1 = operands.pop(0)

# Allocate Register to operand
if operand1 not in registerTable.keys():
    setVarCode += f"MOV {operand1},"

register1 = getRegisterByOperand(operand1)

if len(setVarCode) > 1:
    setVarCode += register1

# Get Operand 2 and its register
operand2 = operands.pop(0)

if operand2 in registerTable.keys():
    operand2 = getRegisterByOperand(operand2)
```

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```
operationCode += f"{operand2},{register1}"
```

```
if len(setVarCode) > 0:
```

```
    print(setVarCode)
```

```
if len(operationCode) > 0:
```

```
    print(operationCode)
```

```
print(f"{expression[0]} : {getRegisterByOperand(expression[0])}")
```

```
print()
```

```
code = open("6-code.txt").readlines()
```

```
for line in code[:]:
```

```
    parseLine(line)
```

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The screenshot shows a Visual Studio Code interface with the 'TERMINAL' tab selected. The terminal displays the output of a Python script and assembly code. The Python script attempts to read lines from a file named 'Exp 6\6-code.txt', but it fails with an 'OSError: [Errno 22] Invalid argument: 'Exp 6\x06-code.txt''. The assembly code is shown in a separate pane on the right, with a 'Code' button visible. The assembly code includes instructions for moving, adding, subtracting, and assigning values to registers R0 through R6.

```
code = open("Exp 6\6-code.txt").readlines()
^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^
OSError: [Errno 22] Invalid argument: 'Exp 6\x06-code.txt'
● PS C:\Users\ivana\Desktop\College\Third Year\SEM 6\SPCC Pracs> python -
u "c:\Users\ivana\Desktop\College\Third Year\SEM 6\SPCC Pracs\Exp 6\6.
py"
t=b+c
MOV b,R0
ADD c,R0
t : R1

v=d+e
MOV d,R2
ADD e,R2
v : R3

u=t-v
SUB R3,R1
u : R4

w=t+u
ADD R4,R1
w : R5

a=w
a : R6

○ PS C:\Users\ivana\Desktop\College\Third Year\SEM 6\SPCC Pracs>
```



Q1]  
Ans

The code generator phase is a compiler translates the intermediate representations of the source code into executable machine.

The code generator has the following issues:

- i) Target Architecture: Designing the code generator to abstract away target arch. interfaces while efficiently utilizing its features is crucial.
- ii) Optimizer: Balancing code size and execution speed while applying various techniques is challenging.
- iii) Target Language Representation: Efficiently mapping high level language constructs to m/c code instructions & data structures is essential.
- iv) Handling Control Flow: Generating code that accurately reflects control flow.
- v) Error Handling: Dealing with errors during code generation.



Q2)

Ans) Basic blocks are fundamental units of code used in various computer optimizations and analysis. It has the following properties.

i) Single Entry, Single Exit: Simplifies the control flow analysis.

ii) No internal Branching: Enables treating basic blocks as linear sequences of instructions.

iii) Atomic Execution: Ensures sequential execution without interruptions for correct program semantics.