Density Internals

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1 Introduction

This document describes the implementation of the library density¹. The exposition of the functionalities is bottom-up, from the internal functionalities to the public data structures. The trivial parts are not covered, while in the critical parts the exposition reaches the source-line level of detail. The document is at the same time an overview of the library, more technical than the one available in the documentation. The exposition assumes a basic knowledge of non-blocking programming. On Wikipedia there is an excellent introduction to this topic².

Density is a C++11 library that provides:

- Page-based memory management: rather than allocating many small blocks of heap memory, all the data structures of the library prefer allocating large memory pages from a page allocator. All the pages have the same size (by default around 64 kibibytes). In the unlikely case that a single object is too big to fit in a page, it is allocated on the legacy heap.
- Lifo ordered allocation of heterogeneous objects, useful for thread-local temporary data. The library introduces the data-stack, a stack (parallel to the call-stack) in which the user can allocate dynamic arrays or raw buffers. The lifo memory management is built upon the

¹ https://github.com/giucamp/density

²https://en.wikipedia.org/wiki/Non-blocking algorithm

paged-memory management, so the data stack is actually composed by memory pages. Allocating on the data-stack is fast almost like allocating a on the call-stack with <code>_alloca</code>, but never results in a stack overflows. In case of out of system memory an <code>std::bad_alloc</code> is thrown.

- Fifo ordered allocation of heterogeneous objects, useful for exchanging messages between threads, or for asynchronous processing in both single-thread and multi-thread scenarios. The library provides a rich set of heterogeneous queues and function queues:
- Concurrency with parametric progress guarantee (blocking, obstruction-free, lock-free and wait-free). The user specifies a progress guarantee with a parameter, and the implementation does its best to successfully complete the operation respecting it. In case that the implementation can't respect the guarantee, the call fails and has no observable side effects.

This is the summary of the heterogeneous queues and function queues provided by the library:

Threading strategy	Heterogeneous queue	Function queue	Producers cardinality	Consumers cardinality
single-thread	heter_queue	function_queue	none	none
locking	conc_heter_queue	conc_function_queue	multiple	multiple
lock-free	lf_heter_queue	lf_function_queue	single or multiple	single or multiple
spin-locking	sp_heter_queue	sp_function_queue	single or multiple	single or multiple

The current implementation of density does not differentiate between obstruction-free and lock-free guarantees: when an obstruction-free operation is requested, the library tries to complete it in lock-freedom.

All the data structures of the library provide the strong exception guarantee on all the operations.

Paged memory management

2 Overview of paged memory management

The library implements a page-based memory management. Grouping heterogeneous objects in large memory pages (instead of allocating each object in a separate heap block) improves the access locality, and increases the granularity of every memory management based task, including safe indirections in lock-free algorithms.

Memory pages have an arbitrary constant size and a constant alignment. The alignment must be greater or equal to the size (and of course an integer power of 2). Given an address of a byte within a page, we are able to compute the start address of the page with a simple bitwise and. Since the size of pages can be less than the alignment, page allocators can store some metadata in a page footer struct. As example, by default the page allocator of density manages pages aligned to 2^16,

with a page size of 2^16 - sizeof (AnInternalPageFooter). The page allocator must guarantee that the space between the end of a page and the next aligned page in the address space will not be used by any other allocator. This guarantee will show very useful for the implementation of lifo memory management.

The data structures allocate many objects of heterogeneous types linearly in the same page and in the same order in which the user will access the objects (in container order). When the space in a page is exhausted, another page is allocated. In this case we say that a page switch occurs. A *page switch* occurs also when destroying\consuming objects, and a page has no more objects allocated within. *In-page* allocation and deallocation are considered the fast path in all the data structures, while a page switch is the slow path. The data structures of the library never cache free pages. The page allocator may possibly do that.

There is no limit on the size of objects to allocate, so in case of objects too large all data structures fallback to legacy heap allocation. For this reason allocator types in the library are required to model two distinct concepts: *PagedAllocator* and *UntypedAllocator*. The first will be exposed in this paragraph, while the latter is just a legacy allocator supporting over-alignment with offset.

To allow a safe access to pages in a lock-free context, the page allocator supports pinning. Pinning is a kind of reference counting: while a page is pinned (that is the reference count is non-zero), even if the page is deallocated, the allocator will not recycle it for an allocation function, and will not alter or read its content in any way.

Pinning a page that has been already deallocated is legal. Accessing such page, or doing anything but unpinning it, triggers an undefined behavior. When encountering a pointer with value P in a data structure, a thread should pin it, and then it should check whether P is still linked to the data structure. If not, it has to unpin and retry.

Sometimes we need that the pages we link to a data structure have a zeroed content. Lock-free algorithms may use initially zeroed pages to make threads agree on an history of the data which begins with the zeroed state. We introduce in our allocator functions to allocate pages with zeroed (rather than undefined) content.

When deallocating a page, we may tell to the allocator that the page is already zeroed. If the page is still pinned, the deallocating thread guarantees that the page will be zeroed when unpinned. This allows the allocator to return the page to the user as zeroed without having to memset it. The set of zeroed pages is not distinct from the set of normal pages: being zeroed is a transient property of a page.

Here is the PageAllocator synopsis:

```
class PageAllocator
{
public:
    static constexpr size_t page_size = ...;
    static constexpr size_t page_alignment = ...;
    static_assert(is_power_of_2(page_alignment))
```

```
%& page_alignment >= page_size, "");

void * allocate_page();
void * try_allocate_page(progress_guarantee) noexcept;
void * allocate_page_zeroed();
void * try_allocate_page_zeroed(progress_guarantee) noexcept;
void deallocate_page(void *) noexcept;
void deallocate_page_zeroed(void *) noexcept;
void pin_page(void *) noexcept;
void unpin_page(void *) noexcept;
};
```

Allocation functions return a pointer to the first byte of the page. Functions taking a page as parameter always align the address, so the user can specify a pointer to any byte within the page. The page allocator and many data structures of the library expose a set of *try_* functions*. These functions have some common properties:

- they return a boolean or a type implicitly convertible to boolean. A true return value indicates success, while false return value indicates a failure with no observable side effects.
- they are noexcept or at least exception neutral. Allocation try_* function are always noexcept, while put try_* functions don't throw any exception, though they pass through any exception raised by the constructor of an user defined type.
- the first parameter has always the type:

```
enum progress_guarantee {
    progress_blocking, progress_obstruction_free,
    progress_lock_free, progress_wait_free };
```

If the implementation can't guarantee the completion with the specified progress guarantee, the function fails. A failure with a blocking progress guarantee generally indicates an out of memory.

Deallocation functions are required to be always wait-free. The rationale for this is that any lock-free CAS-based deallocation algorithm, whenever it does not succeed to perform the operation in a finite number of steps, can push the free page in a thread-local queue, and try later (or satisfy a subsequent request of the same thread).

The current implementation of lock-free and spin-locking queues has a defect related to page pinning. Page pinning and unpinning has an obvious implementation with atomic increment and decrement, that have a lock-free implementation on most modern architectures. Anyway, even when such operations are translated to a single instruction, it's unlikely that they can ever be wait-free. For this reason wait-free try_* operations are forced to fail when they would perform a page switch, even if a thread executes them in isolation.

This is a defect, since any wait-free operation shouldn't fail indefinitely if tried by a thread in isolation. The planned solution for this problem is to extend the PageAllocator concept with two more functions:

```
bool try_pin_page(progress_guarantee, void *) noexcept;
void unpin_page(progress_guarantee, void *) noexcept;
```

The first function is expected to try a possibly successful CAS-based increment in case the first argument is progress_wait_free, and to do an always successful atomic increment in all the other cases.

The second one can't fail by design, because the caller is unlikely to be able to handle the failure. In case of failure of a wait-free unpin, the allocator may push the page on a thread-local list of pages, so that it can re-try the unpin operation later.

3 SingletonPtr

Starting from C++11 the initialization of static local objects is thread safe. Since global objects exhibit the initialization order fiasco, static locals are generally safer than globals to implement singletons. Anyway, unless zero or constant initialization is possible, the compiler introduces a hidden atomic initialization guard, so that only the first access will initialize the object. The cpu's branch predictor greatly reduces the cost of this branch, but in many cases it can be removed at all.

The implementation of density uses an internal class template that implements the singleton pattern:

```
template <typename SINGLETON> class SingletonPtr;
```

This class template has non-nullable pointer semantics. It is stateless (therefore immutable), copyable and thread safe. The actual singleton object is handled internally, allocated in the static storage at a fixed address. Since this address is constant there is no need to store it, and all the specialization of SingletonPtr are guaranteed to be empty (no non-static data members).

The singleton is actually initialized when the first <code>singletonPtr</code> is constructed. When the last <code>singletonPtr</code> is destroyed, the singleton is destroyed too (it uses an internal reference count). The cost of handling the lifetime of the singleton is paid only by the constructor and the destructor. This is an advantage when we can keep a stable pointer to the singleton, and use repeatedly. For example an allocator class may keep a <code>singletonPtr</code> pointing to a memory manager. Everyone who has access to a <code>singletonPtr</code> can safely access the singleton.

Internally every specialization of <code>singletonPtr</code> declares an instance of itself in the static storage, so that the singleton is always constructed during the dynamic initialization. If another <code>singletonPtr</code> is constructed and destroyed before the initialization of this internal instance, the singleton is constructed, destroyed, and then constructed again. Similarly if a <code>singletonPtr</code> is constructed after the destruction of the internal instance, the singleton is created again.

So singletonPtr does not guarantee that only one instance of the singleton is created, but rather that in any moment at most one instance of the singleton exists. In case of contention between threads in the first access, if that happens during the dynamic initialization, a thread may spin-lock waiting for another thread to complete the initialization if the singleton. This is a limitation of the current implementation, and will be probably fixed in the next releases.

4 Raw atomics

Sometimes in lock free algorithms we can't use std::atomic, because even if standard atomics are trivially default-constructible, a default constructed atomic requires a call to std::atomic_init to complete the initialization. So we introduce a minimal set of non-standard of functions for atomic operations on fundamental variables:

```
template <typename TYPE>
     TYPE raw atomic load(
          TYPE const volatile * i atomic,
          std::memory order i memory order) noexcept;
template <typename TYPE>
     void raw atomic store(
          TYPE volatile * i atomic,
          TYPE i value,
          std::memory order i memory order) noexcept;
template <typename TYPE>
     bool raw atomic compare exchange weak (
          TYPE volatile * i atomic,
          TYPE * i expected,
          TYPE i desired,
           std::memory_order i_success,
          std::memory order i failure) noexcept;
template <typename TYPE>
     bool raw atomic_compare_exchange_strong(
          TYPE volatile * i_atomic,
          TYPE * i expected,
          TYPE i desired,
          std::memory order i success,
           std::memory order i failure) noexcept;
```

The default value for memory order parameters is omitted, and it is always std::memory_order_seq_cst. The current implementation deletes all the generic function templates, and specialize an implementation only for some integer types.

These functions behave like the standard counterparts. Anyway fundamental variables are fully trivially constructible, and can be zero-initialized. A second advantage of raw atomics is that in some (rare) cases we can mix non-atomic and atomic writes to the same variable, if we know for sure that the non-atomic access is synchronized in some way.

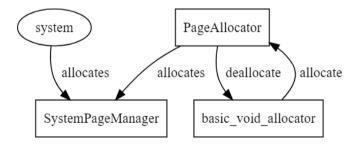
The implementation of <code>lf_heter_queue</code> we will describe requires raw atomics for <code>uintptr_t</code>. If for a given compiler or OS raw atomics aren't available, the lock-free queues can't be used.

5 WF_PageStack

The implementation of the page allocator uses WF_PageStack, a wait-free queue specialized for pages. The stack is implemented as an intrusive linked list.

The class PageStack (used for some member functions) is a non-concurrent stack of pages. All the operations on WF_PageStack can fail in case of contention between threads. The function try_pop_unpinned temporary steals the whole stack, then search for the first page with zero pin count, and then re-links the pages (with possibly one less) to the stack. Stealing the whole content is an easy and elegant way to avoid the ABA problem.

6 Implementation of the page allocator



The page management is composed by 3 layers. The lowest layer is the SystemPageManager, that exposes a public function to allocate a memory page, but no function to deallocate. So it provides an irreversible allocation service.

Internally the SystemPageManager allocates

large memory regions from the system and slices them into pages. Every region has a pointer to the next page to allocate. When a page is to be allocated, this pointer is advanced by the size of a page, and then its previous value is returned. When the current region is exhausted, another region is created. If the <code>systemPageManager</code> gets an out of memory from the system when allocating a region, it tries to allocate a smaller region. After a number of failed tries with decreasing sizes, it reports the failure to the caller. Memory regions are deallocated when the <code>systemPageManager</code> is destroyed, that is when the program exits.

The function of SystemPageManager that allocates a page is try allocate page:

```
void * SystemPageManager::try_allocate_page(
    progress_guarantee i_progress_guarantee) noexcept;
```

On success it returns a pointer to the first byte of the page, while in case of failure it returns null. The conditions under which this function fails depends on the requested progress guarantee, as illustrated in this table:

Progress guarantee	Condition failure	
progress_blocking	Fails only in case of out of system memory	
progress_obstruction_free	Fails if there is no free space in the memory regional already allocated, as the allocation of system memory is likely to be blocking.	
progress_lock_free		
progress_wait_free	Fails if there is no free space in the memory regions already allocated or in case of contention between threads	

The user of SystemPageManager may reserve in advance a capacity of memory to usable in lock-freedom with the following function:

```
uintptr_t SystemPageManager::try_reserve_region_memory(
    progress_guarantee i_progress_guarantee,
    uintptr_t const i_size) noexcept;
```

The return value is the total memory (in bytes) allocated from the system after the call. If this memory would be less than the one specified by the second argument, and the caller has specified the blocking progress guarantee, it tries to allocate memory regions from the system until the total size is equal or greater the the second parameter. Note: the signature of this function does not respect the convention on try_ functions. Anyway it is an internal function, and it may change in the future.

The second layer is the PageAllocator, that provides reversible page allocation, with an interface similar to the PageAllocator concept. In this layer there are a (currently fixed) number of slot, each containing a WF_PageStack of free pages and a WF_PageStack of free and zeroed pages.

Furthermore the PageAllocator associates to every thread some local data:

- A pointer to the *current slot*
- A pointer to the *victim slot*
- A non-concurrent stack of free pages
- A non-concurrent stack of free and zeroed pages

When a new page is requested, the PageAllocator tries to satisfy the request executing an ordered sequence of steps until one of them is successful. More precisely the PageAllocator:

- Peeks the first non-pinned page from the local stack
- Peeks the first non-pinned page from the current slot
- Steals all the pages of the victim slot, pushing them to the current slot. Then peeks the first non-pinned page from the stolen stack
- Tries to allocate from the SystemPageManager in wait-freedom (no new system memory is allocated)
- Loops the pointer to the victim slot through all the slot, stealing all the pages from each of them. If a non-pinned page in found in a stolen stack, the loop is interrupted.
- Now, if the progress guarantee specified by the caller is not progress_blocking, the PageAllocator returns null to signal a failure. Otherwise it forwards the request to the SystemPageManager, that may try to allocate a new memory region.

To deallocate a page the PageAllocator loops the pointer to the current slot through all the slot. At every iteration it tries to push the page on the WF_PageStack. The push may fail only because of contention with another thread. If the push fails on all the slots, the page is pushed on the thread-local stack.

The last layer of the page management is the <code>void_allocator</code>, that just forwards the requests to the <code>PageAllocator</code>. The layers before the <code>void_allocator</code> don't throw exceptions: they report a failure in allocating a page with a null return value. The <code>void_allocator</code> is the layer that, in case of failure of a non-try allocation function, throws a <code>std::bad alloc</code>.

The user can use two functions of <code>void_allocator</code> e to reserve paged-memory to be used in lock-freedom:

These functions make sure that the specified size is already been allocated from the system. In case of out of memory the first function throws an std::bad_alloc, while the second one, in case of failure, just returns false. The parameter o_reserved_size is to retrieve the total memory allocated from the system after the call.

Lifo memory management

7 The life allocator

The lifo memory management provided by the library is not thread safe, the reason being that it is supposed to be used mostly for thread-local data. It is built upon paged-memory management: the class template <code>lifo_allocator</code> adapts an allocator satisfying both he the PagedAllocator and UntypedAllocator concepts to expose a LIFO constrained allocation service.

The LIFO order imposes that only the most recently allocated block can be resized or deallocated. If this constraint is violated the behavior is undefined. Dealing with the LIFO order without the help of the RAII idiom is extremely bug prone, and it's highly discouraged.

This is the signature of the class template:

```
class lifo_allocator {
   template < typename UNDERLYING_ALLOCATOR = void_allocator,
   size_t ALIGNMENT = alignof(void*)>
        class lifo_allocator;
```

To simplify the implementation of the allocator the alignment is constant and the same for all the blocks.

The only non-static data member of is the a pointer to the end of the stack, that is the *top pointer*:

```
static constexpr uintptr_t s_virgin_top =
      uint_lower_align(page_alignment - 1, alignment);
uintptr_t m_top = s_virgin_top;
```

The top pointer usually points to the beginning of next block that will be allocated. It is an uintptr_t rather than a pointer to allow the default constructor to be constexpr (reinterpret_cast is not allowed in constant expressions).

The allocator is designed so that in the fast path, with the same conditional branch, it can redirect to the same slow execution path these 3 cases:

- the block to allocate is not too big, but the current page has not enough free space
- the block to allocate is too big, and it will not fit in a memory page
- the allocator is virgin, that is it has just been constructed and never used (after allocating a page the allocator will never return to the virgin state)

To allocate a block the allocator just adds the input size to the top pointer. If it detects that the updated top would lie past the end of the current page, it enters in the slow execution path. In the slow

path it decides between a new page or an external block (that is a block allocated in the legacy heap). When allocating or deallocating external blocks the state of the allocator is not altered.

When a new page is allocated, a header is added at the beginning of the page. This header contains only a pointer to the previous page, and it is necessary for the deallocation.

```
void * allocate(size t i size)
     DENSITY ASSERT(i size % alignment == 0);
     auto const new top = m top + i size;
     auto const new offset = new top -
           uint lower align (m top, page alignment);
     if (!DENSITY LIKELY(new offset < page size))</pre>
           // page overflow
           return allocate slow path(i size);
     else
           // advance m top
           DENSITY ASSERT INTERNAL(i size <=page size);</pre>
           auto const new block = reinterpret cast<void*>(m top);
           m \text{ top} = \text{new top};
           return new block;
     }
DENSITY NO INLINE void * allocate slow path(size t i size)
     DENSITY ASSERT INTERNAL(i size % alignment == 0);
     if (i size < page size / \overline{2})
           // allocate a new page
           auto const new page =
                 UNDERLYING ALLOCATOR::allocate page();
           DENSITY_ASSERT_INTERNAL(new_page != nullptr);
           auto const new header = new(new page) PageHeader;
           new header->m prev page = reinterpret cast<void*>(
                 m top);
           m top = reinterpret cast<uintptr t>(
                 new header +1) + i size;
           return new header + 1;
     }
     else
     {
           // external block
           return UNDERLYING ALLOCATOR::allocate(
                 i size, alignment);
     }
```

The function allocate requires that the size of the block is aligned, otherwise the behavior is undefined. allocate can allocate a block with size zero. Anyway there is a special allocation function that can be used when the size of the block is always zero:

```
void * allocate_empty() noexcept
{
    return reinterpret_cast<void*>(m_top);
}
```

This function does not alter the state of the queue, is faster and never throws.

The allocator uses a delayed deallocation strategy for the pages: a page is not deallocated when it owns no alive blocks, but rather when a block of the previous page is deallocated. With this strategy a user that occasionally allocates and deallocates a block will enter in the slow path (and will allocate a page) only the first time.

To deallocate a block, if the address is in the same page of the top pointer, the <code>lifo_allocator</code> just assigns the block to deallocate to the top pointer. Otherwise it enters the slow path, in which the allocator detects whether the top pointer is going to leave an empty page, or it has to deallocate an external block.

```
void deallocate (void * i block, size t i size) noexcept
     DENSITY ASSERT(i block != nullptr && i size % alignment == 0);
     // this check detects page switches and external blocks
     if (!DENSITY LIKELY(same page(i block,
                reinterpret cast<void*>(m top))))
          deallocate slow path(i block, i size);
     }
     else
          m top = reinterpret cast<uintptr t>(i block);
     }
DENSITY NO INLINE void deallocate slow path(
     void * i block, size t i size) noexcept
{
     DENSITY ASSERT INTERNAL(i size % alignment == 0);
     if ((m_top & (page_alignment - 1)) == sizeof(PageHeader) &&
                same page(reinterpret cast<PageHeader*>(
                m top)[-1].m prev page, i block) )
     {
           // deallocate the top page
          auto const page to deallocate = reinterpret cast<void*>(
                m top);
          UNDERLYING ALLOCATOR::deallocate page(
                page to deallocate);
```

The allocator allows resizing a block, preserving its content up to the previous size, with the following function:

```
void * reallocate(void * i_block, size_t i_old_size, size_t i_new_size);
```

The implementation of this function is slightly more complex, but it is basically an exception safe mixing of allocate and deallocate, so it is not listed here.

8 The data-stack

The library keeps a private thread-local instance of <code>lifo_allocator</code>, called the *data-stack*. By design the constructor of <code>lifo_allocator</code> is trivial and constexpr. Threads may allocate the first page only when they uses the data-stack for the first time. Direct access to the data-stack is not allowed. Two data structures are provided instead to use indirectly the data-stack: <code>lifo_array</code> and <code>lifo_buf-fer</code>.

The class template <code>lifo_array</code> is very similar to an array. It has an immutable size specified at construction time. If no initializer is provided, <code>lifo_array</code> default-constructs the elements. This is a big difference with to <code>std::vector</code>, that uses value-initialization.

```
// uninitialized array of doubles
lifo array<double> numbers(7);
// initialize the array
for (auto & num : numbers)
    num = 1.;
// compute the sum
auto const sum = std::accumulate(
      numbers.begin(), numbers.end(), 0.);
assert(sum == 7.);
// initialized array
lifo array<double> other numbers(7, 1.);
auto const other sum = std::accumulate(
       other numbers.begin(), other numbers.end(), 0.);
assert(other sum == 7.);
// array of class objects (default-constructed)
lifo array<std::string> strings(10);
```

It's highly recommended to use <code>lifo_array</code> only on the automatic storage. Doing so there is no way to break the LIFO constraint. Any other used should be handled with caution.

The class lifo_buffer is very different from lifo_array mainly for 2 reasons:

- it handles raw memory rather than elements of a type known at compile-time
- it allows resizing (with content preservation) of the most recently constructed instance.

Resizing a lifo_buffer when another most recent lifo_buffer or lifo_array is alive causes undefined behavior.

Internally the default-contructor of <code>lifo_buffer</code> uses the function <code>allocate_empty</code> to allocate a block of size 0, so it is no except and very fast.

Heterogeneous and function queues

9 Introduction to type erasure

Type erasure is a well know technique to decouple actions on objects from their compile-time type. Let's consider an implementation of a heterogeneous sequence of objects. When we insert an object in the sequence we may know the type at compile-time, so we can just call the constructor of the element. Anyway, when we later iterate the sequence, we have no knowledge of the types of the objects. Type erasure solves this problem.

The type erasure used by density requires declaring in advance which features should be captured the types. Let's suppose for example that we need to be able to write the objects of the sequence to an std::ostream. A very simple way to do that is to prepend to every element a pointer to a function that is able to handle the specific object.

The memory layout of this sequence may be something like this:

```
void write_string(const void * i_object, std::ostream & i_stream)
{
    i_stream << *static_cast<const std::string*>(i_object);
}
```

type-eraser	object	type-eraser	object
pointer to write_string	std::string	pointer to write_double	double

```
void write_double(const void * i_object, std::ostream & i_stream)
{
    i_stream << *static_cast<const double*>(i_object);
}
```

In this example the type eraser is a pointer to a function with this signature:

```
void (*)(const void * i_object, std::ostream & i_stream);
```

A trivial consideration is that the implementation of the write function for any type may be provided by a function template:

In a more realistic case we would need to add at least a pointer to a destroy function, so in this case our type erasure would be a struct containing two pointers to functions. We may even add a variable with the size of the type, and one with the alignment. If our type erasure has a considerable size, we may consider to add a level of indirection: the type eraser becomes a pointer to a static consteprx struct with the actual pointer to functions and data.

The above example is simplified, but is very similar to the type erasure used by density. Note that this technique is similar to the v-table used by the C++ compilers to implement virtual functions. The main differences are:

- it is easily customizable. For example one may add data members (for example for the size and alignment), and even pointer to construction functions, while virtual static data and virtual constructors are currently not supported by the standard.
- it is not intrusive, while virtual function are a feature embedded in the type. A class can be type-erased without being modified.

Here follows a digression on some benefits of using type erasure. The reader not interested may skip to the next paragraph.

Let's suppose we have a set of classes representing 2d shapes:

```
class Circle
{
  public:
    bool IsPointInside(const Vector2d & i_point) const;

private:
    Vector2d m_center;
    float m_radius = -1.f;
};

class Box
{
  public:
    bool IsPointInside(const Vector2d & i_point) const;

private:
    Vector2d m_center, m_size{-1.f, -1.f};
};
```

Now we want to implement a class <code>complexShape</code>, that represent a union of other shapes. <code>complexShape</code> provides a function <code>isPointInside</code> that checks if the point is inside any of the child shapes. The classic OOP solution would be introducing a base class or an interface representing a shape, with a virtual function <code>isPointInside</code>. Anyway interfaces and base classes have impact on both the design and the performances. Any instance of <code>Box</code> would have a pointer to a v-table as hidden member, even if polymorphous is not necessary for all the uses.

Instead of using polymorphism, we may implement ComplexShape using type erasure:

```
class ShapeReference
public:
    // ...
    bool IsPointInside(const Vector2d & i point) const
        return (*m type->m is point inside) (m object, i point);
private:
    struct Type
        bool (*m is point inside) (void * const i_object,
                      const Vector2d & i_point);
        void (*m destroy func)(void * i object);
    };
    void * m object;
    Type * m type;
};
class ComplexShape
    // ...
private:
    std::vector<ShapeReference> m children;
```

With type-erasure, polymorphism is paid only when it's actually used.

The RuntimeType pseudo-concept

The heterogeneous queues provided by the library are not bound to a particular type-erasure. They have instead a type template-parameter that allows the user to specify a custom type erasure, provided that it models *RuntimeType*.

Here is the RuntimeType synopsis:

```
class RuntimeType
{
  public:
    using common_type = ...;
    template <typename TYPE>
        static RuntimeType make() noexcept;
```

```
template <typename TYPE>
       bool is() const noexcept;
   RuntimeType(const RuntimeType &);
   size t size() const noexcept;
   size t alignment() const noexcept;
   common type * default construct(void * i dest) const;
   common_type * copy construct(void * i dest,
          const common type * i source) const;
   common type * move construct(void * i dest,
          common type * i source) const;
   void * destroy(common type * i dest) const noexcept;
   const std::type_info & type info() const noexcept;
   bool are equal(const common type * i first,
          const common type * i second) const;
   bool operator == (
          const RuntimeType & i other) const noexcept;
   bool operator != (
          const RuntimeType & i other) const noexcept;
};
```

Most of the functions are optional, so RuntimeType strictly can't be a concept.

The static function template make makes the transition from a type known at compile-time, to an instance of RuntimeType that has no compile-time dependence from the type. The type bound to a RuntimeType is the *target type*. If the type alias <code>common_type</code> is void, the target type can be any type. Otherwise the target type must be a user-defined type deriving from <code>common_type</code>.

The *_contruct functions take as parameter a pointer to the beginning of the storage of the object to construct (i_dest). If common_type is void, they return the same pointer. Otherwise they return a pointer to the common_type sub-object of the complete object. Symmetrically the function destroy takes a pointer to the common_type sub-object of the object, and returns a pointer to the complete object.

By default all the heterogeneous queues of the library use the class template <code>runtime_type</code> for type erasure. A <code>runtime_type</code> internally is just a pointer to a custom v-table containing data and pointer to functions to capture a set of features on the target type. This is the signature of the class template:

```
template <typename COMMON_TYPE = void,
  typename FEATURE_LIST =</pre>
```

```
class runtime type;
```

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Feature What allows on runtime type Allows to use the function runtime type::size to get the size type features::size of the target type Allows to use the function runtime type::alignment to get the type features::alignment size of the target type Allows to use the function runtime type::type info to get a type features::rtti std::type info for the target type Allows to use the function runtime type::destroy to destroy an type features::destroy instance of the target type Allows to use the function runtime_type::move_construct to type features::move construct move-construct an instance of the target type from an instance (only if the comon type is void or of the same type is move constructible) Allows to use the function runtime type::move_construct to type features::copy construct copy-construct an instance of the target type from an instance of (only if the comon type is void or the same type is copy constructible)

runtime type is highly customizable, as it allows to specify the set of feature to capture with the second template argument, which is a type list. The namespace density::type features provides many common features that can be used. This table enumerates the features that a runtime type captures with the default feature list:

This example shows how to define a runtime type customizing the captured features and how to use it:

```
using namespace type features;
using MyRTType = runtime_type<void,</pre>
     feature list<default construct, destroy, size> >;
MyRTType type = MyRTType::make<std::string>();
void * buff = malloc(type.size());
type.default construct(buff);
// now buff points to a valid std::string
*static cast<std::string*>(buff) = "hello world!";
type.destroy(buff);
free (buff);
```

11Overview of the heterogeneous queues

The library provides 4 heterogeneous queues:

• *heter queue*: not thread safe

- conc heter queue: thread safe, blocking
- If heter queue: thread safe, lock-free
- sp heter queue: thread safe, spin-locking

The first 3 template parameters of all the heterogeneous queues are:

- COMMON_TYPE. type parameter, by default void. An element of type T can be put in the queue only if T* is implicitly convertible to COMMON TYPE*.
- RUNTIME_TYPE. type parameter, by default runtime_type<>. It's the type-eraser. It must have the member type alias common_type that matches COMMON_TYPE. A static assert checks that this requirement is met.
- ALLOCATOR_TYPE. type parameter, by default void_allocator. It must model both PageAllocator and UntypedAllocator.

The class template 1f heter queue has these additional template parameters:

- PRODUCER_CARDINALITY. parameter of type concurrency_cardinality, the default value is concurrency_multiple. Specifies whether multiple threads are allowed to put in the queue concurrently.
- CONSUMER_CARDINALITY. parameter of type concurrency_cardinality, the default value is concurrency_multiple. Specifies whether multiple threads are allowed to consume from the queue concurrently.
- CONSISTENCY_MODEL. parameter of type consistency_model, the default value is consistency_sequential. Specifies whether the queue is linearizable, that is all the threads agree on a total ordering of all the operations on the queue.

The class template sp heter queue has these additional template parameters:

- PRODUCER_CARDINALITY. Like the homonymous parameter of lf_heter_queue.
- CONSUMER CARDINALITY. Like the homonymous parameter of 1f heter queue.
- BUSY_WAIT_FUNC. Type callable with the signature void () that is called in the body of a busy wait. The default value is a function type that calls std::this_thread::yield.

An action on the queue is an *operations*. Some operation is executed by a single function call, while some others span more than one call. Every operation belongs to one of these classes:

• put operations: operations that inserts an element to the end of the queue.

- *consume operations*: operations that consume an element at the beginning of the queue, or that check if the queue is empty.
- *lifetime operations*: all the other functions, including constructors, destructors, assignments and swap.

Depending on the queue and on the template arguments, put and consume operations can be used concurrently. All the queues define a set of member constants that describe the concurrent capabilities of the queue:

Member constant	Semantics
static constexpr bool concurrent_puts;	Whether multiple threads are allowed to do put operations concurrently.
static constexpr bool concurrent_consumes;	Whether multiple threads are allowed to do consume operations concurrently.
static constexpr bool concurrent_put_consumes;	Whether put operations can be executed concurrently with consume operations.
static constexpr bool is_seq_cst;	Whether the queue is linearizable, that is all the threads agree on a total ordering of all the operations on the queue.

heter_queue is not concurrent, so all the above constants are false. conc_heter_queue is always fully concurrent, so all the above constants are true.

lf_heter_queue and sp_heter_queue always allows a single producer to run concurrently with a single consumer, so concurrent_put_consumes is always true. The values of concurrent_puts and concurrent_consumes depends on the template arguments PRODUCER_CARDINALITY and CONSUMER_CARDINALITY. The constant is_seq_cst is always true for sp_heter_queue, and depends on the template argument CONSISTENCY_MODEL for lf_heter_queue.

The simplest way to put an element in a queue is using push or emplace:

```
queue.push(19); // the parameter can be an l-value or an r-value
queue.emplace<std::string>(8, '*'); // pushes "******"
```

The type of the element is deduced from the argument in the case of push, but needs to be specified explicitly in the case of emplace.

Put functions containing start_ in their name are *transactional*. They start a put, and return an object of type put transaction, that can be used to *commit* or *cancel* the put.

```
auto put = queue.start_push(12);
```

```
put.element() += 2;
put.commit(); // commits a 14
```

A put transaction becomes observable only when it's committed. If it is canceled, it will never be observable.

An instance of put_transaction in any moment either is bound to a transaction, or is empty. When the functions commit or cancel are called, the put_transaction becomes empty. If the put_transaction is already empty, calling commit or cancel causes undefined behavior. When a non-empty put_transaction is destroyed, the transaction is canceled. put_transaction's are movable but not copyable.

Transactional puts allow to associate a *raw block* with the element:

```
heter_queue<> queue;
struct MessageInABottle
{
    const char * m_text = nullptr;
};
auto transaction = queue.start_emplace<MessageInABottle>();
transaction.element().m_text = transaction.raw_allocate_copy("Hello!");
transaction.commit();
```

A raw block is an untyped and uninitialized range of contiguous memory, in which the user may store additional data associated to an element. An element may be associated to more than one block, but it should keep a pointer to every of them, because otherwise consumers would have no way to get these addresses.

Raw blocks can be allocated specifying a size and an alignment, a pair of iterators, or a range (like in the example). A raw block is automatically deallocated when the put of the associated element is canceled, or when the element is consumed. Accessing a deallocated raw block causes undefined behavior.

By default operations on queues are non-recentrant. During a non-re-entrant operation the queue is not in a consistent state for the calling thread (it is in a consistent state for the other threads, provided that it is concurrency-enabled). So, in the example above, calling any member function on the queue between the start_emplace and the commit would cause undefined behavior. Even a single-call put may introduce re-entrancy:

```
// buggy code:
queue.emplace<ClassThatUsesTheQueueInTheConstructor>(12); <- UB!!!!</pre>
```

The operations that starts with a function containing reentrant_ in their name support *re-entrancy*. Re-entrant operations can overlap each other.

```
auto put_1 = queue.start_reentrant_push(12);
auto put_2 = queue.start_reentrant_push(std::string("Hello "));
auto put_3 = queue.start_reentrant_push(3.14f);
put_3.commit();
put_1.commit();
put_2.element() += "world!!!!";
```

```
put_2.cancel();
```

Non-re-entrancy allows to the implementation some optimizations and may alter thread synchronization. For example heter_queue starts non-re-entrant puts in the committed state, so that the actual commit becomes a no-operation (so the observable state of the queue is temporary wrong). A notable further example is conc_heter_queue, that keeps its internal non-recursive mutex locked during a non-re-entrant operation, and locks it twice for re-entrant operations. If the user starts an operation while a non-re-entrant one is still in progress, the mutex would be locked twice by the same thread, causing undefined behavior.

Put functions containing dyn_ in their name are *dynamic*. Dynamic puts inserts elements of a type unknown at compile-time. The type of the element is rather specified by an instance of the type-eraser, passed as first argument. The element can be default-constructed, copy-constructed or move-constructed, provided that the type-eraser supports it.

```
using namespace type_features;
using MyRunTimeType = runtime_type<void, feature_list<default_construct,
destroy, size, alignment>>;
heter_queue<void, MyRunTimeType> queue;
auto const type = MyRunTimeType::make<int>();
queue.dyn_push(type); // appends 0
```

One possible use of dynamic puts is for transferring one element from one queue to another, as shown in the following example:

Put functions containing try_ in their name allows to specify the progression guarantee as first parameter, and returns a (possibly empty) put_transaction. Try put functions are supported only by lf_heter_queue and sp_heter_queue.

The set of put functions is orthogonal: every combination of transnational, re-entrant, dynamic and try is supported. This is a summary of the complete set:

```
• [try ][start ][reentrant ]push
```

- [try_][start_][reentrant_]emplace
- [try][start][reentrant]dyn push
- [try][start][reentrant]dyn push copy
- [try_][start_][reentrant_]dyn_push_move

12 Anatomy of a queue

The storage of a heterogeneous queue is an ordered set of pages and possibly a set of legacy memory blocks. The first page is the *head page*, while the last is the *tail page*. The queue has two pointers: the *head pointer*, that points to a byte of the head page, and the *tail pointer*, that point to a byte of the tail page.

A *value* is alive if it contains a valid element. The element of an alive value must be destroyed soon or later. A value is dead if it does not contain a valid element. Dead values arise from:

- elements whose put was canceled (including those whose constructor threw an exception).
- elements that have been consumed, but still have a storage
- raw blocks

The layout of a value is composed by:

- an instance of *ControlBlock*, an internal structure of the library. It is always present, and holds the pointer 'next' and the state flags of the value.
 - optionally the run-time type object, not present for raw allocations.
 - optionally the element or the raw block

If the queue is not empty, the head pointer points to the first value of the queue. Values are stored as a forward linked-list, and are allocated linearly, in container order, with an implementation-defined granularity (which is the alignment guaranteed for the storage of the value).

If the queue is fully heterogeneous (that is the template argument <code>common_TYPE</code> is <code>void</code>), a control block is composed only by an <code>uintptr_t</code>, that stores the pointer 'next' and some control flags in the least significant bits. If the queue is partially heterogeneous (that is the template argument <code>common_TYPE</code> is not <code>void</code>), the control block includes a pointer to the element. This pointer is necessary because up-casting a pointer in C++ is a non-trivial operation.

From now on we will describe the memory layout and representation of an lf_heter_queue. The details of the other queues are not covered because:

- heter queue has an extremely similar layout, but a much simpler implementation.
- conc heter queue is just a wrapper of an internal heter queue protected by an std::mutex.
- sp_heter_queue shares much of it implementation with lf_heter_queue. The part of implementation not in common will described at the end.

The bottom of the usable space of every page is reserved to the end-of-page control block. This block is allocated always at the same offset from the beginning of the page. Excluding the first control block of the page, all the other control blocks are allocated at variable offsets.

In the end-of-page control block, the pointer to the next control block always points to another page. In all other cases, the pointer to the next control block points to the same page.

There is an implementation defined limitation on the size and alignment of elements that can be allocated in a page. Whenever an element (or raw block) is too big, the queue inserts in the page just a pointer to a legacy heap memory block used as storage for the element.

The least significant bits of m next are used to store the control flags:

```
enum NbQueue_Flags : uintptr_t
{
         NbQueue_Busy = 1,
         NbQueue_Dead = 2,
         NbQueue_External = 4,
         NbQueue_InvalidNextPage = 8,
         NbQueue_AllFlags = NbQueue_Busy | NbQueue_Dead |
NbQueue_External | NbQueue_InvalidNextPage
};
```

The flag 'Busy' is set to a value while a producer is producing it, or while a consumer is consuming it. The flag 'Dead' is set on values not alive. Consumers search for m_next that are not zeroed, and don't have heither 'Busy' or 'Dead'.

While an element is being produced (before commit is called on the put transaction), the element is busy, and not observable. If commit is not called, no one will ever observe any part of that element.

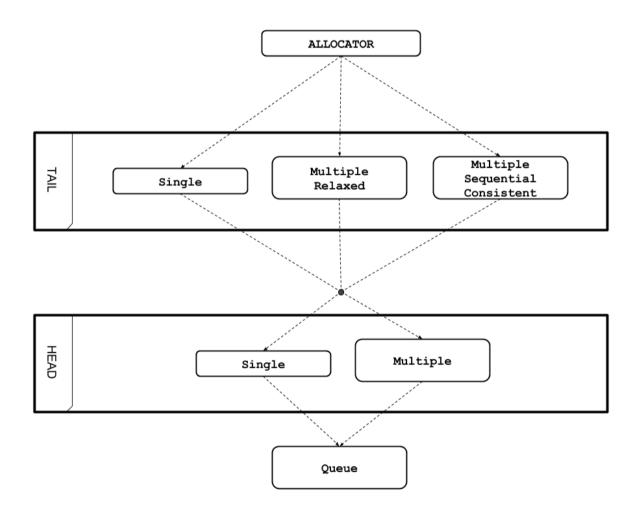
When a thread start consuming an element, it sets the flag 'Busy' on the element. If the consume operation is committed, the element is gone forever. Otherwise, if the consume operation is canceled, the element reappears in the queue (the busy flag is cleared). Since other consumers have observed the absence of the element between the beginning of the consume and the cancel, the consume is not a transaction.

1 Derivation chain

We are implementing the following class template:

```
template < typename COMMON_TYPE = void,
    typename RUNTIME_TYPE = runtime_type<COMMON_TYPE>,
    typename ALLOCATOR_TYPE = void_allocator,
    concurrent_cardinality PROD_CARDINALITY = concurrency_multiple,
    concurrent_cardinality CONSUMER_CARDINALITY = concurrency_multiple,
    consistency_model CONSISTENCY_MODEL = consistency_sequential>
        class lf_heter_queue;
```

To handle such complexity, we split the implementation in 3 class templates. A lf_heter_queue has a four-level private hierarchy.



The queue is internally a null-terminated linked-list, and consumers can iterate it without accessing the tail for the termination condition. So the tail pointer is visible only to producers, and the head pointer is visible only to consumers. Beyond slightly less contention, this simplifies the implementation, and allows to better exploiting reduced cardinalities: if PROD_CARDINALITY is concurrency_single, the tail pointer is not an atomic variable. Similarly, if CONSUMER_CARDINALITY is concurrency single, the head pointer is not an atomic variable.

2 Put operations

The first level is the allocator. In the second level we implement the tail pointer and the put operations in a class template:

The general class template is not defined. The actual implementation is split in 3 template specializations:

One for PROD_CARDINALITY = concurrency_single. This specialization allocates non-zeroed pages, and does not demand zeroing-before-deallocation to consumers.

One for PROD_CARDINALITY = concurrency_multiple and CONSISTENCY_MODEL = consistency_relaxed. This specialization allocates zeroed pages, and does allow (but does not require) zeroing the memory at consume time. This is an optimization: consumers probably have that memory in their cache memories.

One for PROD_CARDINALITY = concurrency_multiple and CONSISTENCY_MODEL = consistency_sequential. This specialization allocates zeroed pages, but does not allow zeroing the memory at consume time, because this would break the algorithm. In this case the cost of zeroing is up to the allocator, since zeroed pages are allocated, but non-zeroed pages are deallocated.

Every specialization of LFQueue_Tail provides this const boolean to communicate to the subsequent layers whether pages should be zeroed at consume time:

```
constexpr static bool s deallocate zeroed pages = ...;
```

Queues that require zeroed pages return zeroed pages to the allocator. While this is not mandatory, it helps the allocator, that would have to write the whole content of the page before reusing it. In contrast the queue can zero the memory while it is probably still in the cache.

The constructor of LFQueue Tail looks the same in all 3 specializations:

```
LFQueue_Tail() noexcept
: m_tail(invalid_control_block()), m_initial_page(nullptr)
{
}
```

We delay the allocation of the first page, so that the default constructor and the move constructor can be noexcept. The value invalid_control_block() is such that it will always cause a page overflow in the first put. When the first page is allocated, it is set to m_initial_page, so that the consume layer can read it during its delayed initialization. After being set to the first allocated page, m_initial_page does not change anymore, even when the page it points to is deallocated.

The code below is the allocation function for an element or raw block in the case of single producer (and non-atomic m tail).

```
// LFQueue Tail<..., concurrency single>::inplace allocate
Block inplace allocate (uintptr t i control bits,
bool i include type, size t i size, size t i alignment)
     if (i alignment < min alignment)</pre>
           i alignment = min alignment;
           i size = uint upper align(i size, min alignment);
     auto tail = m tail;
     for (;;)
           // allocate space for the control block
          void * address = address add(tail,
i include type ? s element min offset : s rawblock min offset);
           // allocate space for the element
           address = address upper align(address, i alignment);
           void * const user storage = address;
           address = address add(address, i size);
           address = address upper align(address, s alloc granularity);
           auto const new tail = static cast<ControlBlock*>(address);
           // check for page overflow
           auto const new tail offset = address diff(new tail,
address lower align(tail, ALLOCATOR TYPE::page alignment) );
           if (DENSITY_LIKELY(new_tail_offset <= s_end_control_offset))</pre>
           {
                // Note: not an atomic store
                new tail->m next = 0;
```

```
auto const control block = tail;
                auto const next ptr =
reinterpret cast<uintptr t>(new tail) + i control bits;
                raw atomic store (&control block->m next,
next ptr, detail::mem release);
                m tail = new tail;
                return { control block, next ptr, user storage };
           else if (i size + (i alignment - min alignment) <=</pre>
s max size inpage)
                tail = page overflow(tail);
           }
           else
           {
                /* this allocation would never fit in a page,
allocate an external block */
                return external_allocate(i control bits, i size,
i alignment);
```

The function LFQueue_Tail::inplace_allocate is the core of all put operations, and is the interface for subsequent layers. It is still a private and low-level function.

This is a good time to make 2 considerations valid for all 3 kinds of tail we are implementing:

We test for the page overflow in the domain of page offset to avoid having to handle the pointer arithmetic overflows.

Since most times all the arguments are compile time constants, an alternate overload template is always provided, to make sure all the possible simplifications are done:

```
template < uintptr_t CONTROL_BITS, bool INCLUDE_TYPE,
size_t SIZE, size_t ALIGNMENT > Block inplace_allocate();
```

This is inplace allocate for the case of multiple producers, but with relaxed consistency:

```
/* LFQueue_Tail<..., concurrency_multiple,
consistency_relaxed>::inplace_allocate */
Block inplace_allocate(uintptr_t i_control_bits,
bool i_include_type, size_t i_size, size_t i_alignment)
{
    if (i_alignment < min_alignment)
    {
        i_alignment = min_alignment;
        i_size = uint_upper_align(i_size, min_alignment);
}</pre>
```

```
auto tail = m tail.load(detail::mem relaxed);
     for (;;)
           // allocate space for the control block
          void * new_tail = address_add(tail,
i include type ? s element min offset : s rawblock min offset);
          // allocate space for the element
          new tail = address upper align(new tail, i alignment);
          void * const user storage = new tail;
          new tail = address add(new tail, i size);
          new tail = address upper align(new tail,
s alloc granularity);
          // check for page overflow
          auto const new tail offset = address diff(new tail,
address lower align(tail, ALLOCATOR TYPE::page alignment));
          if (DENSITY LIKELY(new tail offset <= s end control offset))</pre>
                if (m tail.compare exchange weak(tail,
static cast<ControlBlock*>(new tail),
                      detail::mem acquire, detail::mem relaxed))
                      auto const next ptr = reinterpret cast<uintptr t>(
new tail) + i control bits;
                      raw atomic store (&tail->m next, next ptr,
detail::mem release);
                      return { tail, next ptr, user storage };
          else if (i size + (i alignment - min alignment) <=
s max size inpage)
                tail = page overflow(tail);
          else
                /* this allocation would never fit in a page,
allocate an external block */
                return external_allocate(i_control_bits, i_size,
i alignment);
     }
```

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Unless a page overflow occurs, no page pinning is necessary on the producer side. The reason is that after that a producer allocates space (updating m_tail), that space can never be consumed until it unzeroes it and then removes the busy flag.

The are two reasons why this tail provides relaxed consistency instead of sequential consistency:

Every put updates the tail, and then writes the member m_next of the control-block it has just allocated (m_next is zero before the latter write). In the middle of these two writes, other producers may successfull do other puts (they are not blocked), but for the consumers the queue is truncated to the first zeroed m_next. A put may be temporary not observable even to the thread that successfully has carried it.

Consumers are requested to zero the memory while consuming elements. In some cases of high contention a consumer may see an m_next zeroed by the other consumers, and incorrectly consider it an end-of-queue marker.

The last case of LFQueue_Tail::inplace_allocate is for multiple producers, and sequential consistent queue. Basically we have to solve the two problems above.

In this specialization of LFQueue_Tail we allocate in the pages only values requiring at most a number of bytes equal to the square of s_alloc_granularity. All other values are allocated with a legacy heap allocation.

To solve the problem 1 we adopt a two phases tail update. To solve the problem 2 we just ask to consumers to not zero the memory. Here is the code of the put for sequential consistent queues:

Block inplace allocate(uintptr_t i control_bits, bool i include_type,

```
size t i size, size t i alignment)
     if (i alignment < min alignment)</pre>
           i alignment = min alignment;
           i size = uint upper align(i size, min alignment);
     auto const overhead = i include type ? s element min offset :
s rawblock min offset;
     auto const required size = overhead + i size + (i alignment -
min alignment);
     auto const required units = (required size +
(s alloc granularity - 1)) / s alloc granularity;
     detail::ScopedPin<ALLOCATOR TYPE> scoped pin(this);
     bool const fits in page = required units <</pre>
size min(s alloc granularity, s end control offset /
s alloc granularity);
     if (fits in page)
           auto tail = m tail.load(mem relaxed);
           for (;;)
```

```
auto const rest = tail & (s alloc granularity - 1);
                if (rest == 0)
                      // we can try the allocation
                      auto const new control =
reinterpret cast<ControlBlock*>(tail);
                      auto const future tail = tail +
required_units * s alloc granularity;
                      auto const future tail offset = future tail -
uint lower align(tail, page alignment);
                      auto transient tail = tail + required units;
                      if (DENSITY LIKELY(future tail offset <=</pre>
s end control offset))
                           if (m tail.compare exchange weak(tail,
transient tail, mem relaxed))
                                 raw atomic store(&new control->m next,
future tail + i control bits, mem relaxed);
     m tail.compare exchange strong(transient tail,
future tail, mem relaxed);
                                 auto const user storage =
address upper align (
address add(new control, overhead), i alignment);
return { new control, future tail +
i_control_bits, user_storage };
                      }
                      else
                      {
                           tail = page overflow(tail);
                }
                else
                      // an allocation is in progress, we help it
                      auto const clean tail = tail - rest;
                      auto const incomplete control =
reinterpret_cast<ControlBlock*>(clean_tail);
                      auto const next = clean tail + rest *
s alloc granularity;
                      if (scoped pin.pin new(incomplete control))
                      {
                           auto updated tail = m tail.load(mem relaxed);
                           if (updated tail != tail)
                                 tail = updated tail;
```

A producer starts analyzing the value of m_tail. If it is multiple of s_alloc_granularity, then there is no other put in progress. So it:

- Adds to m_tail the required size in bytes divided by s_alloc_granularity. This is enough to make other consumers realize that a put is in progress, and how much memory this put is allocating.
- Setups the control block (that is sets m next)
- Sets m tail to point after the allocation
- Otherwise, if the tail is not multiple of s_alloc_granularity, the thread first completes the put in progress, and then try to do its own put.