My Final College Paper

 $\label{eq:control} \mbox{A Thesis}$ $\mbox{Presented to}$ $\mbox{The Division of Mathematics and Natural Sciences}$ $\mbox{Reed College}$

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Preface

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Abstract

Dedication

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Chapter 4

Prefix Sums

Let $a = a_0, a_1, a_2, \dots a_{n-1}$ be a finite sequence of n numbers. The prefix sum of a, is a sequence $b = b_0, b_1, b_2, \dots b_{n-1}$ where $b_i = \sum_{a=0}^{i} a_i$.

Note that the following equivalence holds for all i > 0:

$$b_i = b_{i-1} + a_i$$

The preceding equation strongly suggests a natural algorithm to compute prefix sums on a sequential computer. The first element of the output sequence serves as a kind of base case; since there is only one element in our sum, no operations need to be performed, and so we can simply copy the first element of the input sequence to the first element of the output sequence. To compute the remaining values of the output sequence we iterate through the input sequence starting at the second element. As the equivalence above suggests, we compute the corresponding value in the output sequence by adding the value of the input sequence (a_i) , to the value we just computed (b_{i-1}) . The following is a pseudocode implementation of the aforementioned algorithm, where both the input and output sequences are stored in memory as arrays (the reader should note that other data structures like linked lists are amenable to the same algorithm with only trivial modifications).

Algorithm 1 A sequential implementation of the prefix sum operation.

```
output[0] \leftarrow input[0]

for i = 1 \rightarrow n - 1 do

output[i] \leftarrow output[i-1] + input[i]

end for
```

It is easy to see that it is impossible to come up with an algorithm that performs better than this one in any reasonable model of sequential computation. There is simply no way to circumvent the n-1 addition operations performed in this algorithm as the final value of our output sequence is the sum of n values (which cannot be computed with fewer than n-1 additions operations). Similarly, each item in the input sequence must be read, and a value must be stored to each location in the output

sequence. As we have accounted for every operation performed in the algorithm, we conclude that it is optimal within the model of computation we are using.

The reader should note that the prefix sum operation can be generalized to use any binary associative operator in the place of addition. For example, it is possible to compute the prefix minimums of a sequence, since the operation of minimizing two numbers is associative.

Given that the sequential implementation of prefix sums is so natural, it is tempting to conclude that something about the operation is inherently sequential. After all, there is no getting around the fact that to calculate the ith value of the output sequence i-1 operations must be preformed. Surprisingly, it turns out that there are a family of parallel algorithms that compute prefix sums in an amount of time that is asymptotically than the amount of time taken by the sequential algorithm.

4.1 Parallel Prefix Sums

The following section culminates in the description of an alogorithm that efficiently computes the prefix sums of an arbitrary sequence on a PRAM architecture with any number of processors.

For the time being, we make the simplifying assumption that the number of processors available to us, p, is half the number of elements in our sequence n. Furthermore, we assume that $p = n = 2^k$ for some integer value of k.

We begin by describing a parallel algorithm for the left fold operation, which is a close relative of prefix sums/scan operation.

Like scan, parallel-left-fold takes a sequence $a = a_0, a_1, a_2, \dots a_n$ together with a binary associative operator, \oplus . The output of this operation is the single value

$$a_0 \oplus a_1 \oplus a_2 \oplus \ldots \oplus a_{n-1}$$

When the binary operation provided is conventional addition, parallel-left-fold produces the total sum of the input sequence. One might say that parallel-left-fold is a simplified version of scan where only the last value of the sequence is computed.

Consider that for any associative binary operator \oplus :

$$a_0 \oplus a_1 \oplus a_2 \oplus \ldots \oplus a_{n-1} = (a_0 \oplus a_1) \oplus (a_2 \oplus a_3) \oplus \ldots \oplus (a_{n-2} \oplus a_{n-1})$$
 (4.1)

This simple rewrite suggests an equivalence between the sequences a and $a' = (a_0 \oplus a_1), (a_2 \oplus a_3), \dots, (a_{n-2} \oplus a_{n-1})$, in terms of parallel-left-fold.

$$\operatorname{parallel-left-fold}(a, \oplus) = a_0 \oplus a_1 \oplus a_2 \oplus \ldots \oplus a_{n-1} = (a_0 \oplus a_1) \oplus (a_2 \oplus a_3) \oplus \ldots \oplus (a_{n-2} \oplus a_{n-1}) = \operatorname{parallel-left-fold}(a', \oplus)$$
(4.2)

Note that while the way we described a' depends on the assumption that n is even, the equivalence above also holds for n odd when we set the final element of a' to be a_{n-1} .

The equivalence (4.2) implies that the problem of calculating parallel-left-fold on a sequence of length |a|=n can be reduced to the problems of calculating the sequence a' from a, and calculating parallel-left-fold of a sequence of size $|a'| = \lceil \frac{n}{2} \rceil$. This observation can be transformed in to a precise statement about the runtime of parallel-left-fold:

$$T(n) = T(n/2) + A(n)$$
 (4.3)

Where T(n) is the running time of parallel-left fold and A(n) is the amount of time it takes to calculate the sequence a' from a.

Note that we can apply this identity to itself to produce another reduction of our problem, T(n) = T(n/4) + A(n/2) + A(n). The idea behind parallel-left-fold is to recursively apply this reduction until the sequence we are left with a sequence of length one. The only term in this sequence will be the value of parallel-left-fold of the original sequence. This algorithm can be represented visually in the form of a balanced binary tree.

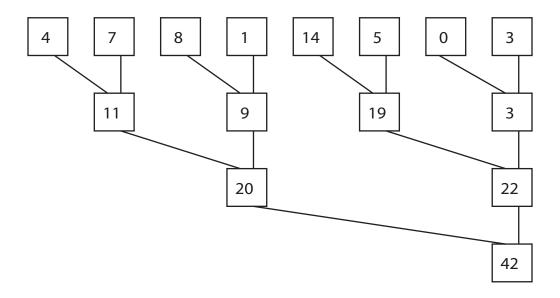


Figure 4.1: An illustration of the successive reduction of parallel-left-fold where \oplus is set to be addition.

This figure is drawn with the parents right aligned to indicate that the result of the addition ends up overwriting the slot originally occupied by the second summand. The bottom-most value in each column of the graphical representation will be the final value stored in the temporary array that stores intermediate sums.

An explicit procedure to determine which processor performs each \oplus operation must be devised to complete this implementation of parallel-left-fold. Note that each level in this tree represents a reduction of the original input sequence. Since we have a balanced binary tree, the number of nodes at the *j*th level (starting at 0) of the tree is $\lceil \frac{n}{2^j} \rceil$. Thus we need $\lceil \frac{n}{2^j} \rceil$ processors to perform the *i*th (starting at 1) reduction

of our sequence. Note that there are exactly $\lceil \frac{p}{2^j} \rceil = \lceil \frac{n}{2^{j+1}} \rceil$ numbers l in the range 0 to p-1 for which $(l \mod 2^j = 0)$. This means that the modulus operator can be used, together with a variable that is multiplied by two in each iteration to select which processors will be active. Since all p of our processors must be active in the first round, we will initialize this variable to the value of 1. Note that this value also corresponds to the distance between the values that each processor will add in any particular reduction. The following pseudo-code describes the rest of the details of the implementation.

```
Algorithm 2 parallel-left-fold where p = n/2 and n = 2^k

values[pid] \leftarrow input[2 \cdot pid] \oplus input[2 \cdot pid + 1]

stride \leftarrow 1

while pid mod \frac{n}{\text{stride}} = 0 and stride < n do

values[pid] \leftarrow values[pid] \oplus values[pid + stride]

stride \leftarrow stride·2

end while

return values[0]
```

Since each of the computations at a particular depth in the tree above are computed in lock step, the height of the tree is the running time of parallel-left fold. Since the tree above is a balanced binary tree with n leaves it has height $\lceil log_2 n \rceil$. This result can also be obtained by solving the recurrence relation (4.3) with the base case T(1) = 0.

In computing parallel-left-fold in this manner, many of the partial sums that constitute the output or parallel-prefix-sums are computed.

Specifically, all the partial sums that are placed at output indices 2^a-1 for positive integers a are correct. The other sums that are computed as intermediaries to the final sum are incomplete in the sense that they are the correct prefix sums for some subsequence of the input sequence.

The partial sum at the *i*th element of the output array after a reduce operation is the sum of the subsequence going back 2^j elements where j is the last $\max_k \{i \mod 2^k\}$

end while

Algorithm 3 parallel-prefix-sums where p = n/2 and $n = 2^k$ output[pid] \leftarrow input[$2 \cdot$ pid] \oplus stride $\leftarrow 1$ while pid mod $\frac{n}{\text{stride}} = 0$ and stride < n do values[pid] \leftarrow values[pid] \oplus values[pid + stride] stride \leftarrow stride·2end while return myitem \leftarrow pid·2segmentsize = n/2while stride > 0 do if myitem mod segmentsize = 0 and pid! = 0 then values[myitem -1 + segmentsize/2] += values[myitem-1] end if segmentsize = segmentsize/2

Conclusion

Appendix A The First Appendix

References

- Angel, Edward. Interactive Computer Graphics: A Top-Down Approach with OpenGL. Boston, MA: Addison Wesley Longman, 2000.
- ——. Batch-file Computer Graphics: A Bottom-Up Approach with QuickTime. Boston, MA: Wesley Addison Longman, 2001.
- Deussen, Oliver and Strothotte, Thomas. "Computer-Generated Pen-and-Ink Illustration of Trees." "Proceedings of" SIGGRAPH 2000: 13–18.
- Fisher, Robert, Perkins, Simon, Walker, Ashley, and Wolfart, Erik. *Hypermedia Image Processing Reference*. New York, NY: John Wiley & Sons, 1997.
- Gooch, Bruce and Gooch, Amy. *Non-Photorealistic Rendering*. Natick, Massachusetts: A K Peters, 2001.
- Hertzmann, Aaron and Zorin, Dennis. "Illustrating Smooth Surfaces." *Proceedings* of SIGGRAPH 2000 5.17 (2000): 517–526.
- Jain, Anil K. Fundamentals of Digital Image Processing. Englewood Cliffs, New Jersey: Prentice-Hall, 1989.
- Molina, S. T. and Borkovec, T. D. "The Penn State Worry Questionnaire: Psychometric properties and associated characteristics." *Worrying: Perspectives on theory, assessment and treatment.* eds. G. C. L. Davey and F. Tallis. New York: Wiley, 1994. 265–283.
- Noble, Samuel G. Turning images into simple line-art. Undergraduate thesis, Reed College, 2002.
- Reed College. "LaTeX Your Document." 2007. http://web.reed.edu/cis/help/LaTeX/index.html
- Russ, John C. *The Image Processing Handbook, Second Edition*. Boca Raton, Florida: CRC Press, 1995.
- Salisbury, Michael P., Wong, Michael T., Hughes, John F., and Salesin, David H. "Orientable Textures for Image-Based Pen-and-Ink Illustration." "Proceedings of" SIGGRAPH 97: 401–406.

20 References

Savitch, Walter. JAVA: An Introduction to Computer Science & Programming. Upper Saddle River, New Jersey: Prentice Hall, 2001.

Wong, Eric. Artistic Rendering of Portrait Photographs. Master's thesis, Cornell University, 1999.