



Organización y diseño de computadoras

Jiménez Sánchez / Ismael

Rodriguez Noh Rogelio Iván

Tarea #991: Circuitos digitales

30/03/2021

NOT

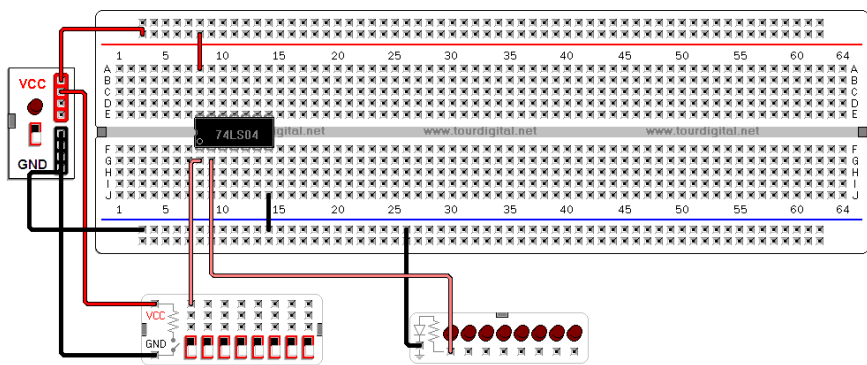


Tabla de verdad

Input		Output
A		NOT A
0		1
1		0

AND (2 Entradas)

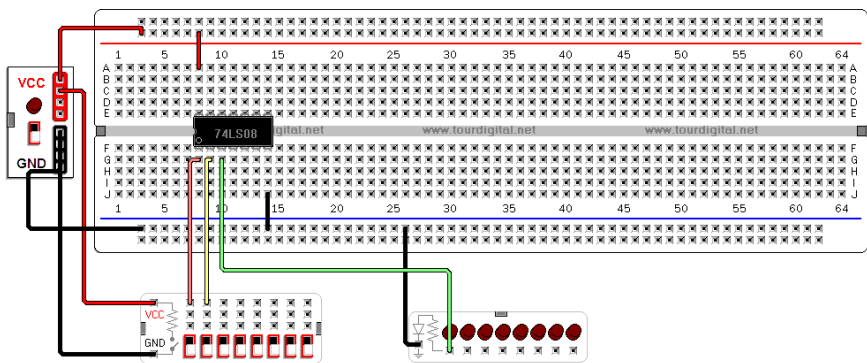


Tabla de verdad

Input		Output
A	B	A AND B

0	0	0
0	1	0
1	0	0
1	1	1

AND (3 Entradas)

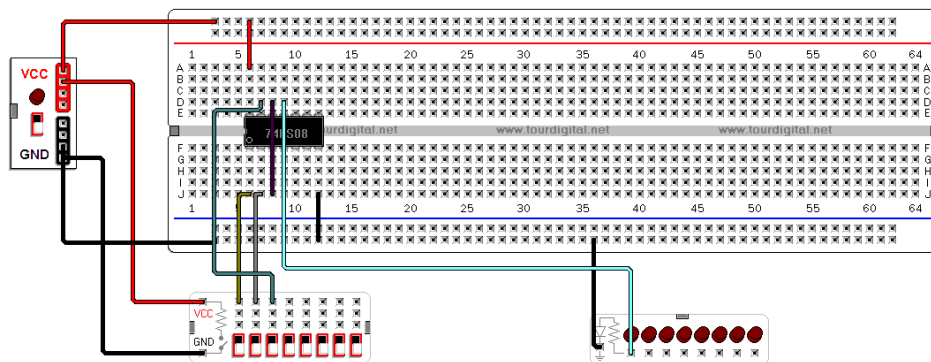
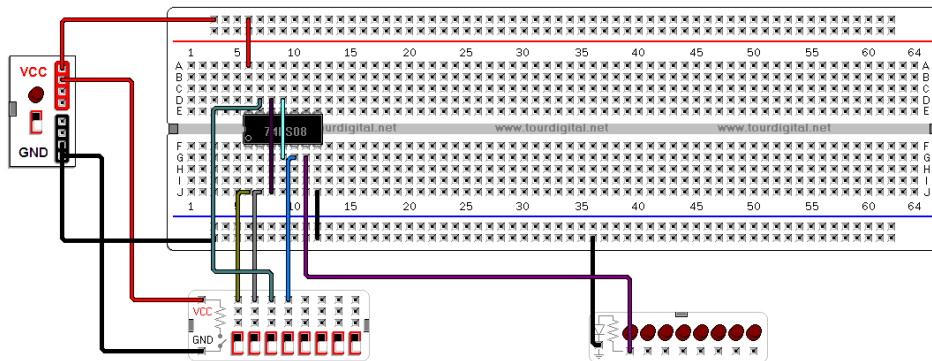


Tabla de verdad

Input			Output
A	B	C	(A AND B) AND C
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0

1	1	1	1
---	---	---	---

AND (4 ENTRADAS)



Input				Output
A	B	C	D	
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0

1	0	1	1	0
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	1

NAND (2 Entradas)

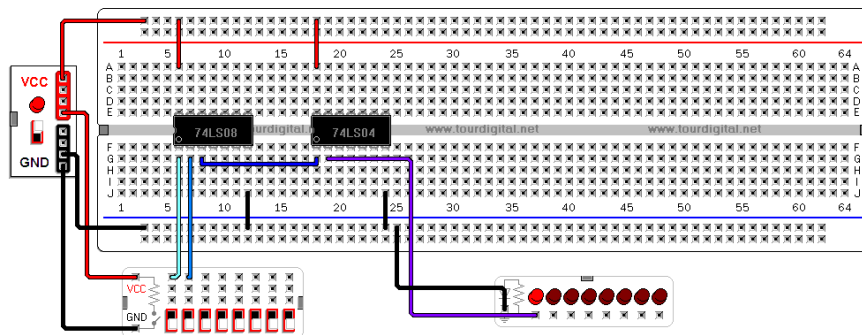


Tabla de verdad

Input		Output
A	B	
0	0	0
0	1	1
1	0	1
1	1	0

NAND (3 Entradas)

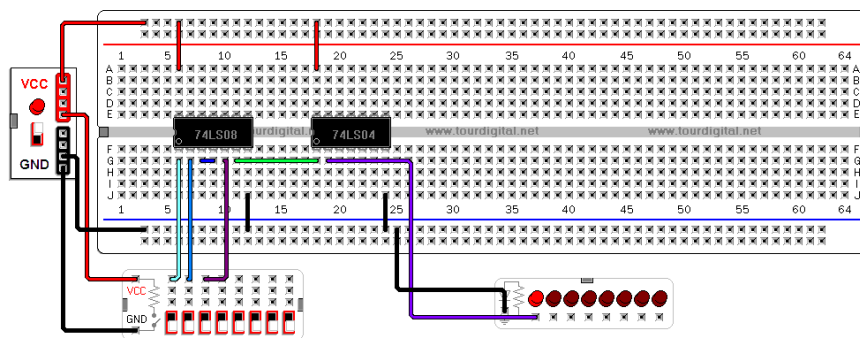
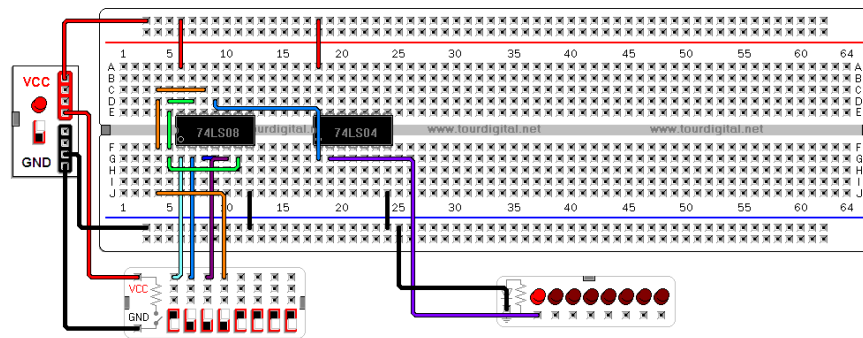


Tabla de verdad

Input			Output
A	B	C	
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

NAND (4 ENTRADAS)



Input				Output
A	B	C	D	
0	0	0	0	1
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	1
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	1
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	1

1	1	0	1	1
1	1	1	0	1
1	1	1	1	0

NOR (2 Entradas)

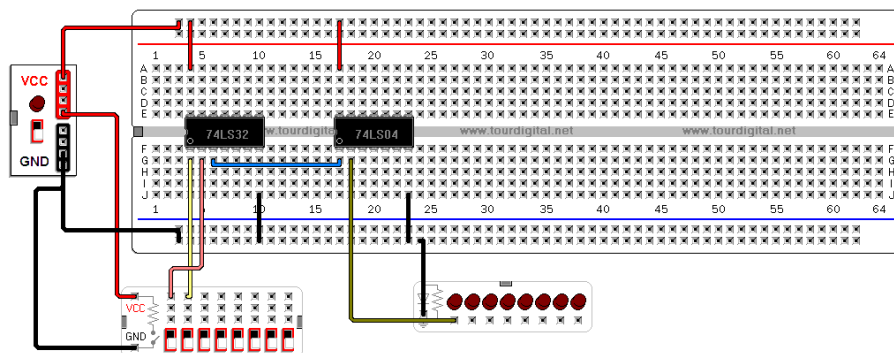


Tabla de verdad

Input		Output
A	B	
0	0	1
0	1	0
1	0	0
1	1	0

NOR (3 Entradas)

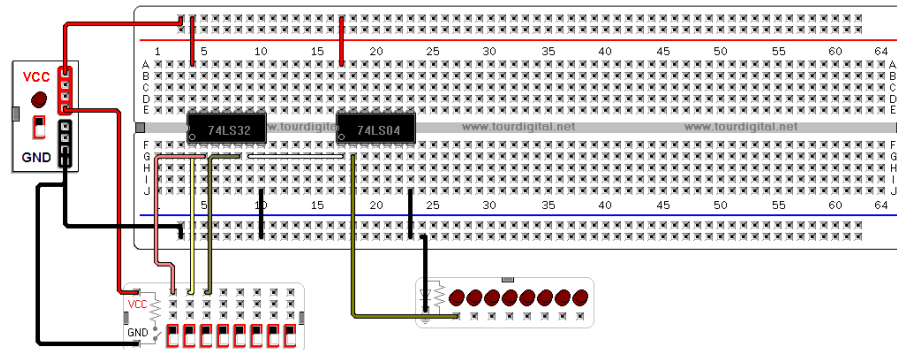


Tabla de verdad

Input			Output
A	B	C	
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

OR (2 Entradas)

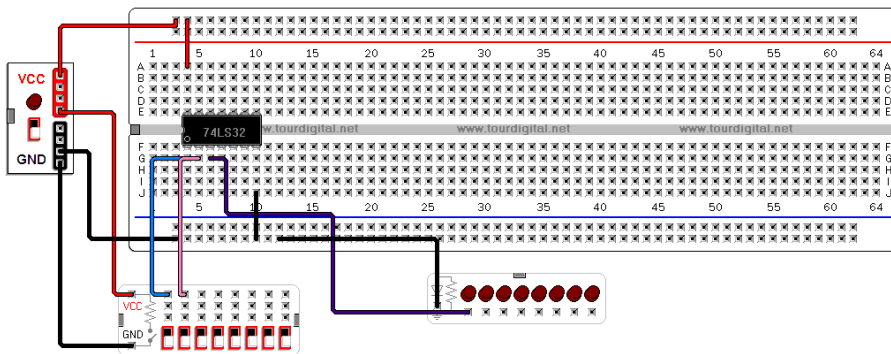


Tabla de verdad

Input		Output
A	B	
0	0	0
0	1	1
1	0	1
1	1	1

XOR (2 Entradas)

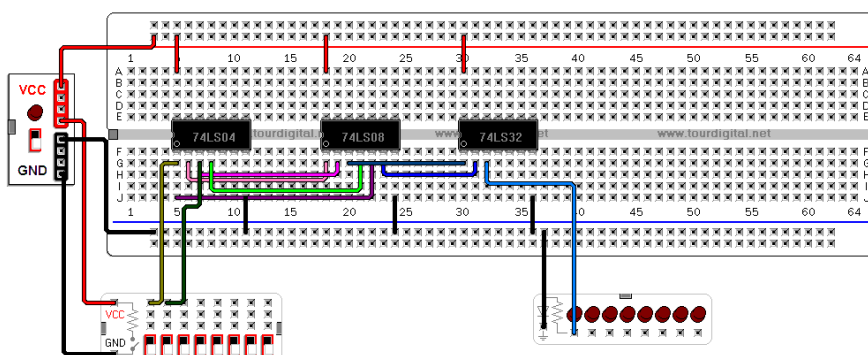


Tabla de verdad

Input		Output
A	B	
0	0	0
0	1	1
1	0	1
1	1	0