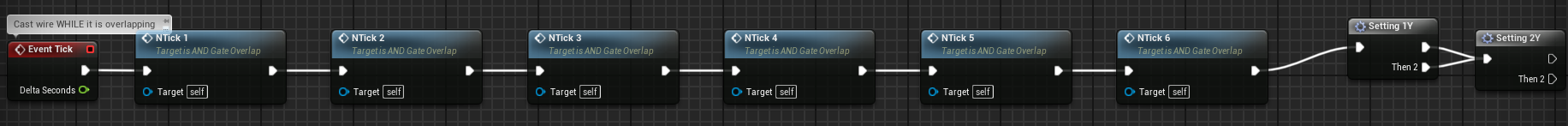
Design: Michael DiNardi, Acacia Mastropaolo

AND gate overlap (same event graph for all overlap chips except NOT gate which has different number of inputs and outputs. Additionally, the Event ticks have different names on different graphs)

A screenshot of a computer

Description automatically generated with medium confidence



Graphical user interface

Description automatically generated

(same basic connection for each Reading box in the screenshot above but replaced with specific Reading xx, NTickx and On Component Begin/End Overlap (Box\_pinxx))

Reading 1A (same for all overlap chips)

A screenshot of a video game

Description automatically generated

Reading 1B (same for all overlap chips (except NOT gate which has no Read 1B))

A screenshot of a video game

Description automatically generated with medium confidence

Reading 1A, 1B, 2A, 2B, Vcc have same blueprint except for the isOverlapping and Input Array Indexes, and the Set material target changes according to the pin that is being read.

Reading 2A (same for all overlap chips (except NOT gate which has different array index))

A screenshot of a video game

Description automatically generated with medium confidence

Reading 2B (same for all overlap chips (except NOT gate which has no Reading 2B))

Graphical user interface

Description automatically generated

Reading Vcc (same for all overlap chips)

Graphical user interface

Description automatically generated

Reading GND (same for all overlap chips) (different from rest of Read Macros because it NOTs wire input)

Graphical user interface

Description automatically generated

AND Setting 1Y. Setting 1Y same as Setting 2Y except for Index values and Set material pin Target

Graphical user interface

Description automatically generated

AND Setting 2Y

A picture containing text, indoor, tiled

Description automatically generated

AND 2Y performing Boolean function Graphical user interface, diagram

Description automatically generated

OR Setting 1Y

A screenshot of a computer

Description automatically generated with low confidence

Diagram, schematic

Description automatically generated

OR Setting 2Y

A screenshot of a computer

Description automatically generated with low confidence

XOR Setting 1Y

A screenshot of a computer

Description automatically generated with low confidence

XOR Setting 2Y

A picture containing text, indoor

Description automatically generated

NOR Setting 1Y

A screenshot of a computer

Description automatically generated with low confidence

A screenshot of a computer

Description automatically generated with low confidence

NOR Setting 2Y

Graphical user interface

Description automatically generated

NAND Setting 1Y

A screenshot of a video game

Description automatically generated with medium confidence

NAND Setting 2Y

A screenshot of a computer

Description automatically generated with low confidence

Diagram

Description automatically generated

NOT Overlap Ship Event Graph

A screenshot of a computer

Description automatically generated with medium confidence

NOT Reading 2A

Graphical user interface

Description automatically generated with low confidence

NOT Reading 3A

Graphical user interface

Description automatically generated

NOT Setting 1Y

A picture containing text, indoor

Description automatically generated

NOT Setting 2Y

A screenshot of a computer

Description automatically generated with low confidence

NOT Setting 3Y

A screenshot of a computer

Description automatically generated with low confidence

A screenshot of a video game

Description automatically generated with medium confidence