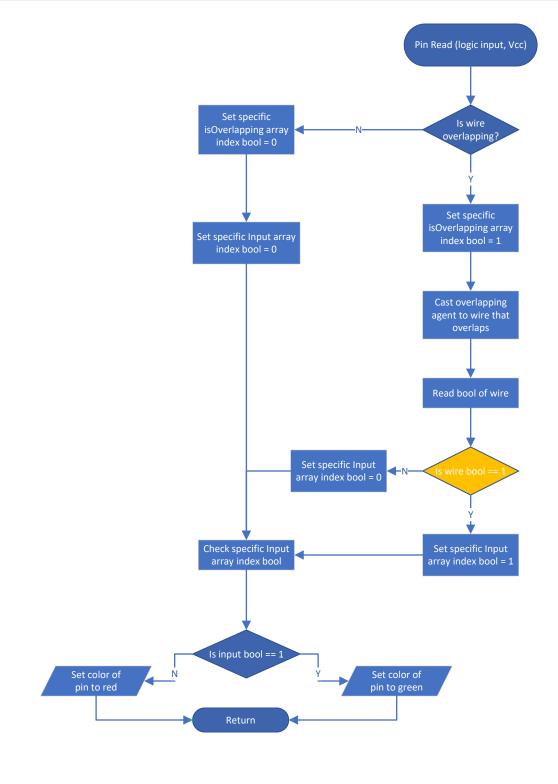


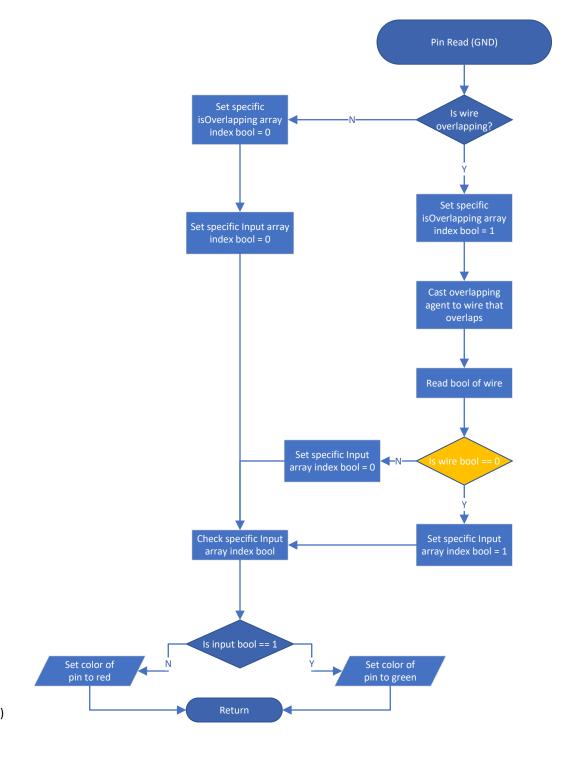
Acacia Mastropaolo (Des. Michael DiNardi, Acacia Mastropaolo)
Logic Components

Group 5: 3D Design Systems

Overlap Chip Main

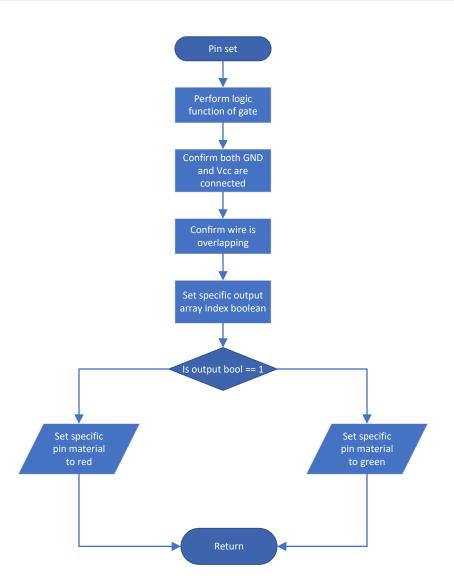


Acacia Mastropaolo (Des. Michael DiNardi, Acacia Mastropaolo) Logic Components Group 5: 3D Design Systems Overlap Chip Pin Read for Logic Inputs, Vcc



Acacia Mastropaolo (Des. Michael DiNardi, Acacia Mastropaolo) Logic Components Group 5: 3D Design Systems

Overlap Chip Pin Read for GND



Acacia Mastropaolo (Des. Michael DiNardi, Acacia Mastropaolo) Logic Components Group 5: 3D Design Systems Overlap Chip Pin Set