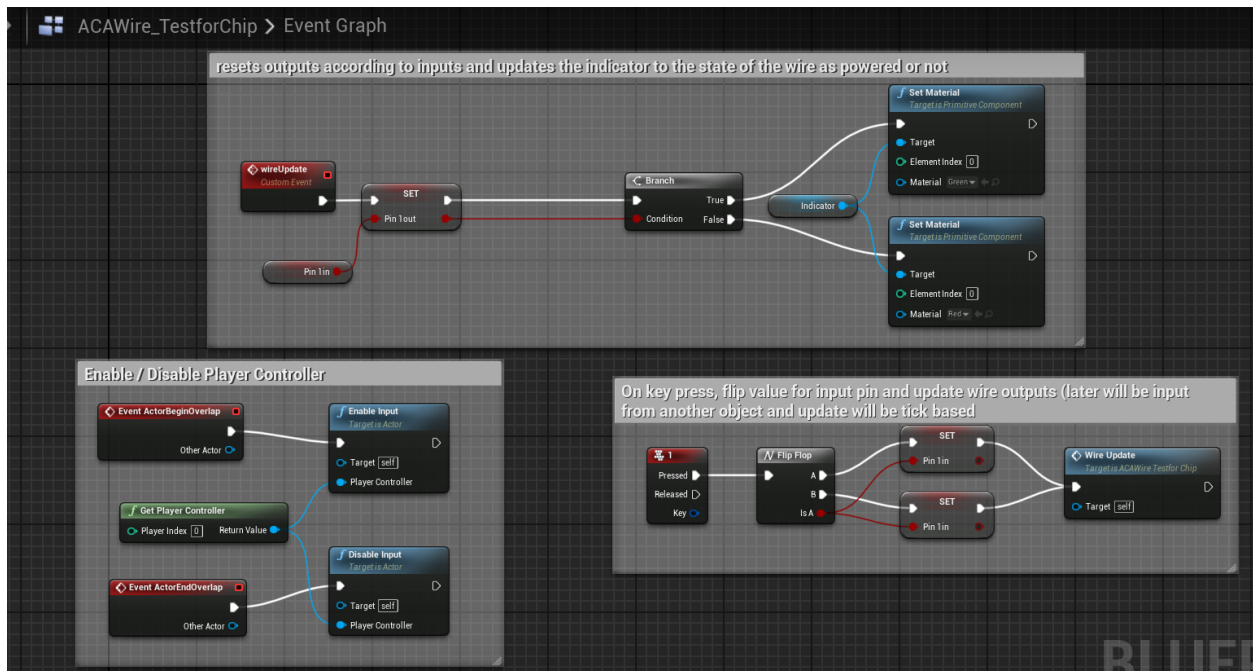


Wire Test for Chip (ver.ACAWire_TestforChip) Design Peter Estep, Acacia Mastropaolo



Wire test for chip (ver. Wire_TestforChip) Design Peter Estep

