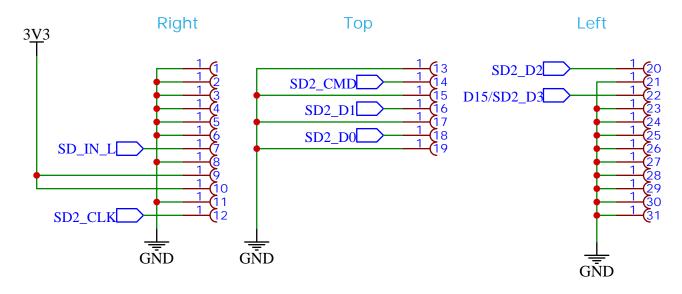


Castellations



Stackup/Controlled Impedance:

SD Signals: 50

Outer layer: 0.29mm width Inner layer: 0.23mm width JLCPCB Stackup: JLC7628

TI	TLE: Sheet	_1	REV: v1.b
		Company: Wilderness Labs	Sheet: 1/1

Drawn By: bryan costanich

2022-03-11