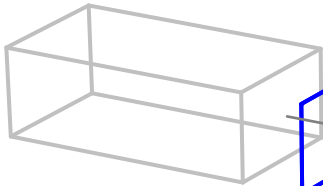
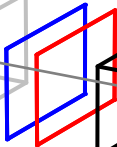


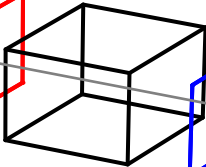
Input



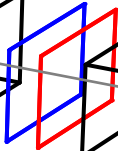
Batch Norm
ReLU



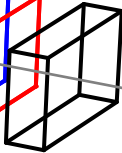
1×1 Conv (bottleneck)



Batch Norm
ReLU



Conv



Output