

# Final Report

## Parallel Computing for Computational Mechanics

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**Abstract:** Lorem ipsum dolor sit amet, consectetur adipiscing elit, sed diam nonummy eirmod tempor invidunt ut labore et dolore magna aliquyam erat, sed diam voluptua. At vero eos et accusam et justo duo dolores et ea rebum. Stet clita kasd gubergren, no sea takimata sanctus est Lorem ipsum dolor sit amet. Lorem ipsum dolor sit amet, consetetur sadipscing elitr, sed diam nonummy eirmod tempor invidunt ut lre et dolore magna aliquyam erat, sed diam voluptua. At vero eos et accusam et justo duo dolores et ea rebum. Stet clita kasd gubergren, no sea takimata sanctus est Lorem ipsum dolor sit amet. Lorem ipsum dolor sit amet, consetetur sadipscing elitr, sed diam nonummy eirmod tempor invidunt ut labore et dolore magna aliquyam erat, sed diam voluptua. At vero eos et accusam et justo duo dolores et ea rebum. Stet clita kasd gubergren, no sea takimata sanctus est Lorem ipsum dolor sit amet.

## 1 Introduction

The parallelization of code for the simulation of complex mechanical systems is a key-topic in modern computational mechanics. Constantly increasing computing power of high-performance clusters allow for the simulation of large and complex systems. This requires an efficient parallelization approach, where different parallelization paradigms can be of use.

In this report the parallelization of a finite element code for the simulation of the stationary temperature distribution on a two-dimensional disk is investigated. For the shared memory approach the OpenMP library is used, where as the distributed memory approach is assessed by using a one-sided parallelization approach with MPI. The different parallelization approaches are discussed and compared for different number of CPUs. This report is structured as follows. In the second chapter *Theory and Methods* are presented. The *Implementation and Validation* of the presented problem is discussed in chapter 3. The results of the optimization of the runtime of the code is illustrated in chapter 4 *Results* and the assessment of the different optimization techniques is discussed in chapter 5 *Discussion*. Finally, this report is closed with a *Conclusion* in chapter 6.

serial optimization is performed by

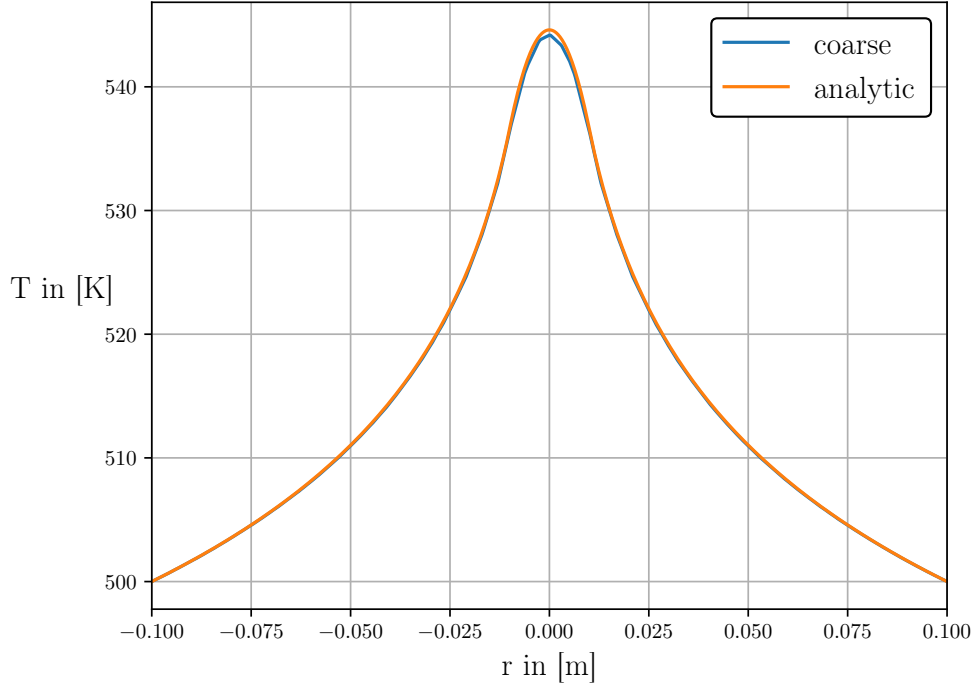
## 2 Theory and Methods

- describe different meshes

## 3 Implementation and Validation

### 3.1 R1 a)

For the validation of the FEM solver, the solution of the coarse mesh is compared to the analytical solution in Fig. 1. The numeric solution is in good agreement with the analytical solution. Slight deviations are observed at the disk's center where the temperature distribution has it's global maximum. Accounting for large changes in the temperature gradient at the center of the disk.



**Figure 1:** Comparison of the steady-state temperature distribution of the analytical solution (refEQ) and the numerical solution on the coarse mesh

### 3.2 Parallelization with OpenMP

A critical region is to be executed by only one thread at a time. This is important to prevent data races. A data race means that multiple threads try to access and manipulate the same variable / memory address at the same time. This might lead to unpredictable behavior, which can result in incorrect computations.

**Listing 1:** Parallelization of two most time consuming loops with implementation A and B

```

1  for (int iter=0; iter<nIter; iter++)
2  {
3      // clear RHS MTnew
4      A) #pragma omp parallel for
5      for(i=0; i<nn; i++){
6          MTnew[i] = 0;
7      }
8      A) #pragma omp parallel firstprivate(MTnew)
9      B) #pragma omp parallel
10     {
11         // Evaluate right hand side at element level
12         A) #pragma omp for private(elem, M, F, K, TL, i, MTnewL)
13         B) #pragma omp for private(elem, M, F, K, TL, i, MTnewL)
14         B) reduction(+: MTnew[0:nn]) schedule(dynamic,512)
15         for(int e=0; e<ne; e++)
16         {
17             elem = mesh->getElem(e);
18             M = elem->getMptr();
19             F = elem->getFptr();
20             K = elem->getKptr();
21             for(i=0; i<nen; i++)
22             {
23                 TL[i] = T[elem->getConn(i)];

```

```
24         }
25
26         MTnewL[0] = M[0]*TL[0] + dT*(F[0]-(K[0]*TL[0]+...));
27         MTnewL[1] = M[1]*TL[1] + dT*(F[1]-(K[3]*TL[0]+...));
28         MTnewL[2] = M[2]*TL[2] + dT*(F[2]-(K[6]*TL[0]+...));
29
30
31         // RHS is accumulated at local nodes
32         A) #pragma omp critical
33         MTnew[elem->getConn(0)] += MTnewL[0];
34         A) #pragma omp critical
35         MTnew[elem->getConn(1)] += MTnewL[1];
36         A) #pragma omp critical
37         MTnew[elem->getConn(2)] += MTnewL[2];
38     }
39     // Evaluate the new temperature on each node on partition level
40     partialL2error = 0.0;
41     globalL2error = 0.0;
42     A) #pragma omp for private(pNode, massTmp, MT, Tnew)
43     B) #pragma omp for private(pNode, massTmp, MT, Tnew)
44     B) reduction(+:partialL2error) schedule(dynamic,512)
45     for(int i=0; i<nn; i++)
46     {
47         pNode = mesh->getNode(i);
48         if(pNode->getBCtype() != 1)
49         {
50             massTmp = massG[i];
51             MT = MTnew[i];
52             Tnew = MT/massTmp;
53             A) #pragma omp critical
54             partialL2error += pow(T[i]-Tnew,2);
55             T[i] = Tnew;
56         }
57     }
58 }
59
60 globalL2error = sqrt(partialL2error/nn);
```

---

### 3.3 Parallelization with MPI

## 4 Results and Discussion

### 4.1 Serial Solver

To run the finite element solver as efficient and fast as possible in parallel, it is important that the serial performance is optimized. Therefore, the effect on the runtime of multiple compiler flags is compared in this section.

#### 4.1.1 R1 a)

In order to decrease the serial runtime of the implemented solver, different compiler flags were tested and assessed by comparing their influence on the solver runtime. The code is compiled with the Intel C++ Compiler (v19.1). The compiled code is tested on a Intel Platinum 8160 which is based on Intel's Skylake architecture using the "medium" unstructured mesh with 21650 nodes resulting in 21332 triangular elements.

The investigated compiler flags are contained in Tab. 1. In the following subsection the individual compiler flags are briefly presented and explained:

### **-O0**

The capital number behind the capital "O" refers to the optimization level and performs general optimizations by bundling several individual compiler flags. Using the base Level (0), the compiler will disable all optimizations. This is useful for debugging (section R1 b).

### **-O1**

Optimization Level 1 includes the optimization for speed, but disables all optimizations that increase code size and affect the codes speed. It includes the analysis of data-flow, code motion, strength reduction and further performance enhancing options.

### **-O2**

The second optimization level includes vectorization, which allows for concurrent execution of the separate steps that are necessary to perform different basic mathematical operations on array-like data structures. Such as an element wise addition of an array. A scalar or "Von Neumann" processor would simply perform all four necessary steps for each index step by step. Where as a vector processor allows for concurrent operations. This prevents that instruction units are idle, while waiting on a task to be finished.

The option also enables the inlining of intrinsics and intra-file inlining of functions. Which means the replacement of the function call with the instructions inside of a certain function itself. This can cause significant performance gain, especially if functions are called multiple times inside the body of a loop.

### **-O3**

Performs O2 optimizations with higher thresholds and enables more aggressive loop transformations such as Fusion, Block-Unroll-and-Jam, and collapsing IF-statements. It can be used with the option "-ax" to force the compiler to do a more aggressive data dependency analysis and optimize for a certain CPU architecture. Where "x" refers to the CPU's instruction set.

### **-Ofast**

On Linux systems this sets "-O3 -no-prec-div -fp-model fast=2". This causes the compiler to enhance the code with alle possible optimizations of "-O3" and reduce the precision of floating-point divides.

### **-ax arch**

Optimization of instruction set for a specific hardware architecture of the CPU. As the simulations were conducted on a two-socket node with two Intel Platinum 8160 with each 24 cores, based on the Skylake architecture. The shortest runtime was achieved with the flag "-axSKYLAKE-AVX512". Where AVX512 is the abbreviation for *Advanced Vector Extensions* with 512-bit instructions and is an extension of the SIMD instruction set.

### **-fp-model fast**

This flag controls the precision of floating-point operations. Using Level 2, enables a more aggressive optimization of floating-point calculations. This might decrease accuracy.

**-ipo**

Enables the inter-procedural optimization between files functions from different files may be inlined.

**-unroll**

Performs loop unrolling which helps to exploit multiple instruction units, by increasing the stride length of the loop variable and performing the same operation on multiple array elements in one stride.

[1, 2, 3, 4]

The runtimes that were measured for the different compiler flags are shown in Tab. 1. For each flag, three measurements on a single core with a single thread were taken and the average time was determined. The shortest runtime is achieved by using the compiler flag combination "-O3 -axSKYLAKE-AVX512" resulting in an average runtime of 54.77 seconds which is 4.37 times faster than the not optimized code (-O0). For further investigations, assessing the parallel optimization with OpenMP and MPI, the "-O3 -axSKYLAKE-AVX512" flag combination was used.

compiler flag	time 1	time 2	time 3	average time
-O0 (no optimisation)	233.71	241.5	243.02	239.41
-O1	62.41	63.71	62.76	62.96
-O2	56.96	55.39	57.11	56.49
-O3	55.49	55.23	57.8	56.17
-O3 -axSSE4.2, SSSE3, SSE2 -fp-model fast=2	56.17	56.21	56.46	56.18
-O3 -fp-model fast=2	57.69	56.19	57.89	57.26
-O3 -axSKYLAKE-AVX512	53.99	54.06	56.25	<b>54.77</b>
-O3 -ipo	54.57	57.26	60.38	57.40
-unroll	55.17	57.01	55.94	56.04
-Ofast	55.43	56.17	56.27	55.96

**Table 1:** Timings of the various compiler flags for serial optimization, the shortest achieved runtime is printed in bold font

**4.1.2 R1 b)**

A higher level of optimization can cause a decrease in the accuracy of the floating-point operations. For the validation of a certain code or high fidelity simulations, like the implemented FEM Solver, one is advised to minimize the numerical error, by using the highest level of accuracy. So the optimizations should not inflict with the desired results. Also for debugging purposes, optimization is not helpful as

the compiler changes the code structure, when using more aggressive optimizations. To name a few effects: functions are inlined, loops are unrolled or fused together. This makes it very hard to search for bugs, when using debugging software.

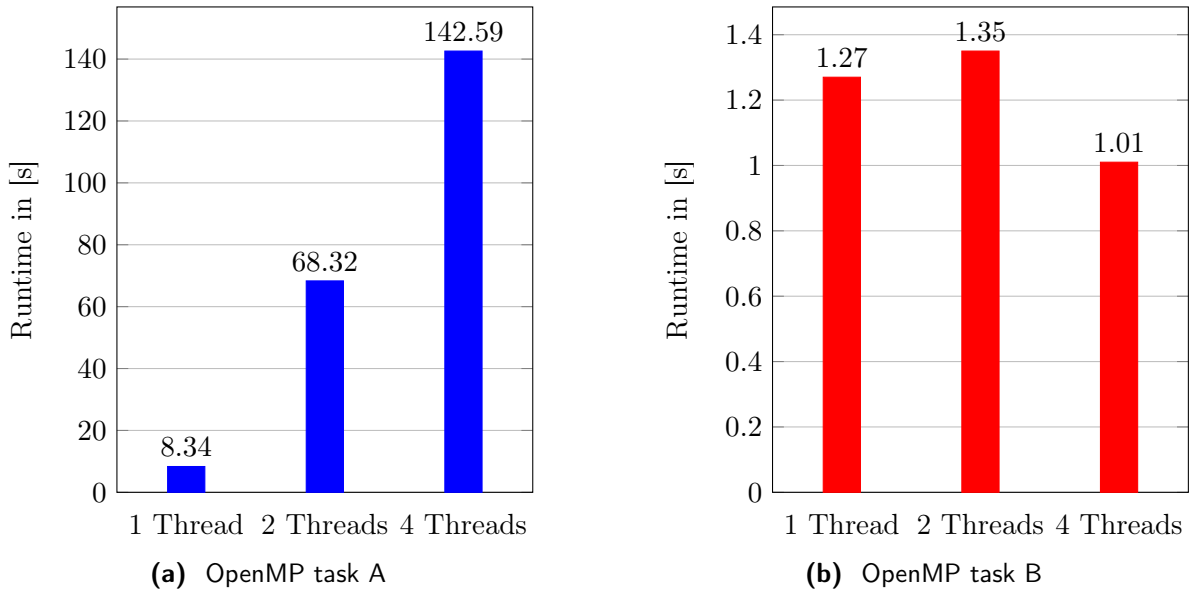
## 4.2 Parallel OpenMP Solver

In this section the performance gain by parallelization with OpenMP of the two most time consuming loops is investigated (Code excerpt 1).

### 4.2.1 R2 a)

First, two different OpenMP parallelization approaches are compared and discussed. The Approaches are denoted with "A)" and "B)" in the code extract in Code excerpt 1. Approach A) uses critical regions, which are executed by each thread serially for the computation of the RHS at element-level and the partial L2 errors. Whereas approach B) is performing a reduction operation for the determination of  $MT_{new}$  and the partial L2 error, using a dynamic scheduling with a chunk size of 512 for both loops.

To compare both approaches, the runtimes for the simulation of the temperature distribution on the coarse mesh is determined for 1, 2 and 4 threads for each parallelization approach. Three runtimes were measured per thread count and averaged. As presented in Fig. 2, the runtimes of the approach A) (Fig. 2a) for different number of threads is in general significantly higher than the runtimes of approach B) (Fig. 2b), with a top runtime of 142.59 seconds for a run with 4 threads for approach A and a runtime of 1.01 seconds for approach B. The scaling behavior of approach B was found to be as theoretically expected, as the runtime decreases with an increasing number of threads. Due to the very short runtimes the run with 2 threads was measured to be slower than a run with only a single thread. This requires further investigations as possible fluctuations in the runtime, for instance due to memory access, are in the order of the runtime itself. Approach A shows the opposite dependency of thread number and runtime. The runtime increases with an increasing number of threads.



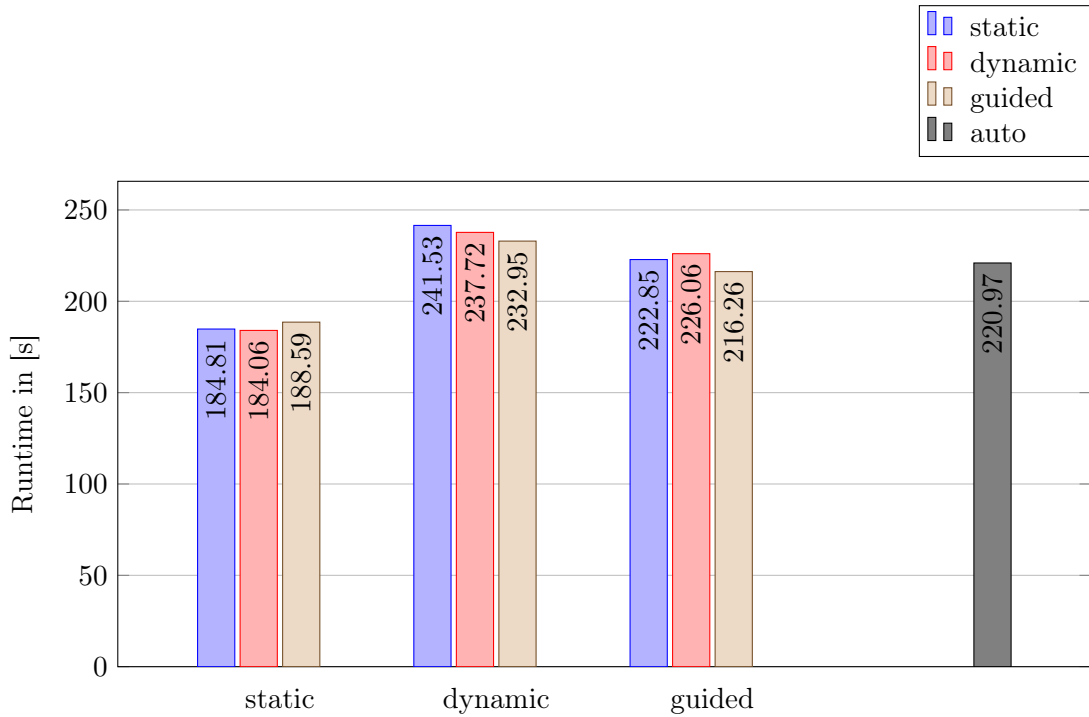
**Figure 2:** Timings for solver with different OpenMP optimizations using the coarse mesh

As the critical regions of approach A) are to be executed by only one thread at a time, the runtime will increase with an increasing number of threads, as observed in Fig. 2a. A large overhead is created due to the management of lock, to ensure that only one thread at a time is running through a critical region. This overhead increases with a higher number of threads. While one thread is executing a critical region the other threads have to wait for this specific thread to finish this section. Only if this

thread has completely finished to computations inside a critical region the next thread is allowed to execute this region for its assigned loop indices. To prevent the overhead of multiple locks and to allow threads to work in parallel the reduction keyword for openMP is used. The reduction operation for the computation of the RHS at element level for each element node, allows each thread to first compute local sums of the each MTnew entry in parallel and then performs a serial summation of all local sums to obtain the value of an MTnew entry. But this serial summation is only computed once, compared to the critical region. The same procedure applies also for the computation of the partial L2 error.

#### 4.2.2 R2 b)

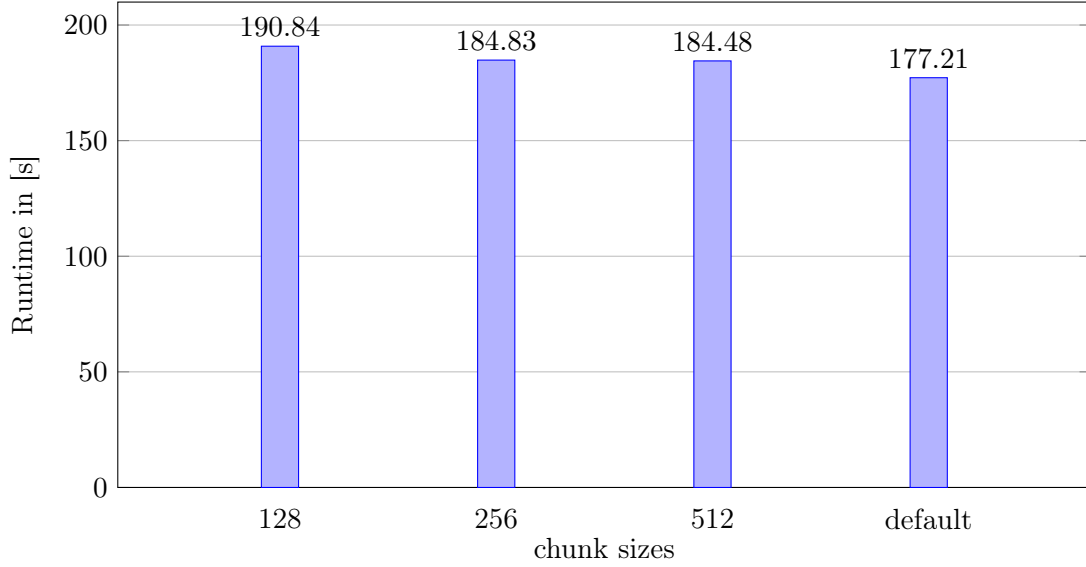
To further optimize the parallelization with OpenMP, different scheduling options and chunk sizes are investigated for approach B) (Code excerpt 1). Therefore simulations on the fine mesh with a constant number of 4 threads were conducted. Some unexplained behavior was observed, when using scheduling options other than static for the second loop in which the partial L2 error are computed, where the L2 error gets initialized again with 0, which leads to an untimely convergence of the simulation. Therefore different scheduling options and chunk sizes were first only applied for the first loop (line 15 Code excerpt 1) for the computation of the RHS on element level. For the second loop (line 45 Code excerpt 1) a static scheduling with default chunk size was used. The results are presented in Fig. 3. In general the static scheduling performs best with a shortest runtime of 184.06 seconds for a chunk size of 256. In general larger runtimes are observed for the dynamic and guided scheduling options. For dynamic scheduling a larger chunk size of 512 iterations leads to shorter runtimes, where as for static and guided scheduling the medium chunk size of 256 iterations performs best. The runtime of the auto scheduling is at 220.97 seconds, which is marked in grey in Fig. 3.



**Figure 3:** Comparison of different scheduling options (static, dynamic, guided, auto) for different chunk sizes(128, 256, 512) in first parallelized loop for 4 threads using the finest mesh

In addition to the introductory investigations regarding the different scheduling options, static scheduling was inspected further. Therefore, the reduction operations for the parallelization of the first and second loop are both equipped with static scheduling and the effects of different chunk sizes are compared in Fig. 4. The runtime is decreasing with an increasing chunk size. The shortest runtime is observed for the default option in scheduling with 177.21 seconds on average, which divides the

number of loop iterations by the number of threads. This results in a chunk size of 21164 iterations for the first loop and in a chunk size of 21322.5 iterations on average for the second loop for the fine mesh with 85290 nodes and 84656 elements. Thus, the static scheduling with the default chunk size is used for the calculation of speed-up and efficiency for the different meshes in the following section.



**Figure 4:** Runtimes for parallelization of loop in line 15 and loop in line 45 in Code excerpt 1 with static scheduling and the same option for chunk size

### 4.2.3 R2 c)

To evaluate the performance gain by using multiple threads for the parallelization approach B) with static scheduling and default chunk size, the runtime, the speed-up (Eq. (1)) and the Efficiency (Eq. (2)) are computed for 1, 2, 4, 6, 8 and 12 threads and plotted in Fig. 5 and Fig. 6. The Speed-up is defined as

$$S_p = \frac{T_1}{T_p}, \quad (1)$$

where  $T_1$  denotes the serial runtime of the respective code and  $T_p$  the time of parallelized code with  $p$  number of threads (cores when considering MPI). The Efficiency is defined as the quotient of speed-up and number of processors

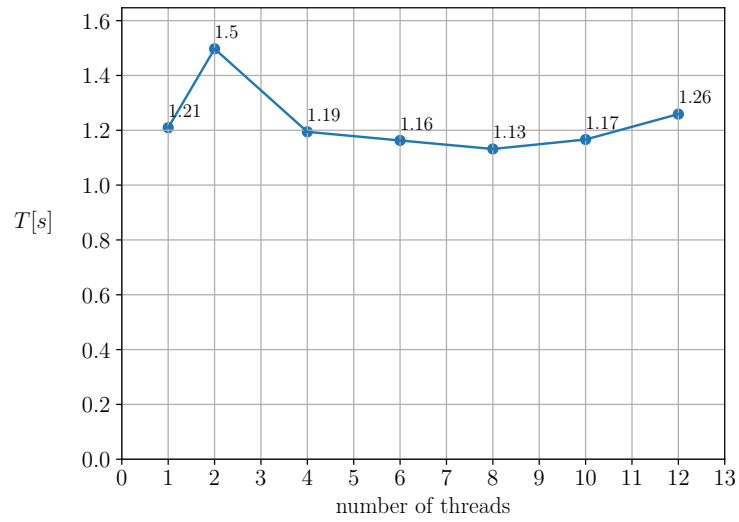
$$E_p = \frac{S_p}{p}. \quad (2)$$

Ideal efficiency would correspond to  $E_p = 1$ , which means a direct proportional dependency of Speed-up and number of processors. This means  $x$  number of processors would result in an  $x$  times faster code in comparison to the serial code. Ideal Efficiency and Speed-up can not be achieved in a real situation. First, there are always parts of the code that just can be executed serially or have to be executed by each thread. Second, there will always be some overhead or idle time, if threads are waiting for other threads to complete their tasks at the end of parallel regions. If time is the most important feature, speed-up has to be considered. If the core-hours are to be minimized and used most efficiently, the efficiency should be the measure of choice.

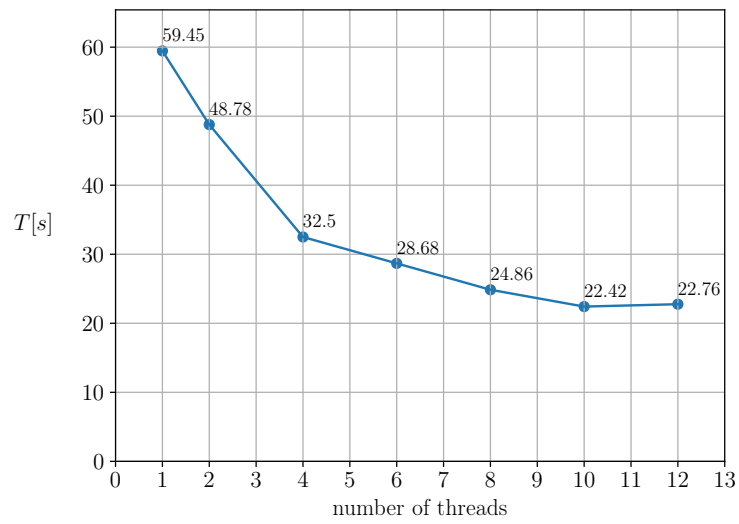
In Fig. 5 the different runtimes for the different simulations on three different meshes (coarse, medium, fine) are visualized. For the coarse mesh the shortest runtime was found for 8 threads with 1.13 seconds (Fig. 5a). In comparison to the medium (Fig. 5b) and fine grids (Fig. 5c), which show good scaling behavior, with a monotonous decreasing runtime for an increasing number of threads, the maximum runtime for the coarse mesh was found for 2 threads with 1.5 seconds. This behavior can



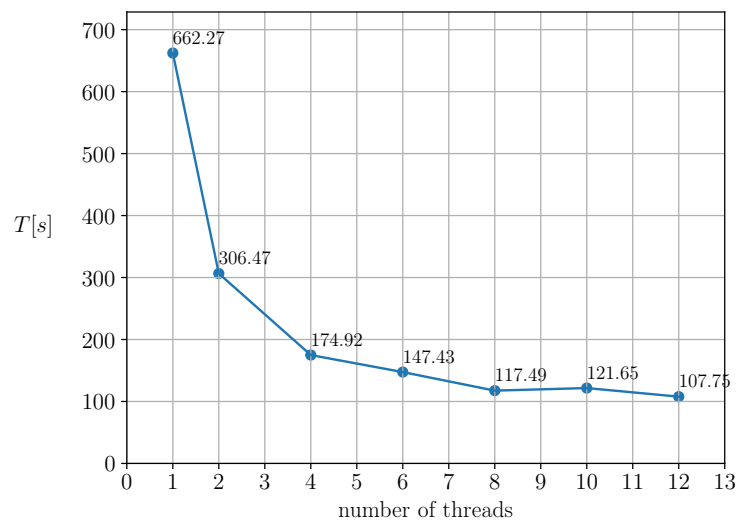
contributed to the short runtime of the simulations on the coarse mesh itself, as they are in the order of possible fluctuations. Also the number of loop iterations is for the coarse grid not high enough, that parallelization would pay off. For the medium mesh the shortest runtime is 22.76 seconds and 107.75 seconds for the fine mesh, both obtained with 12 threads.



(a) coarse mesh



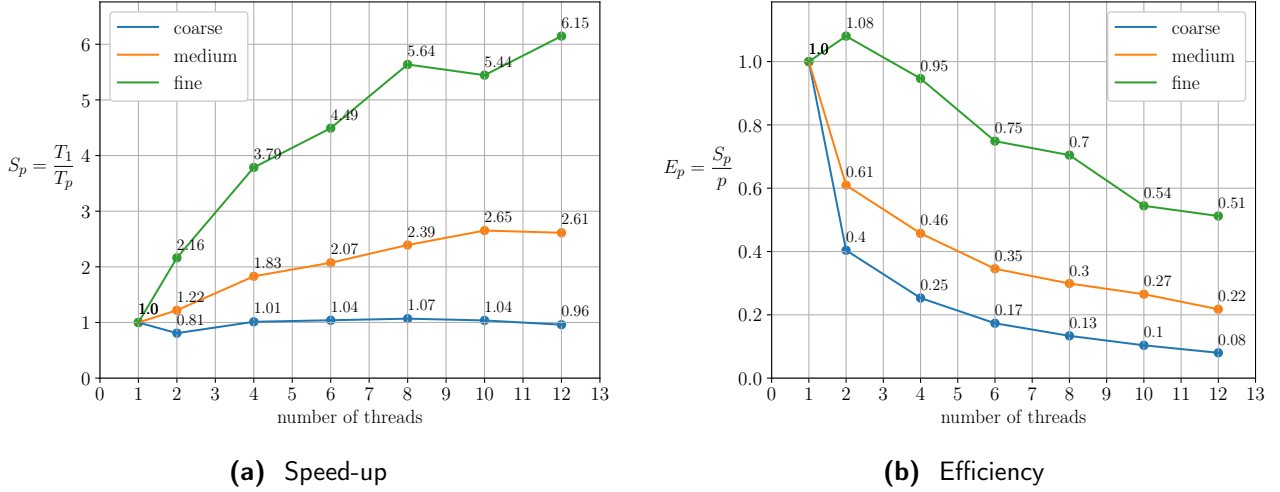
(b) medium mesh



(c) fine mesh

**Figure 5:** Runtime for coarse, medium and fine mesh for different number of threads

Overall the highest speed-up is observed for 12 threads the fine mesh with a 6.15 times faster execution than the serial runtime. This corresponds to an efficiency of 0.51. The fine mesh shows the highest speed-up and efficiencies for the different number of threads. This is due to the amount of computations. The coarse mesh has not enough elements and nodes to achieve great performance gain through parallelization as the overhead gets fast to large for a higher number of threads. This results in 8 threads being fastest for the coarse mesh, where as 10 threads for the medium mesh and 12 threads for the fine mesh result in the fastest execution time (Fig. 6).



**Figure 6:** Speed-up and efficiencies for coarse, medium and fine mesh for different numbers of threads

### 4.2.4 R2 d)

This short subsection is dedicated to the default values of the chunk sizes for different scheduling methods. The default scheduling for OpenMP usage on an intel compiler is static. The default chunk size of static is determined by the quotient of the number of loop iterations and the number of threads. For dynamic scheduling the default chunk size is 1. Lastly, the default chunk size for guided scheduling is proportional to the number of unassigned iterations divided by the number of the threads. This means the chunk size dynamically decreases for guided scheduling. The scheduling option auto delegated the decision to the scheduling type to the compiler or runtime of the system [5].

## 4.3 Parallel MPI Solver

In this section the mpi parallelization of the FEM Solver (reference) is examined regarding it's parallel performance.

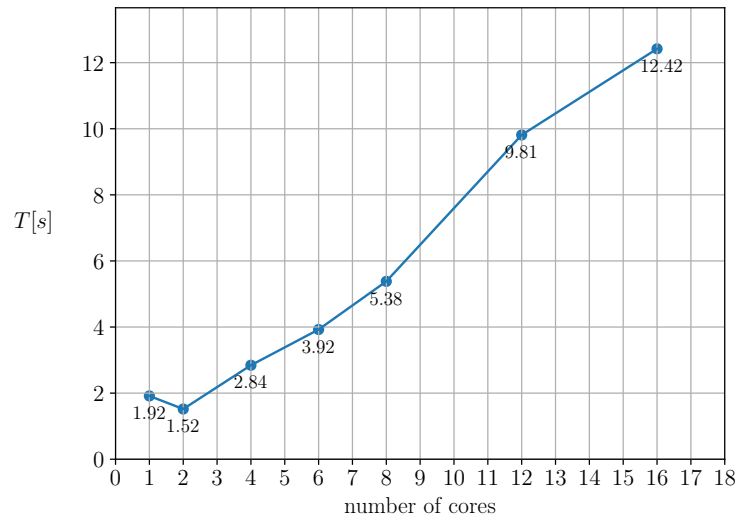
### 4.3.1 R3 a)

Fig. 7 presents the different runtimes for three different meshes that have different level of refinement. The FEM code was run for 1,2,4,6,8,12 and 16 cores, using the compiler flags for best serial performance (Sec. 4.1).

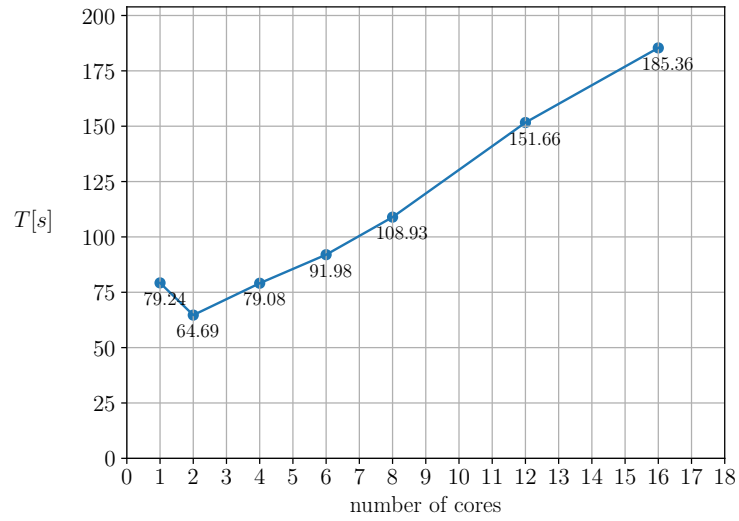
For the coarse mesh the shortest average runtime is at 1.52 seconds with 2 cores (Fig. 7a), for the medium mesh at 64.69 seconds also with 2 cores (Fig. 7b) and for the fine mesh at 342.57 seconds, which is achieved with 4 cores (Fig. 7c).

In Fig. 8, speed-up and efficiency are illustrated for the different meshes. As already observed in the runtime plots, the best speed-up for the coarse and medium mesh is obtained with 2 cores with a speed-up of 1.26 and 1.22, respectively. For the fine mesh the parallelized code ran fastest with 4 cores, resulting in a speed-up of 2.32. The efficiency graphs show an expected development as the efficiency

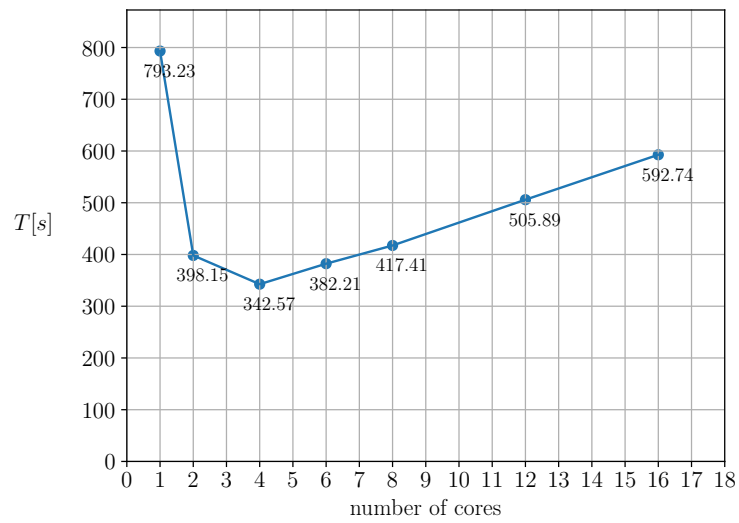
monotonically decreases with an increasing number of threads. For the MPI parallelization with 2 cores on the fine mesh a noticeable high efficiency with 100% was measured, being at optimum efficiency, marked in orange in (Fig. 8f). This means that 2 cores do not introduce nearly any communication overhead and a perfect scaling behavior can be achieved. For the highest number of cores all meshes show a very poor efficiency between only 1-8%. This is also due to the used partitioning approach, which is discussed in (Sec. 4.3.2). Again the same principle applies for distributed memory parallelization. If time is the priority, then according to the speed-up plots, 2 cores should be chosen for the coarse and medium mesh and 4 cores for the fine mesh. If, on the other hand the core-hours are limited and the simulations are supposed to be as efficient as possible, a serial code execution would be most efficient for coarse and the medium mesh. For the fine mesh two cores would be optimal regarding efficiency as they showed excellent scaling behavior.



(a) coarse mesh

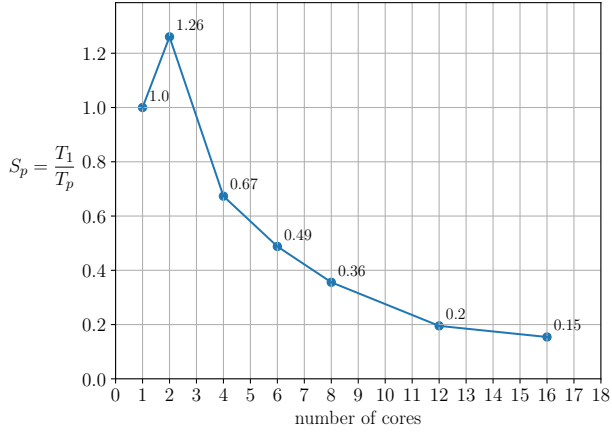


(b) medium mesh

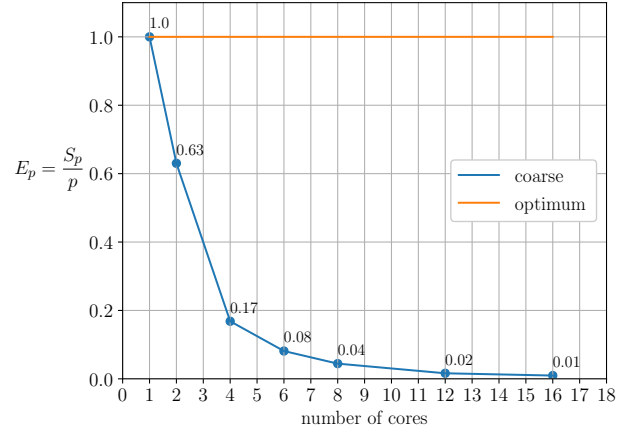


(c) fine mesh

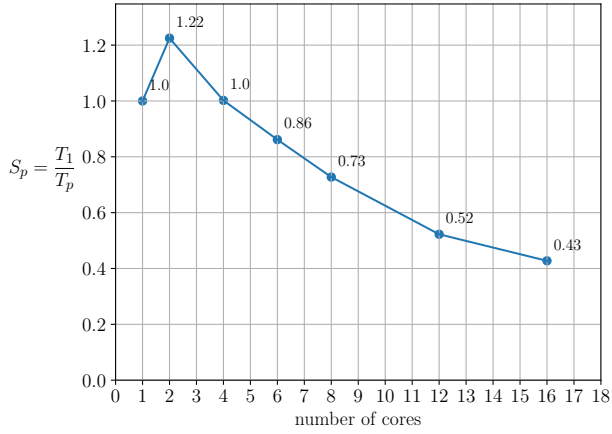
**Figure 7:** Runtime for coarse, medium and fine mesh for different number of cores



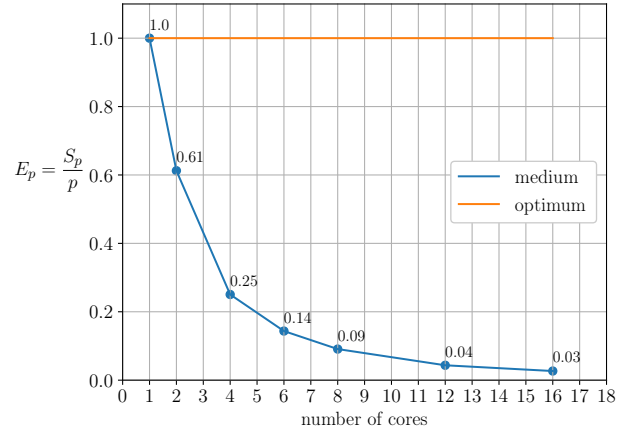
(a) Speed-up coarse mesh



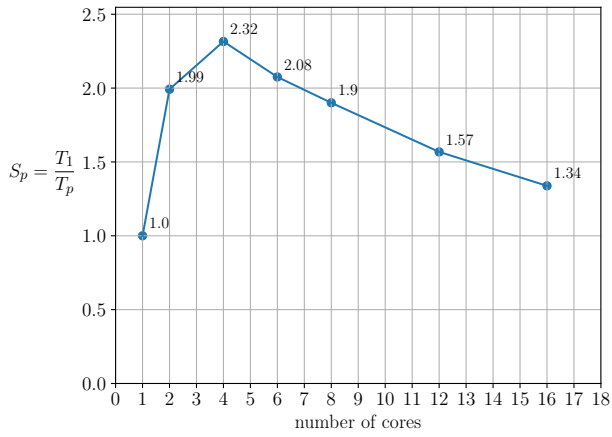
(b) Efficiency coarse mesh



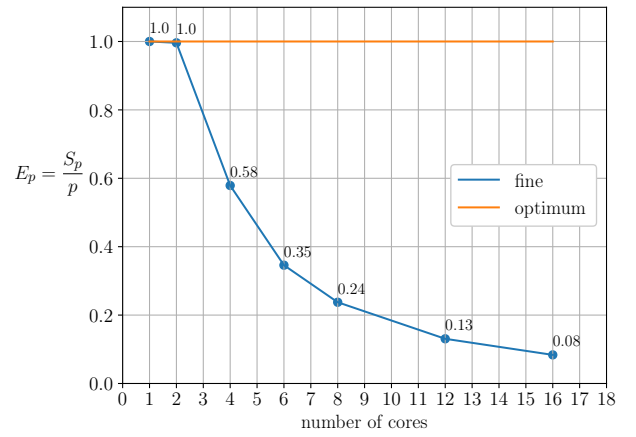
(c) Speed-up medium mesh



(d) Efficiency medium mesh



(e) Speed-up fine mesh

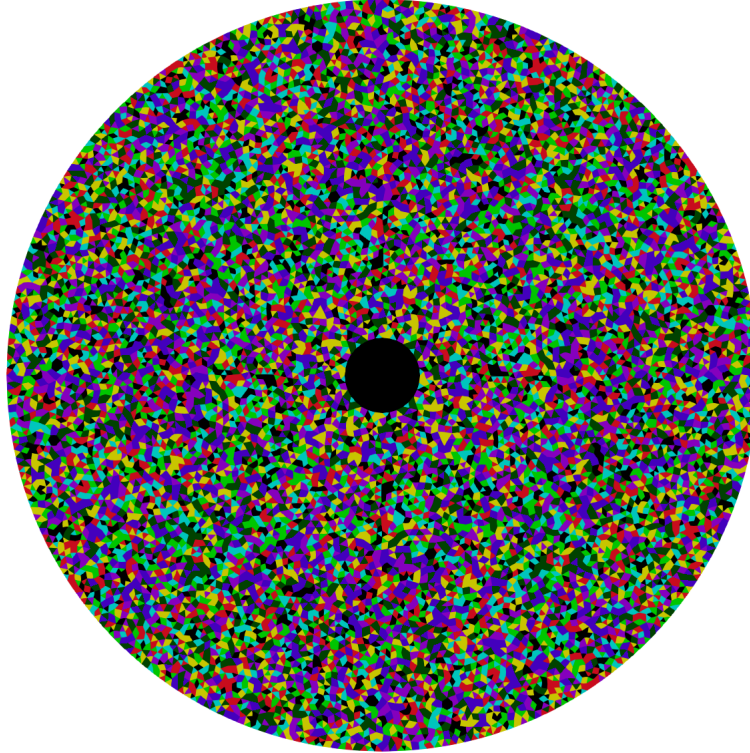


(f) Efficiency fine mesh

**Figure 8:** Speed-up and efficiency for coarse, medium and fine mesh for different number of cores

#### 4.3.2 R3 b)

The partitioning of the elements and nodes of the mesh among the individual cores is performed in a round-robin fashion, depending on how the mesh data structure is stored in the mesh input file. Each processors reads with a certain offset a specific amount of elements and nodes from this input file, that is distributed as evenly as possible between the different cores. This results in a more irregular distribution as it can be seen from Fig. 9. This partitioning is far from optimal as it is in general recommended to use a partitioning with minimum boundary surfaces between different partitions. This will reduce the necessary communications between neighboring partitions. For a circular domain like this heat diffusion problem on a flat circular plate, a distributions of  $p$  similar circle sectors is advisable, where  $p$  represents the number of processors used. This partitioning will result in a minimization of the boundary surfaces.



**Figure 9:** Partitioning of medium mesh using 8 cores

#### 4.3.3 R3 c)

The overall performance of the code can be improved. Some speed-up is achieved, with the shortest runtime for two cores for the coarse and medium mesh and for four cores for the fine mesh. However the efficiency is poor, especially for a higher number of cores (Fig. 8). The current bottleneck is the communication overhead. A lot of loops (line numbers) run over all processors, which results in an increasing loop size for an increasing number of processors. Inside these loops a lot of MPI calls take place, which will result in high communication overhead. Therefore some kind of static data structure, which stores information for all nodes could help. Also a window-halo approach would be beneficial, which allows certain processors to store some data of directly neighboring elements and nodes, that are required for an efficient communication.

Another way to improve the performance of the code is to couple shared memory parallelization on a loop level with OpenMP with the distributed memory parallelization with MPI on global mesh

level. Meaning the loops, especially for computing the RHS on element level and for the computation of the L2 error can be parallelized with the proposed approach B) (Code excerpt 1) on each processor. So each processor executes or at least creates multiple threads. This approach is then called hybrid parallelization.

#### 4.3.4 R3 d)

This section answers some general question regarding MPI commands and communication methods:

- 1) Why do we use *MPI\_Accumulate* in the code instead of *MPI\_Put*?

With the *MPI\_Accumulate* function a reduction among all the processors of a certain variable or array elements can be performed. Like a summation or finding the maximum value throughout all processors. Put simply overwrites a value.

- 2) What are the advantages of using one-sided MPI communication over two-sided MPI communication?

On one hand, one-sided communication has no risk of deadlocks as the individual processors do not have to wait for send and receives of other processors in a communicator. Also the communication patterns are in general easier to implement for one-sided communication. On the other hand, the processors simply can read and write to another processor's memory. This however, is error prone and requires a thought-through communication. Two-sided communication is more time-consuming or cumbersome to implement. But, through the send and receive approach, data encapsulation is ensured and the programmer is always aware of which data is exchanged between individual processors. But blocking methods that are required for two-sided communication can lead to an increase in waiting time.

- 3) What advantage does MPI offer over OpenMP, what is a main obstacle of MPI?

The difference in the MPI and the OpenMP parallelization approaches lies in the memory architecture. MPI is designed for a distributed memory approach, which does not make any limitations to the number of processors to be used. In contrast, OpenMP follows the shared memory approach, so this makes the physical memory of a CPU the limitation for the number of threads. But the parallelisation with OpenMP is less elaborate as it can be easily added to existing code, when considering loop parallelization. MPI introduces a lot of additional overhead, when writing parallel code, due to specific MPI commands and the partitioning.

## 5 Conclusion

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