

* ZHENG KAI 50247576 **

* XINBO YU 50102922 **

EXTENDS *Integers, Sequences, FiniteSets, TLC*

CONSTANT *RM, RMMAYFAIL, TMMAYFAIL*

```
--algorithm t2pc{
  variable rmState = [rm ∈ RM ↦ "working"];
    tmState = "init";
  define {
    canCommit ≜ ∀ rm ∈ RM : rmState[rm] ∈ {"prepared", "committed"}
    canAbort ≜ ∃ rm ∈ RM : rmState[rm] ∈ {"aborted", "failed"} ∧ tmState ≠ "committed"
  }

  macro Prepare( p ) {
    await rmState[p] = "working";
    rmState[p] := "prepared";
  }

  macro Decide( p ) {
    either { if ( rmState[p] = "prepared" ∧ tmState = "committed" )
      {
        rmState[p] := "committed"
      }
    }
    or { if ( rmState[p] ∈ {"working"} ∨ ((rmState[p] = "prepared") ∧ (tmState = "aborted")) )
      {
        rmState[p] := "aborted"
      }
    }
  }

  macro Fail( p ) {
    if ( RMMAYFAIL ) {
      either rmState[p] := "failed";
      or skip;
    }
  }

  fair process ( RManager ∈ RM ) {
    RS: while ( rmState[self] ∈ {"working", "prepared"} ) {
      either Prepare(self) or Decide(self) or Fail(self)
    }
  }

  fair process ( TManager = 0 ) {
    TS: either { await canCommit;
```

```

    TC: tmState := "committed" ;
    F1: if ( TMMAYFAIL ) tmState := "hidden" ; }

  or { await canAbort ;
    TA: tmState := "aborted" ;
    F2: if ( TMMAYFAIL ) tmState := "hidden" ; }
}

  TM backup
  fair process ( TManagerBackup = 1 ) {
    L1: await  $\vee$  TMMAYFAIL  $\wedge$  tmState = "hidden"
       $\vee$   $\neg$  TMMAYFAIL ;
    if ( tmState = "hidden" ) {
      TS: either {
        await canCommit ;
        TC: tmState := "committed" ;
      }
      or {
        await canAbort ;
        TA: tmState := "aborted" ;
      }
    }
  }
}

```

BEGIN TRANSLATION

Label *TS* of process *TManager* at line 48 col 13 changed to *TS_*

Label *TC* of process *TManager* at line 49 col 17 changed to *TC_*

Label *TA* of process *TManager* at line 53 col 17 changed to *TA_*

VARIABLES *rmState*, *tmState*, *pc*

define statement

$canCommit \triangleq \forall rm \in RM : rmState[rm] \in \{ \text{"prepared"}, \text{"committed"} \}$

$canAbort \triangleq \exists rm \in RM : rmState[rm] \in \{ \text{"aborted"}, \text{"failed"} \} \wedge tmState \neq \text{"committed"}$

$vars \triangleq \langle rmState, tmState, pc \rangle$

$ProcSet \triangleq (RM) \cup \{0\} \cup \{1\}$

Init \triangleq Global variables

$\wedge rmState = [rm \in RM \mapsto \text{"working"}]$

$\wedge tmState = \text{"init"}$

$\wedge pc = [self \in ProcSet \mapsto \text{CASE } self \in RM \rightarrow \text{"RS"}$

$\square \quad self = 0 \rightarrow \text{"TS_"}]$

$\square \quad self = 1 \rightarrow \text{"L1"}]$

$RS(self) \triangleq \wedge pc[self] = \text{"RS"}$

```

    ∧ IF  $rmState[self] \in \{\text{"working"}, \text{"prepared"}\}$ 
      THEN  $\wedge \vee \wedge rmState[self] = \text{"working"}$ 
         $\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"prepared"}]$ 
         $\vee \wedge \vee \wedge \text{IF } rmState[self] = \text{"prepared"} \wedge tmState = \text{"committed"}$ 
          THEN  $\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"committed"}]$ 
          ELSE  $\wedge \text{TRUE}$ 
             $\wedge \text{UNCHANGED } rmState$ 
         $\vee \wedge \text{IF } rmState[self] \in \{\text{"working"}\} \vee ((rmState[self] = \text{"prepared"}) \wedge (tmState = \text{"committed"}))$ 
          THEN  $\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"aborted"}]$ 
          ELSE  $\wedge \text{TRUE}$ 
             $\wedge \text{UNCHANGED } rmState$ 
         $\vee \wedge \text{IF } RMMAYFAIL$ 
          THEN  $\wedge \vee \wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"failed"}]$ 
           $\vee \wedge \text{TRUE}$ 
             $\wedge \text{UNCHANGED } rmState$ 
          ELSE  $\wedge \text{TRUE}$ 
             $\wedge \text{UNCHANGED } rmState$ 
         $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"RS"}]$ 
      ELSE  $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"Done"}]$ 
         $\wedge \text{UNCHANGED } rmState$ 
     $\wedge \text{UNCHANGED } tmState$ 

```

$RManager(self) \triangleq RS(self)$

$TS_- \triangleq \wedge pc[0] = \text{"TS_-"} \\ \wedge \vee \wedge canCommit \\ \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"TC_-"}] \\ \vee \wedge canAbort \\ \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"TA_-"}] \\ \wedge \text{UNCHANGED } \langle rmState, tmState \rangle$

$TC_- \triangleq \wedge pc[0] = \text{"TC_-"} \\ \wedge tmState' = \text{"committed"} \\ \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"F1"}] \\ \wedge \text{UNCHANGED } rmState$

$F1 \triangleq \wedge pc[0] = \text{"F1"} \\ \wedge \text{IF } TMMAYFAIL \\ \text{THEN } \wedge tmState' = \text{"hidden"} \\ \text{ELSE } \wedge \text{TRUE} \\ \wedge \text{UNCHANGED } tmState \\ \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"Done"}] \\ \wedge \text{UNCHANGED } rmState$

$TA_- \triangleq \wedge pc[0] = \text{"TA_-"} \\ \wedge tmState' = \text{"aborted"}$

$$\begin{aligned}
& \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"F2"}] \\
& \wedge \text{UNCHANGED } rmState \\
F2 & \triangleq \wedge pc[0] = \text{"F2"} \\
& \wedge \text{IF } TMMAYFAIL \\
& \quad \text{THEN } \wedge tmState' = \text{"hidden"} \\
& \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \wedge \text{UNCHANGED } tmState \\
& \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"Done"}] \\
& \wedge \text{UNCHANGED } rmState \\
TManager & \triangleq TS_ \vee TC_ \vee F1 \vee TA_ \vee F2 \\
L1 & \triangleq \wedge pc[1] = \text{"L1"} \\
& \wedge \vee TMMAYFAIL \wedge tmState = \text{"hidden"} \\
& \quad \vee \neg TMMAYFAIL \\
& \wedge \text{IF } tmState = \text{"hidden"} \\
& \quad \text{THEN } \wedge pc' = [pc \text{ EXCEPT } ![1] = \text{"TS"}] \\
& \quad \text{ELSE } \wedge pc' = [pc \text{ EXCEPT } ![1] = \text{"Done"}] \\
& \wedge \text{UNCHANGED } \langle rmState, tmState \rangle \\
TS & \triangleq \wedge pc[1] = \text{"TS"} \\
& \wedge \vee \wedge canCommit \\
& \quad \wedge pc' = [pc \text{ EXCEPT } ![1] = \text{"TC"}] \\
& \quad \vee \wedge canAbort \\
& \quad \wedge pc' = [pc \text{ EXCEPT } ![1] = \text{"TA"}] \\
& \wedge \text{UNCHANGED } \langle rmState, tmState \rangle \\
TC & \triangleq \wedge pc[1] = \text{"TC"} \\
& \wedge tmState' = \text{"committed"} \\
& \wedge pc' = [pc \text{ EXCEPT } ![1] = \text{"Done"}] \\
& \wedge \text{UNCHANGED } rmState \\
TA & \triangleq \wedge pc[1] = \text{"TA"} \\
& \wedge tmState' = \text{"aborted"} \\
& \wedge pc' = [pc \text{ EXCEPT } ![1] = \text{"Done"}] \\
& \wedge \text{UNCHANGED } rmState \\
TManagerBackup & \triangleq L1 \vee TS \vee TC \vee TA \\
Next & \triangleq TManager \vee TManagerBackup \\
& \vee (\exists self \in RM : RManager(self)) \\
& \vee \text{Disjunct to prevent deadlock on termination} \\
& ((\forall self \in ProcSet : pc[self] = \text{"Done"}) \wedge \text{UNCHANGED } vars) \\
Spec & \triangleq \wedge Init \wedge \Box [Next]_{vars} \\
& \wedge \forall self \in RM : WF_{vars}(RManager(self)) \\
& \wedge WF_{vars}(TManager)
\end{aligned}$$

$\wedge \text{WF}_{vars}(TManagerBackup)$

$Termination \triangleq \Diamond(\forall self \in ProcSet : pc[self] = \text{"Done"})$

END TRANSLATION

$Termination2 \triangleq \Diamond(\forall self \in ProcSet : pc[self] = \text{"Done"})$

$Consistency \triangleq \forall rm1, rm2 \in RM : \neg(rmState[rm1] = \text{"aborted"} \wedge rmState[rm2] = \text{"committed"})$

```
\ **ZHENG KAI 50247576 **
\ **XINBO YU 50102922 **
\ * Part 1.1
\ * Model check Consistency and Termination with RMMAYFAIL = FALSE and TMMAYFAIL =
FALSE,
\ * it means that no RM false and no TM false, and there are no errors in my program.
\ * Model check Consistency and Termination with RMMAYFAIL = TRUE and TMMAYFAIL =
FALSE,
\ * it means that RM can be false and no TM false, not all RMs can change their status to
committed, and there are no errors in my program.

\ * Part 1.2
\ * Model check Consistency and Termination with RMMAYFAIL = FALSE and TMMAYFAIL =
TRUE,
\ * it means that no RM false and TM will be false, and the Termination property will be
violated.
\ * The error example is rmState < "aborted", "prepared", "aborted" > and tmState = hidden.
When
\ * tmState equals hidden, it doesn't make a decision. When the RMs read the state of the TM,
it can't
\ * return committed or aborted, so the RMs can't change their state.

\ * Part 1.3
\ * When set RMMAYFAIL TRUE and TMMAYFAIL TRUE, model check Consistency and
Termination
\ * Both of them are not violated. Because when TM fail, the RMs can connect to the Backup
TM,
\ * they can read the state of Backup TM, the Backup TM can reset the status of the tm, so the
RMs can
\ * change to their right final status according to tmState.

\ * Modification History
\ * Last modified Tue Dec 05 11:17:23 EST 2017 by xinboyu
\ * Last modified Tue Nov 28 13:56:03 EST 2017 by kz-pc
\ * Created Sun Nov 26 15:42:50 EST 2017 by xinboyu
```