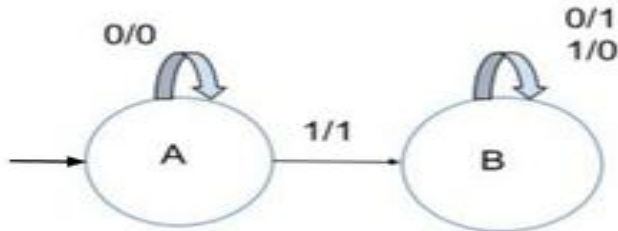
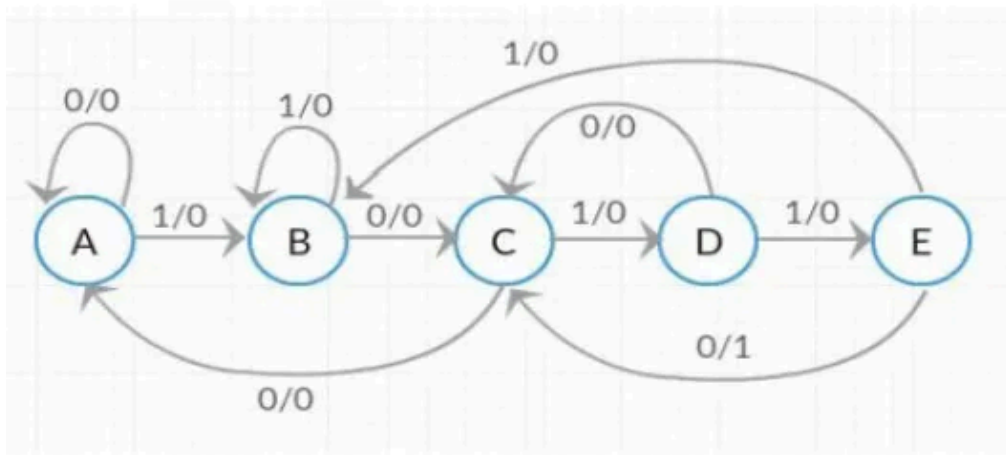


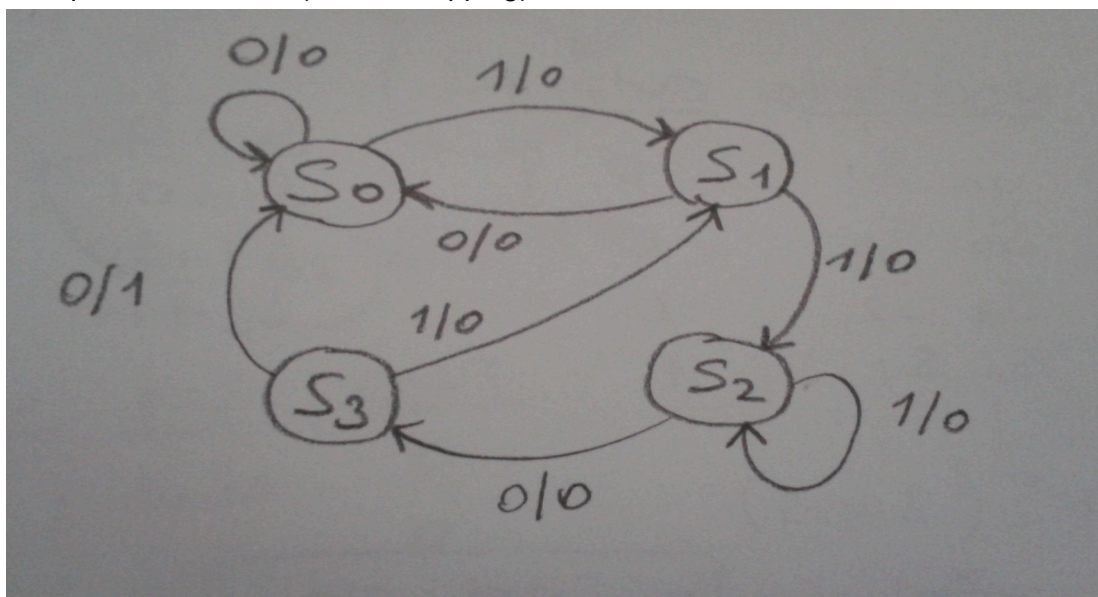
1. Design a finite state machine to detect the following sequences
  - a. Serial 2's complement block  
Read the input from LSB to MSB (right to left).



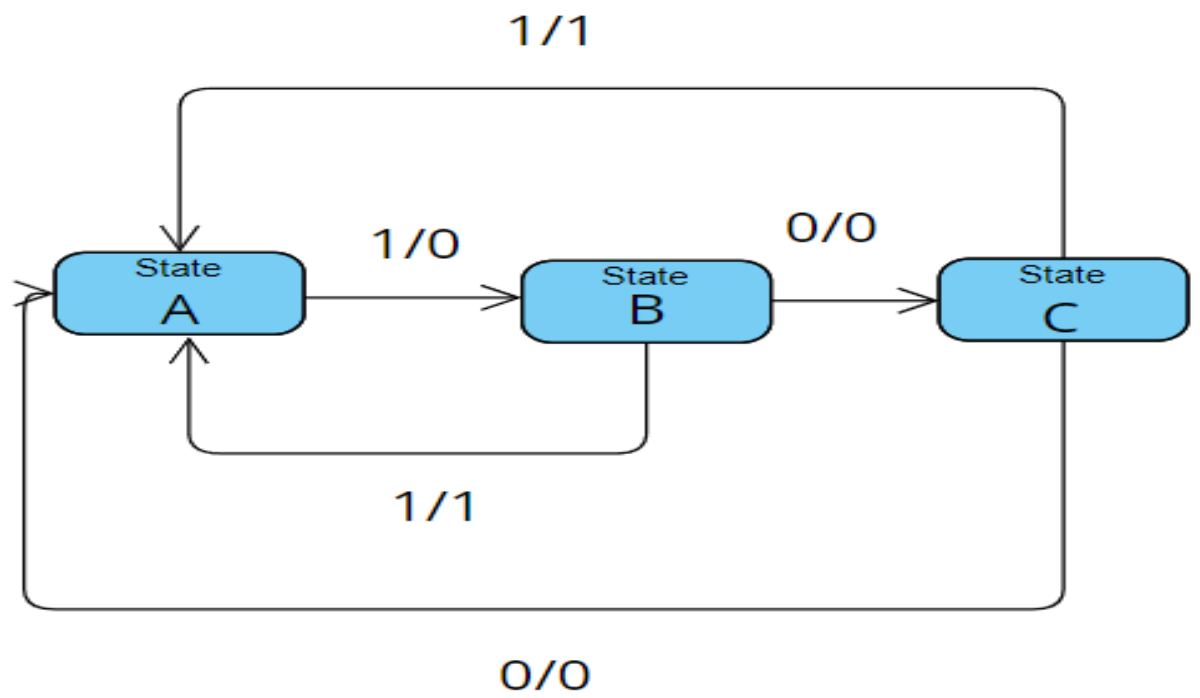
- b. A sequence 10110 (overlapping)



- c. A sequence 11001100 (non-overlapping)



d. More than one '1's in the last 3 samples



e. Consequent 4 '0's (overlapping)

