

Using a Vulnerability Assessment Methodology to build and improve Countermeasures against Multi-Fault Injection

Bruno Ferres¹, Louis Sassier, Etienne Boespflug
with Marie-Laure Potet and Laurent Mounier

¹bruno.ferres@univ-grenoble-alpes.fr



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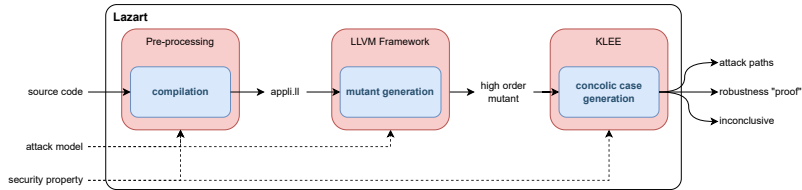


Plan

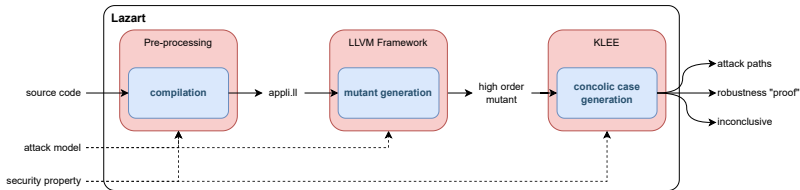
- 1 Overview of the Methodology
- 2 Case Study: Protecting Control-Flow against Multiple Faults
- 3 Proposing the CFIShield
- 4 Conclusion and Perspectives



Recalls on Lazart



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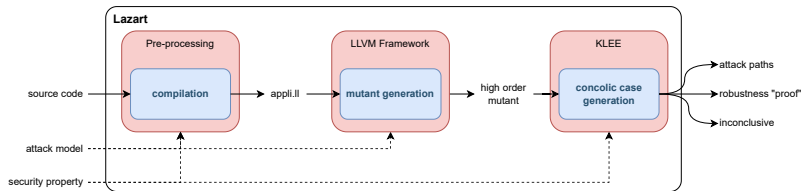


Multi-fault Analysis

■ pre-defined fault models:

- ▶ data load mutation (DL)
- ▶ test inversion (TI)
- ▶ switch call (SC)
- ▶ no call (NC)

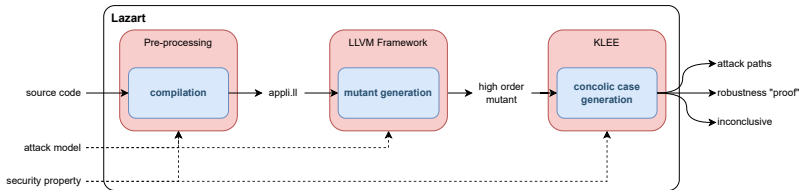
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Lazart

What's new ?

Adding countermeasures (CM) increases attack surface **in multi-fault**...

- ↪ how to consider attack surface while placing countermeasures ?
- ↪ how to consider countermeasure state in the analysis ?

security property

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Protecting Sensitive Schemes

Sensitive Scheme

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bb_target:  
stmt1 ...  
%target = load %val  
stmt2 ...
```

← IP 1



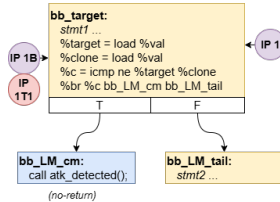
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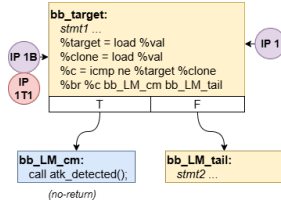
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Load Duplication



```
klee_make_symbolic(&val);  
LoadDupl(val, &target);  
oracle(!(target == val));
```

Oracle for load duplication

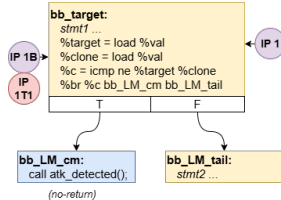
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Analysis Principle

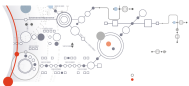
- consider only **nominal behavior** of sensitive scheme (*i.e.* injection points IP)
↪ not of program P
- either preserve nominal, or detect attack



Studying Counter-Measures in Isolation

Robustness levels

Use **symbolic execution** to compute a **robustness level** (rl) against **multi-fault attacks**.



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Countermeasure	Fault model					
	Test inversion		Load modification		Combination	
	rl	$Vuln$	rl	$Vuln$	rl	$Vuln$
Test duplication	1	2	-	-	1	2
Load duplication	-	-	1	1	1	2
Load triplication	-	-	2	1	2	4

Robustness levels of countermeasure schemes (for $\text{max_order}=4$)

$Vuln$: number of attack paths found with $rl + 1$ faults



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↪ **automatic placement of counter-measures against multi-fault**





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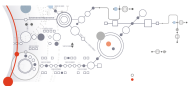
Minimal Working Example

```
bool is_authenticated = false;
```

```
void authenticate() {  
    is_authenticated = true;  
}
```

```
void try_authenticate() {  
    if (check_password()) {  
        authenticate();  
    }  
}
```

```
void caller() {  
    try_authenticate();  
}
```





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Requirements

- ensure **control-flow integrity**
 - ↔ both forward and backward edges
 - ⇒ check both call and return addresses
- be robust against multiple fault models
 - ▶ DL (data load modification)
 - ▶ TI (test inversion)
 - ▶ SC (switch call)
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- handle composition (multiple calls)





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void caller() {
    authenticate(); // applying SC
}
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Minimal Working Example

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bool is_authenticated = false;

void authenticate() {
    is_authenticated = true;
}

void try_authenticate() {
    if (!check_password()) { // TI
        authenticate();
    }
}

void caller() {
    try_authenticate();
}
```

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Protecting Both Directions with CFIShadow

Main Idea

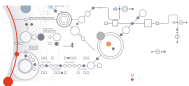
- store **function signature** and **return address**
(inspired from **SecSwift** and **shadow stacks**)
- ↪ check that:
 1. we call the **right function**;
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 - ↪ with primitives to ensure **integrity**
 - ▶ `push`, `peek_and_check`, `pop`, `check_current`



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```
void try_authenticate (addr ret_caller) {  
    peek_and_check(sig_try_authenticate);  
    pop();  
    ... // function body  
    peek_and_check(ret_caller);  
    return;  
}  
  
void caller() {  
    int mem_current = current;  
    push(ret_caller);  
    push(sig_try_authenticate);  
    try_authenticate(ret_caller);  
ret_caller:  
    pop();  
    check_current(mem_current);  
    ... // remaining code  
}
```

*protection

Protecting Both Directions with CFIStack

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Oracle for CFISStack Nominal Behavior:

```
old_cfi_current == cfi_current &&  
cmp(cfi_stack, old_cfi_stack, STACK_SIZE)
```

```
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Nothing really new...





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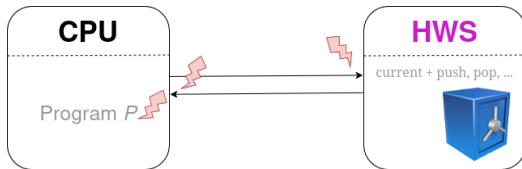
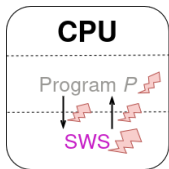
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Proposing SWS vs. HWS

- **SWS** (soft): primitives (push, ...) **logic** are vulnerable (e.g. TI, DL)
- **HWS** (hard): primitives logic (+ current) are considered secure
↪ just change the Lazart setup (mainly, IPs for **TI** and **DL**)



Comparing Robustness Levels

Version	Fault model	#attacks		Robustness level
		1F	2F	
SWS	SC		2	1
	SC + DL	132+		0
	SC + TI	3		0
	SC + DL + TI	135+		0
HWS	SC		2	1
	SC + DL		4	1

SWS: Software CFISStack / **HWS:** Hardware CFISStack

Fault models:

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PSWS	SC		2	1
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PSWS: Protected SWS (with our methodo.)
↪ add **load-** and **test-duplication** to SWS

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- CM obtained in isolation can be re-used
↪ at least for **sequential calls**

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- **but can we do better ?**



Feedback and Distribution

Insightful Feedback

Proposed methodology can be used to:

- identify **hotspots** to harden
↪ and which ones to consider first



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Try it yourself !

Both methodology and example documented in Lazart wiki:

<https://gricad-gitlab.univ-grenoble-alpes.fr/securitytools/lazart/-/wikis/Countermeasure-hardening-tutorial>





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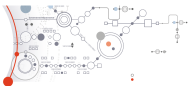




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Proposed Methodology

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↪ including complex CMs with state
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- **formalize the requirements** between HW and SW
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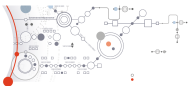
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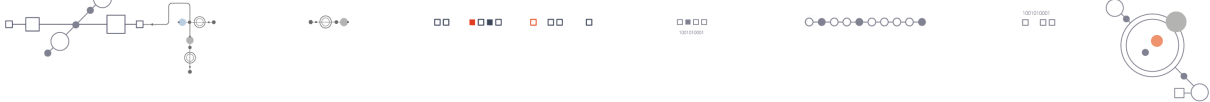
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BACKUPS

Please don't be mean





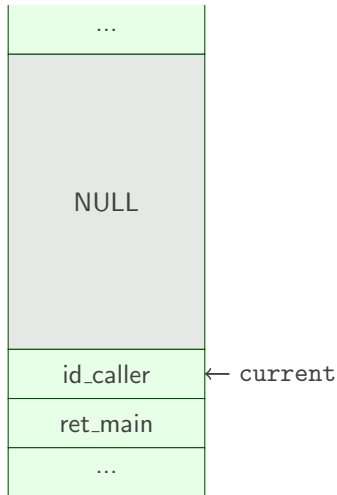
Need to Preserve the Whole Stack ?

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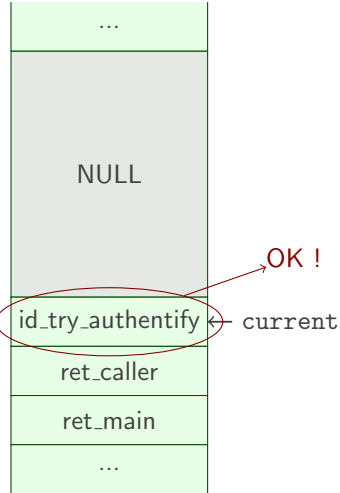
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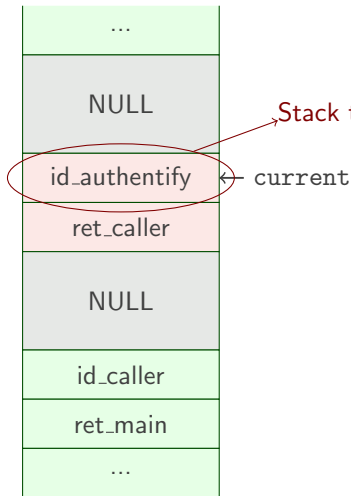
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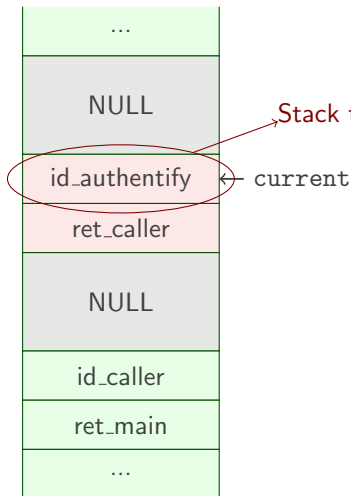
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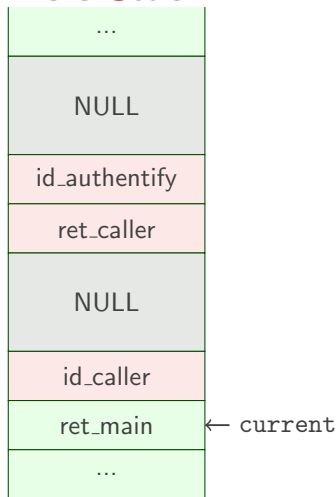
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```
void authenticate() {  
    is_authenticated = true;  
}
```



Why do we need such a strong oracle ?

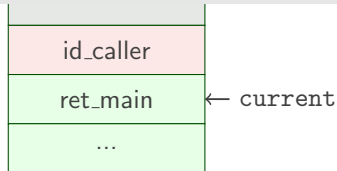
In a multi-fault context, we might temper the stack by combining DL and MC...

↪ could lead to erroneous control-flow, that we need to consider in analysis

↪ by checking the whole stack, it would require twice more faults to erase the traces

```
}
```

```
void caller() {  
    try_authenticate();  
}
```



Need to Preserve the Whole Stack ?

```
bool is_authenticated = false;
```

```
void authenticate() {  
    is_authenticated = true;  
}
```



Why do we need such a strong oracle ?

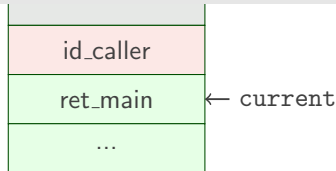
In a multi-fault context, we might temper the stack by combining DL and MC...

↪ could lead to erroneous control-flow, that we need to consider in analysis

↪ by checking the whole stack, it would require twice more faults to erase the traces

```
}
```

```
void caller() {  
    try_authenticate();  
}
```



Considered Fault Models for CFISStack Analysis

Variant	Fault model	Target	Function
SWS	SC/NC	try_authenticate	caller()
		push	
		pop	
	SC/NC	check_current	try_authenticate()
		peek_and_check	
SWS	TI	pop()	push()
		push()	
		peek_and_check()	
	DL	mem_current	caller()
		sig_try_authenticate	
SWS	DL	sig_authenticate	authenticate()
		current	
		pop()	push()

Variant	Fault model	Target	Function
HWS	SC/NC	try_authenticate	caller()
		check_current	
	TI	pop	-
HWS	DL	mem_current	caller()
		sig_try_authenticate	
		sig_authenticate	authenticate()

Comparing Robustness Levels with NC and SC

Version	Fault model	#attacks 1F	2F	Robustness level
SWS	SC		2	1
	SC + DL	132+		0
	SC + TI	3		0
	SC + DL + TI	135+		0
	NC		1	1
	NC + DL	72+		0
	NC + TI	3		0
Protected SWS	NC + DL + TI	75+		0
	SC		2	1
	SC + DL		7+	1
	SC + TI		7+	1
	SC + DL + TI		10+	1
	NC		1	1
	NC + DL		5+	1
	NC + TI		5+	1
	NC + DL + TI		10+	1

Version	Fault model	#attacks 1F	2F	Robustness level
HWS	SC		2	1
	SC + DL		4	1
	NC		1	1
	NC + DL		2	1

Toward n -Robustness

Version	Fault model	#IP to protect for 2-robustness
HWS	NC	1
	NC + DL	1
	SC	3
	SC + DL	3
PSWS	NC	1
	NC + DL	3
	NC + TI	4
	NC + TI + DL	6
	SC	3
	SC + DL	5
	SC + TI	6
	SC + TI + DL	8

Insights from the methodology

- which IP to protect ?
- which CM to use ?
- try to **minimize** the protection while **ensuring robustness**

Some feedback to harden using CFISStack variants

