Arnay Jain

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EDUCATION

National Institute of Technology (NIT), Silchar

B.tech in Electronics and Instrumentation Engineering - 8.84/10

Shri. Chhatrapati Shivaji School

All India Senior Secondary School Certificate Examination-82.6 %

St. George Hr.Sec. School

All India Secondary School Certificate Examination - 95.2 %

Silchar, Assam, India November 2020- Present Indore, Madhya Pradesh, India

May 2020

Dhar, near Indore, Madhya Pradesh, India

May 2018

WORK EXPERIENCE

Indian Institute of Information Technology, Research Intern, Allahabad, India

January 2023 - present

- Designed Pixel Circuit for AMOLED displays with a-IGZO based TFTs to minimize effect of driving TFT V_{th} variation on I_{OLED}
- The 6T1C pixel circuit, simulated in Hspice gave maximum current variation of only 0.296% for Vth range of [0.35V, 4.35V]
- Optimizing P_{DISSP} for emission phase, verifying results in Origin and designing the layout in Cadence Spectre
 Tools/Framework used: Hspice, Origin, Cadence Spectre

Marine Research Centre, 3D-software package developing Intern, Pune, India

August 2022-November 2022

- Factoring depth variations under sea level using AIS data (Automatic Identification System) for ambient noise mapping in IOR
- Perform detailed analysis for 11 oceanic water layers for low frequency noise radiated using PyRAM modeling
- Developed software package for 2D-mapping of Shipping Noise variation levels across Lat-Long in IOR (Indian Ocean Region)
 Tools/Framework used: Acoustic toolbox, PyRAM, Python, Jupyter Notebook

DS- Dairy System, API chatbot and android developer as tech team lead, Start-up, Neemuch, India

April 2022 - June 2022

- Developed automated customer user applications for collecting large data through interception of API system and Respond.io
- API generated automated customer billing and supplier employment system using ML & MessageBird for easy scheduling
- Well notified customer query interface for better handling increased the customer count by 100 each month
 Tools/ Framework used: Respond.io, API, MessageBird, Machine learning, Android development

PROJECTS

Testing the stability of a control system- Control system

May 2022 - July 2022

- Developed the insights of finding transfer function, root locus, error approximation, Routh Hurwitz criterion
- Used the results obtained for plotting Nyquist plot of the system and verified the stability graphically

Characterization of LVDT with application in Valve Pitch Actuators

February 2022 - March 2022

- Verified the working principle of Linear Variable Differential Transformer with air supply between 40 and 120 psi
- Determined parameters like position and displacement using LVDT and compared with values obtained from other sensors

Design of two stage Operational Amplifier to obtain 120dB gain - Microelectronics

December 2021- February 2022

- Concluded that gain of the design is **111dB** and **optimized** design parameters to meet Power and Output swing constraints
- Performed Stability analysis in LTSplice and concluded design with calculation of ICMR, CMRR and PSRR

Advanced Library Management System | C - Language (libraries)

August 2021 - October 2021

- Developed **LMS** to perform **CRUD** on library resources using locks to ensure **ACID** properties of transaction
- Application was also designed to register, authenticate and authorize individuals based on user type (STUDENT, STAFF, ADMIN etc)

PUBLICATION

Jain, A. (2022) Review to 3D mapping of Low frequency ambient shipping noise, Maritime Research Center website, a research note published on :

• Studying all techniques modeled so far, developing complete bookmark to 3D- version of the sound mapping propagating underwater Review from: Review to 3D mapping of Low frequency ambient shipping noise

TECHNICAL PROFICIENCY

Verilog, C & C++, Python, ML - Algorithms, LTspice, MATLAB, Simulink, PSIM, LabView

KEY COURSES TAKEN

Digital Design, Control Systems, Circuits and Networks, Microprocessors and Microcontroller Interfacing, Digital Signal Processing, Data Communication, Analog & Digital VLSI Design

POSITION OF RESPONSIBILITY

INSEES, Instrumentation and Electronics Engineering Society, Vice-president, NIT Silchar

April 2022 - Present

• Vice-president, managing and organizing core branch society, ALPHA-CRESCENDO 2021, in association with HexNBit, E-tech startup E-cell, Entrepreneurship cell, Senior associate, NIT Silchar

June 2022 - Present

• Core team member at organizing, managing and publicity team for Srijan 2.0&3.0 , in association with Geekforgeeks

NCC (National Cadet Corps), Cadet, NIT Silchar

January 2021 - Present

B-certificate holder under 3 Assam battalion, attended Cadre from 16th -23rd of Jan including activities like rifle shooting, mock drills

VOLUNTEER EXPERIENCE

Muskurahat Foundation (NGO), Mumbai - Role: Group funding volunteer

| Cause : Social Services

May 2022-July 2022

Part of the social media handling team and fund collection drive providing education to thousands of children

Hamari Pehchan NGO, Delhi - Role: Charity drive volunteer | Cause : Social Services

June 2022

Social media posts handling and executing a virtual charity drive for collecting funds through it

Gyansagar, NIT Silchar - Role: Media & Content Handler

| Cause : Social Service | March 2021- January 2022

• Assisted survey to find Old age homes in localities all over India and getting them recognition through a YouTube program Gyan-samwad

CO-CURRICULARS

 $[\]cdot \textbf{Musical Instrument} \cdot \textbf{Guitar} \cdot \textbf{Content writing} \cdot \textbf{professional} \cdot \textbf{Sport} \cdot \textbf{football} \cdot \textbf{Graphic designing} \cdot \textbf{Adobe Illustrator}$