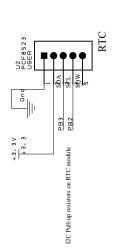
TM4C123 - Chip & Launchpad

| | PA7 | PA6 | PA5 | PA4 | PA3 | PA2 | PA1 | PAØ | |
|---------------------|----------|-------|---------|---------|-------|--------|-----|-----|--|
| | 24 RESET | 23 DC | 22 MOSI | 21 MISO | 20 CS | 19 SCK | 18 | 1.7 | |
| U1a TM4C123GH6PM | | PA6 | PA5 | PA4 | PA3 | PA2 | PA1 | PAØ | |

RTC - DS1307



J3 LogicAnalyzer DIP

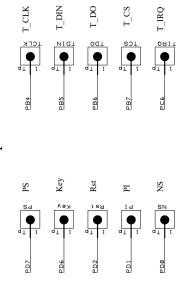
SCLK 16 Chang D
CS 15 Chang
MISO 14 Chang
MOSI 13 Chang
USTX 12 Chang
USTX 12 Chang

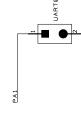
CS 15 C MISO 14 C MISO 14 C MISO 13 C USTX 12 C USTX 12 C USTX 12 C USTX 11 C USTX 11 C USTX 11 C USTX 11 C USTX 12 C USTX 12 C USTX 13 C USTX 14 C USTX 14 C USTX 15 C USTX 15

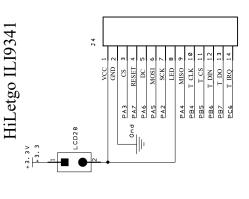
PA2 PA3 PA4 PA5 PE5 PE4 PB3

Logic Analyzer

Testpoints







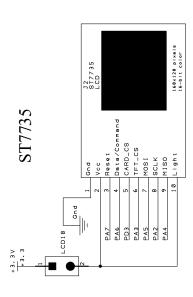
HiLetgo ILI9341

PB7 PB8 PB3 PB3

U1b TM4C123GH6PM LQFP

58 SCLK 48 12C0 SDA 47 12C0 SCL 46 UITX 45 UIRX

PB7 PB5 PB3 PB3 PB1



| | | J2 ST773E | LCD LCD | | | | | | | | 168×128 pixels | 16-bit color |
|--------|-------|--------------|------------|-------|--------------|---------|--------|------|------|------|----------------|--------------|
| ST7735 | | Gnd | Vcc | Reset | Data/Command | CARD_CS | TFT_CS | MOSI | SCLK | MISO | 10 Light | |
| | Gnd | | 2 | 7 3 | 4 | 3 5 | 3 6 | 7 | 2 8 | 6 | 1.0 | |
| 3.3 × | LCD18 | <u>v_</u> | | PA7 | PA6 | PD3 | PA3 | PAS | PA2 | PA4 | | |

U1d TM4C123GH6PM LQFP

PD7 PD6 PD5 PD3 PD3 PD1

University Of Texas At Austin

Lab 7 starter Schematic Name:

Arjun, Joshua, Paulina, Sihyung Name(s):

10/14/2019

Date:

Semester:

Fall 2019

