

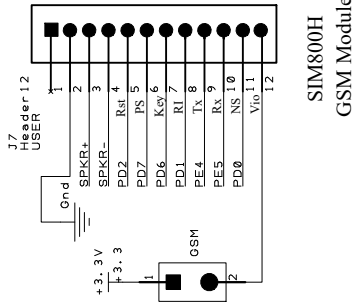
Logic Analyzer

[illegible]

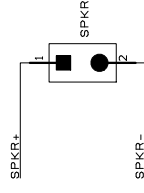
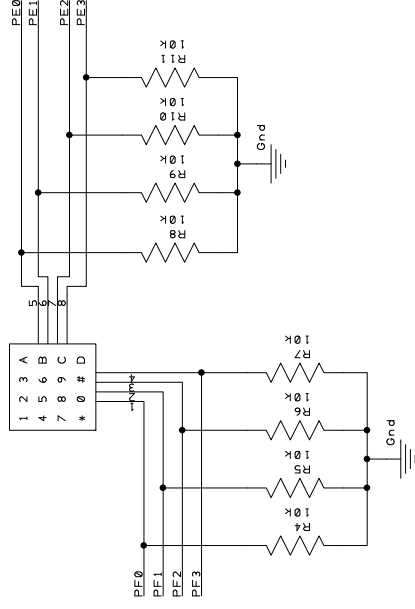
Logic Analyzer

University Of Texas At Austin	
Schematic Name: Lab 7 starter	
Name(s): Arjun, Joshua, Paulina, Sihyung	
Date: 10/14/2019	Semester: Fall 2019

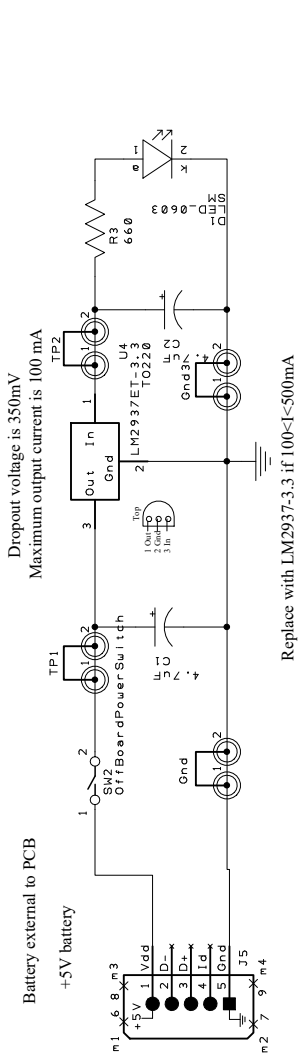
GSM - SIM800H



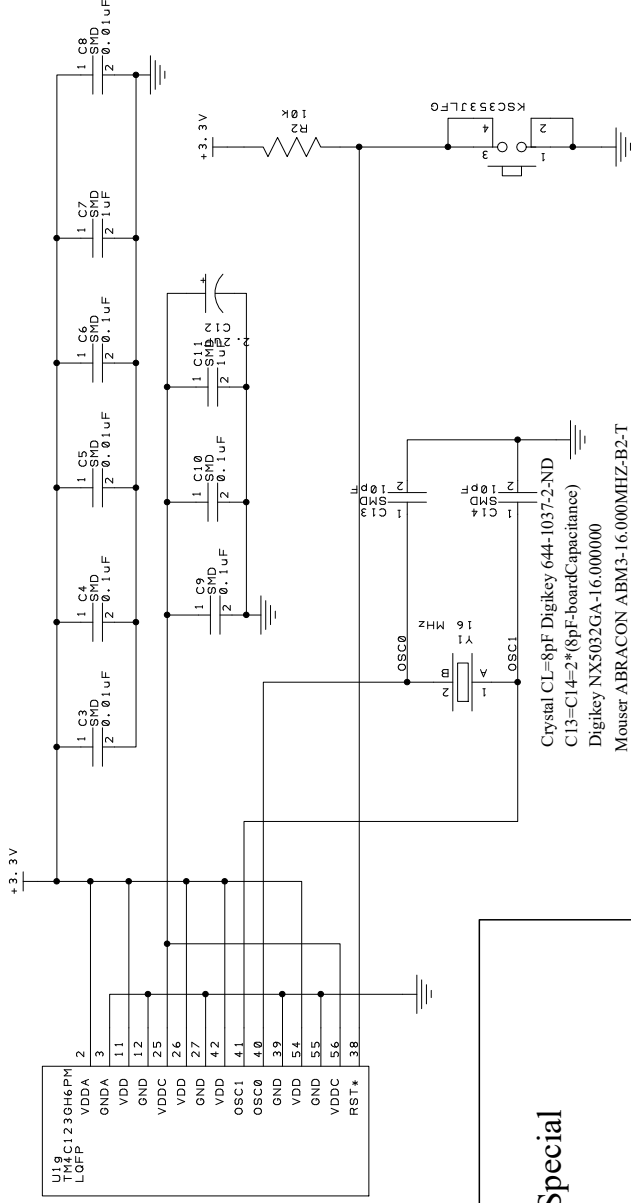
4x4 Numpad Input



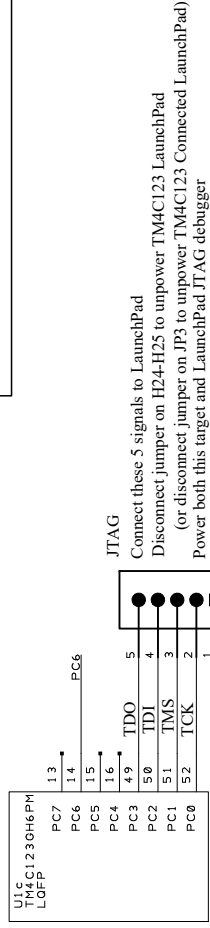
USB Power



TM4C1234 - Power



TM4C1234 - Special



University Of Texas At Austin

Schematic Name: Lab 7 starter file Power Clock JTAG

Name(s): Arjun, Joshua, Paulina, Sihyung

Date: 10/14/2019 Semester: Fall 2019

