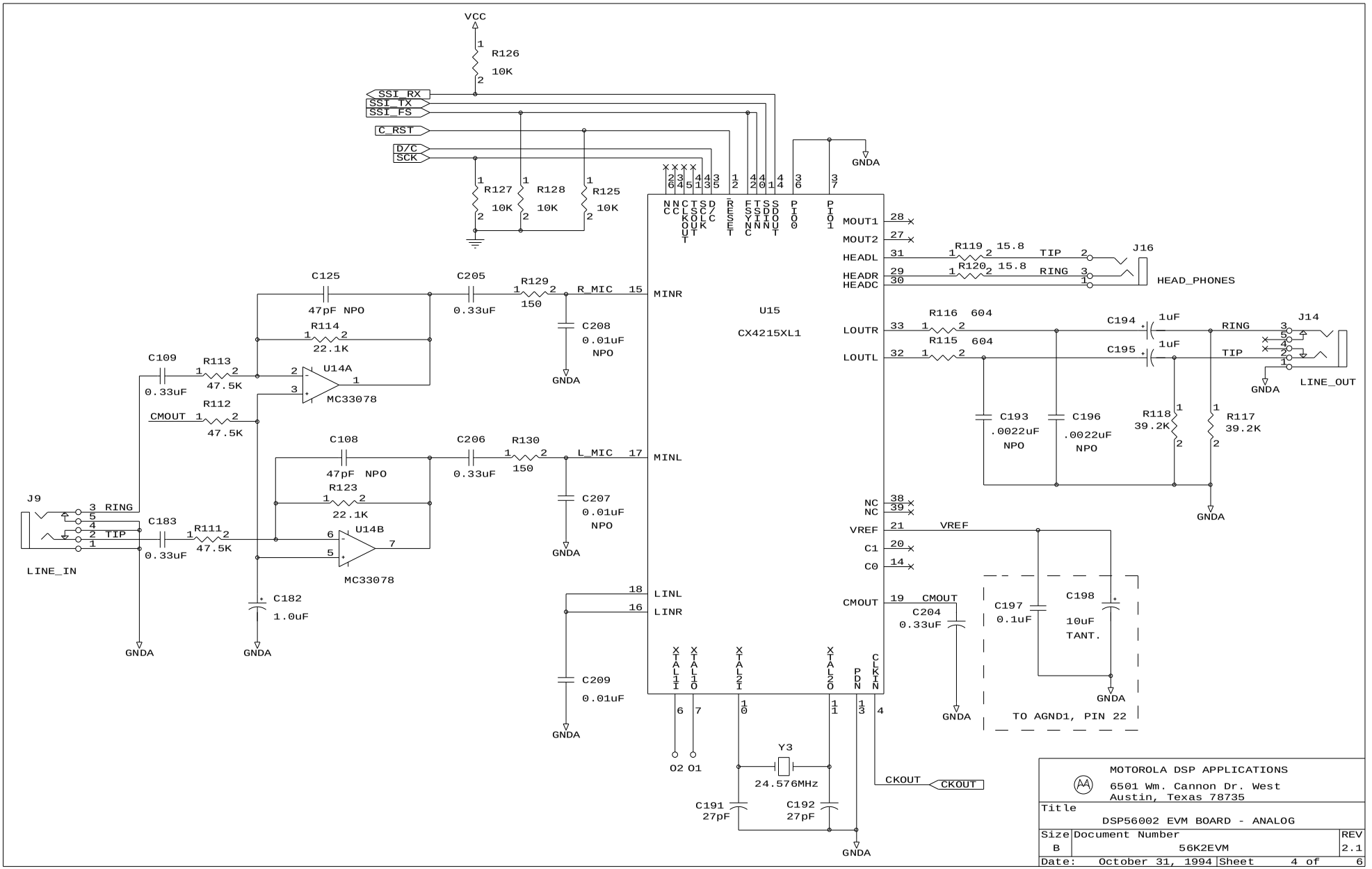
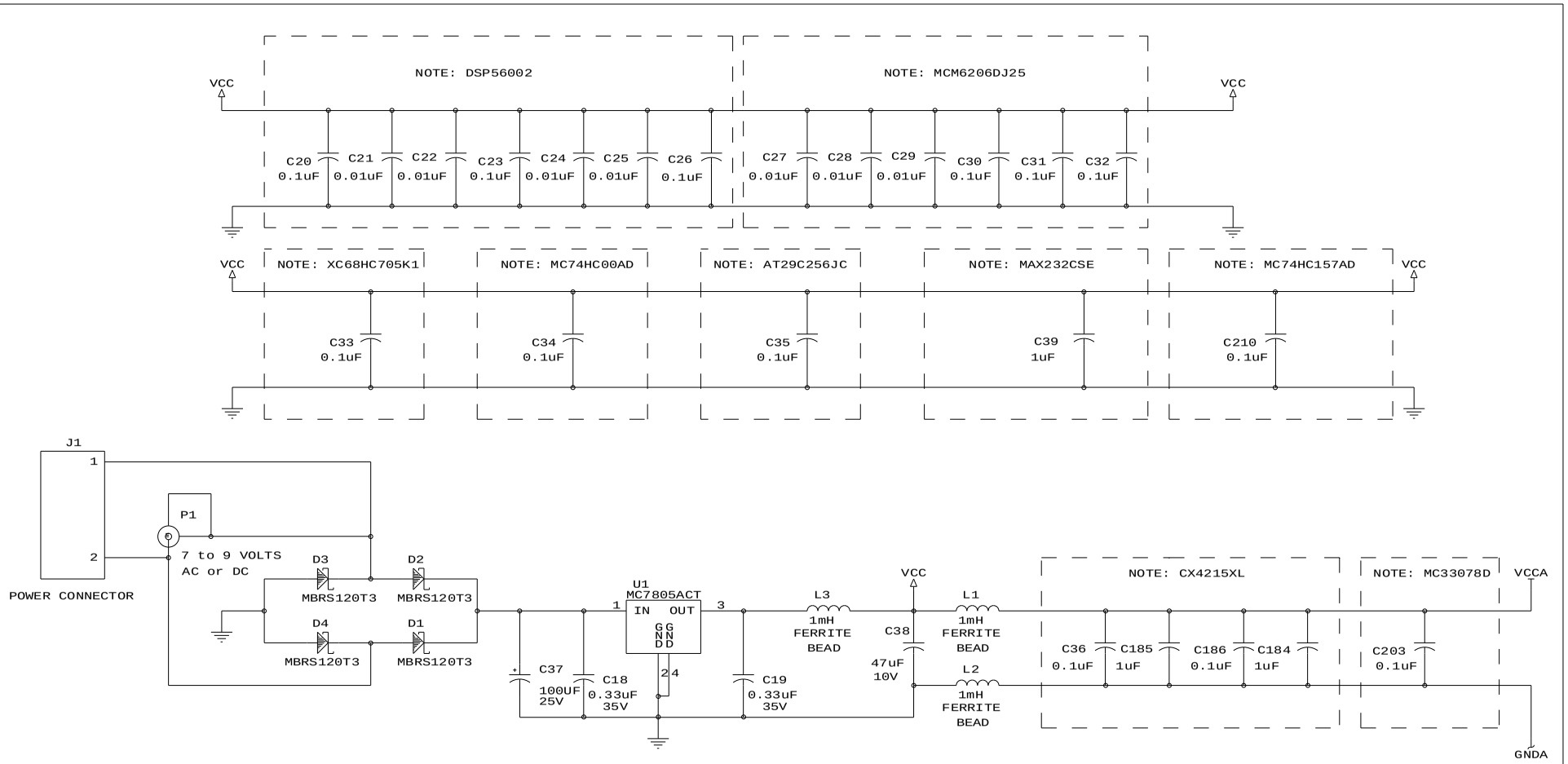
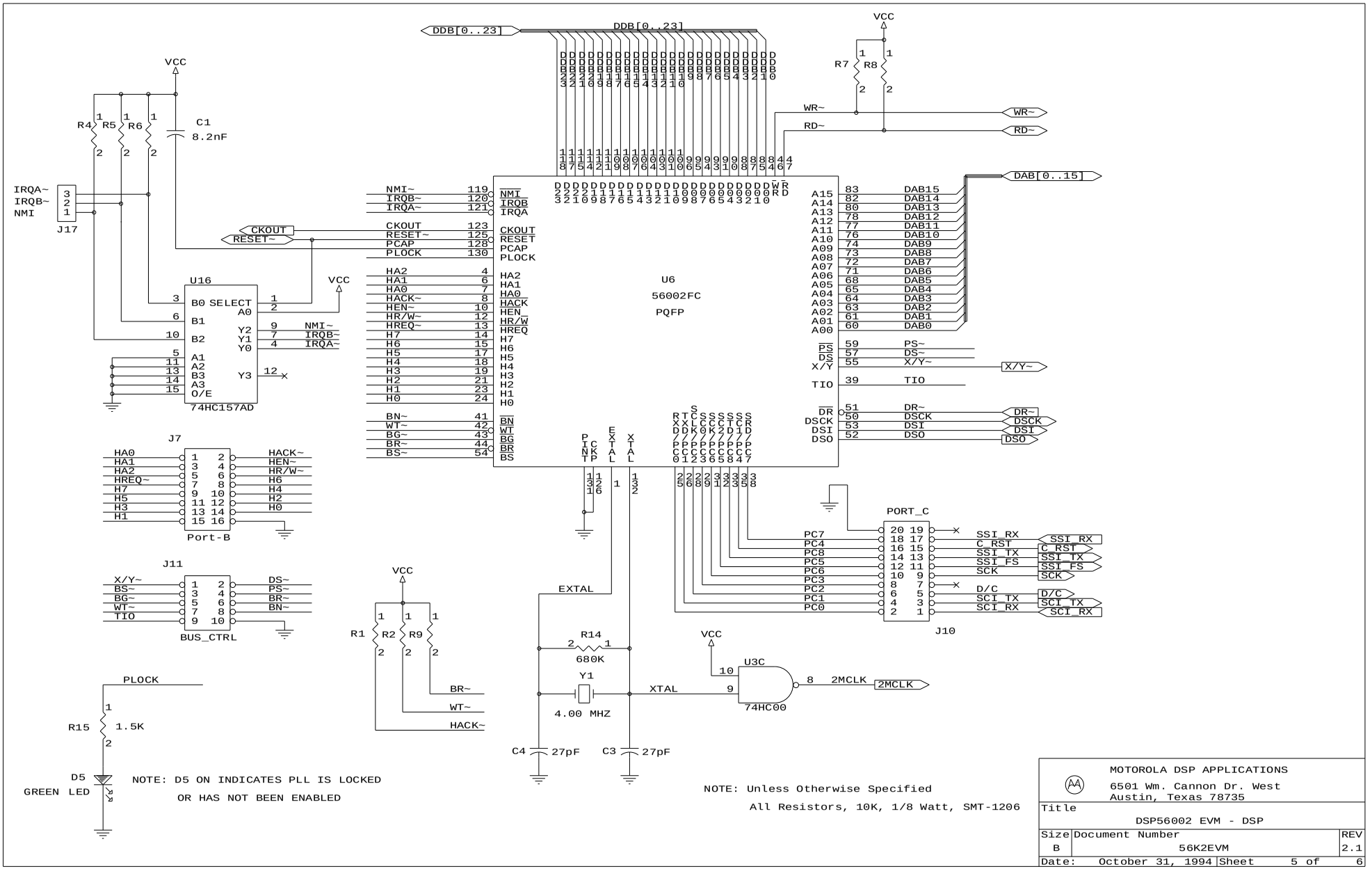

VCC
R126
10K
SSI_RX
SSI_TX
SSI_FS
C_RST
D/C
SCK
R127
10K
R128
10K
R125
10K
GNDA
U15
CX4215XL1
MOUT1
MOUT2
HEADL
HEADR
HEADC
MINR
MINL
LINL
LINR
LOUTR
LOUTL
NC
NC
VREF
C1
C0
CMOUT
XTALI
XTALO
XTAL2I
XTAL2O
PDN
CLKIN
C125
47pF NPO
C205
0.33uF
R129
150
R_MIC
C208
0.01uF
NPO
R114
22.1K
C109
0.33uF
R113
47.5K
U14A
MC33078
R112
47.5K
CMOUT
C108
47pF NPO
C206
0.33uF
R130
150
L_MIC
C207
0.01uF
NPO
R123
22.1K
J9
3 RING
2 TIP
1
LINE_IN
C183
0.33uF
R111
47.5K
U14B
MC33078
C182
1.0uF
C209
0.01uF
R119 15.8
R120 15.8
TIP
RING
J16
HEAD_PHONES
C194 1uF
C195 1uF
R116 604
R115 604
J14
LINE_OUT
C193
.0022uF
NPO
C196
.0022uF
NPO
R118
39.2K
R117
39.2K
VREF
C204
0.33uF
C197
0.1uF
C198
10uF
TANT.
TO AGND1, PIN 22
Y3
24.576MHz
C191
27pF
C192
27pF
O2 O1
CKOUT
MOTOROLA DSP APPLICATIONS
6501 Wm. Cannon Dr. West
Austin, Texas 78735
Title
DSP56002 EVM BOARD - ANALOG
Size B
Document Number
56K2EVM
REV 2.1
Date: October 31, 1994
Sheet 4 of 6



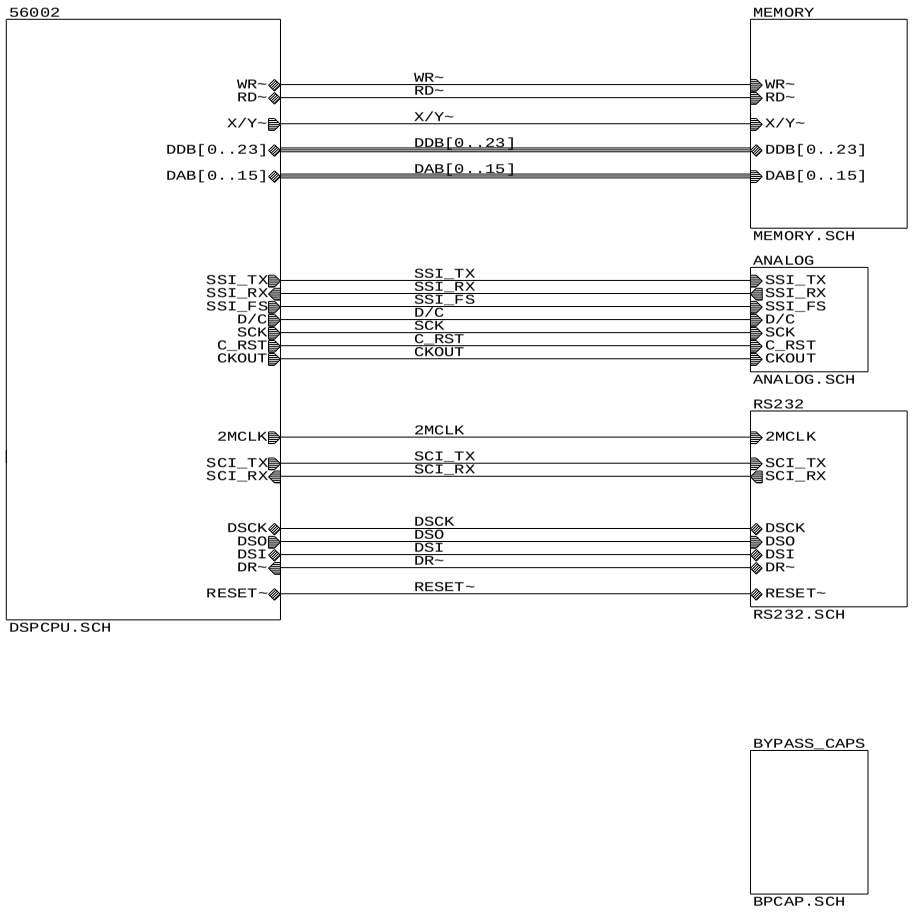
NOTE: C18,C19 ARE TANTALUM OR MYLAR CHIPCAPS

P1 IS 2.1mm Power Jack

MOTOROLA DSP APPLICATIONS 6501 Wm. Cannon Dr. West Austin, Texas 78735		
Title: DSP56002 EVM - POWER/BYPASS CAPACITORS		
Size: B	Document Number: 56K2EVM	REV: 2.1
Date: October 31, 1994	Sheet 2 of 6	

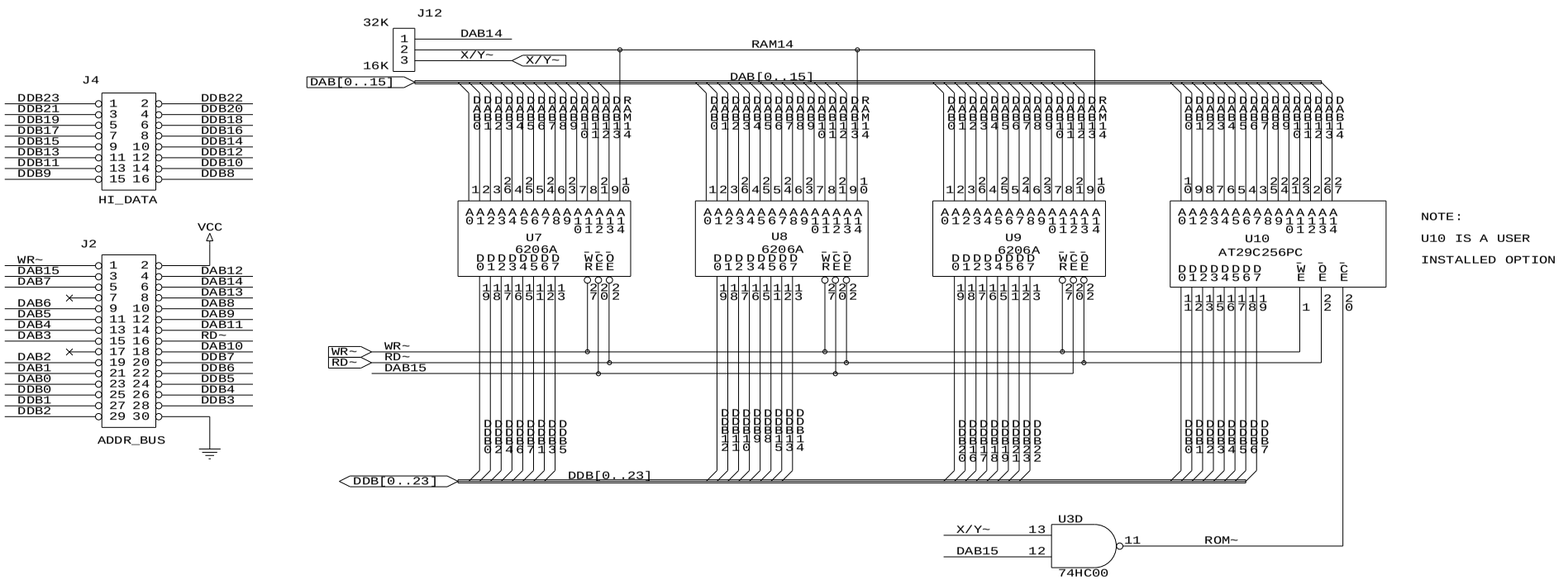


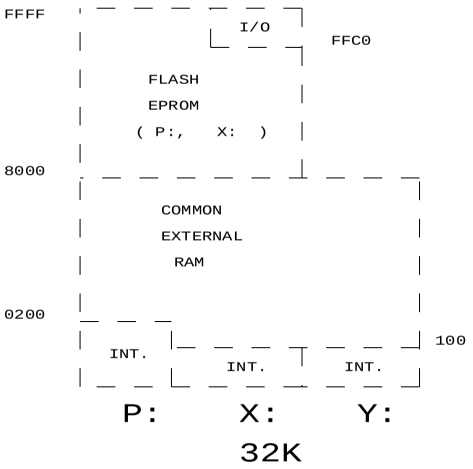
MOTOROLA DSP APPLICATIONS 6501 Wm. Cannon Dr. West Austin, Texas 78735		
Title: DSP56002 EVM - DSP		
Size: B	Document Number: 56K2EVM	REV: 2.1
Date: October 31, 1994	Sheet 5 of 6	

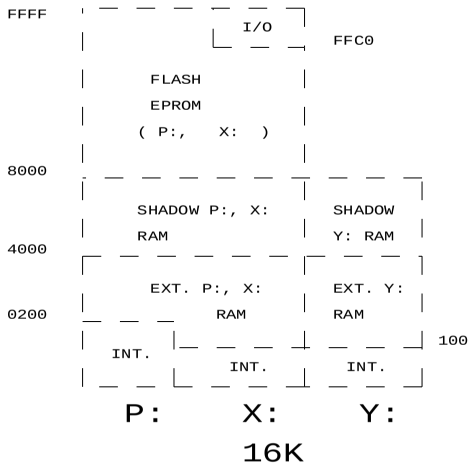


NOTE: ON ALL CONNECTORS (EXCEPT J12), Jx
DESIGNATOR IS PLACED NEXT TO PIN 1.

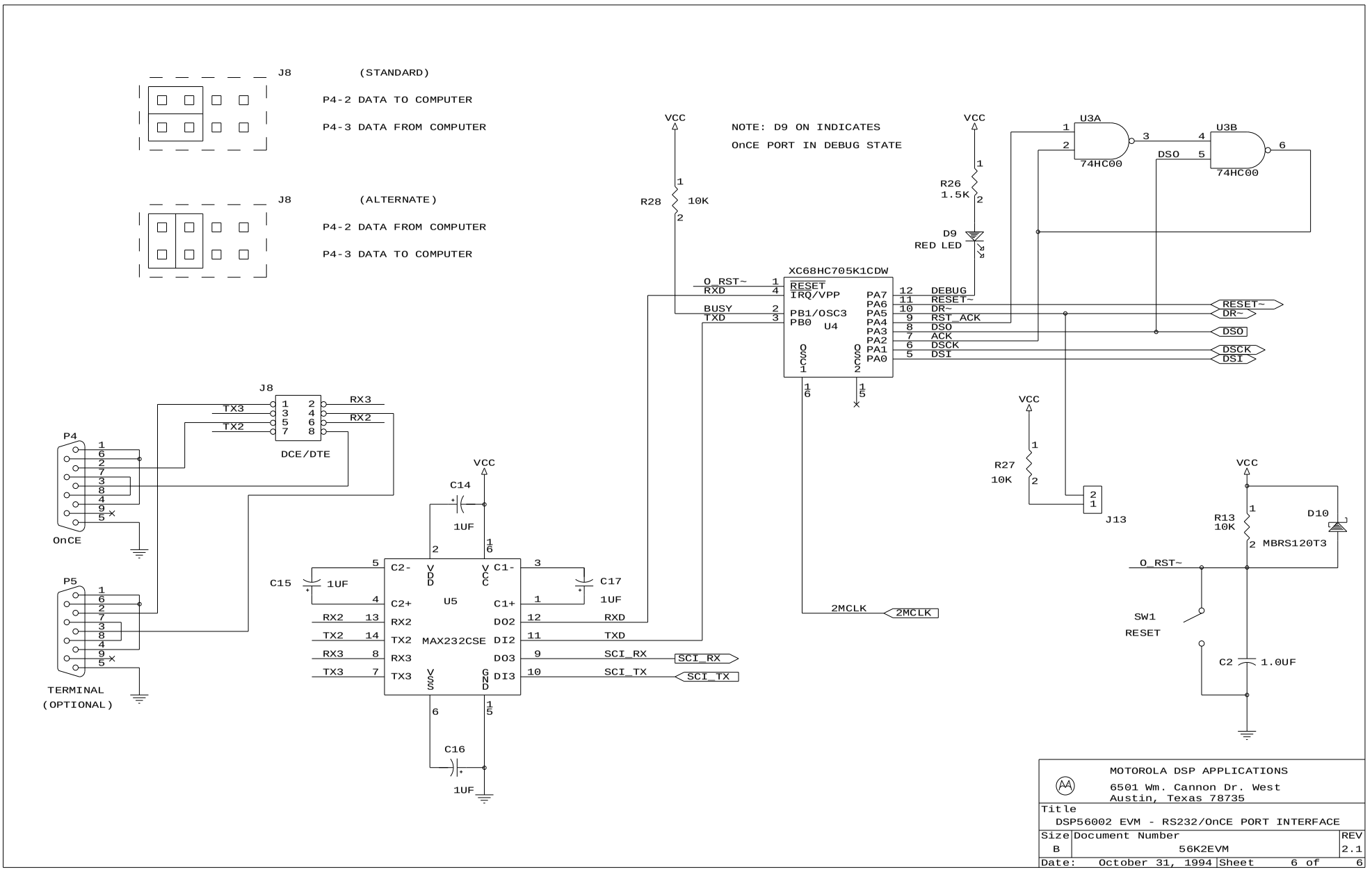
	MOTOROLA DSP APPLICATIONS 6501 Wm. Cannon Dr. West Austin, Texas 78735	
Title	DSP56002 EVM BOARD - MAIN	
Size: B	Document Number: 56K2EVM	REV: 2.1
Date: October 31, 1994	Sheet 1 of 6	







	MOTOROLA DSP APPLICATIONS 6501 Wm. Cannon Dr. West Austin, Texas 78735	
Title	DSP56002 EVM - MEMORY	
Size B	Document Number 56K2EVM	REV 2.1
Date: October 31, 1994	Sheet 3 of 6	



MOTOROLA DSP APPLICATIONS 6501 Wm. Cannon Dr. West Austin, Texas 78735		
Title DSP56002 EVM - RS232/OnCE PORT INTERFACE		
Size B	Document Number 56K2EVM	REV 2.1
Date: October 31, 1994	Sheet 6 of 6	