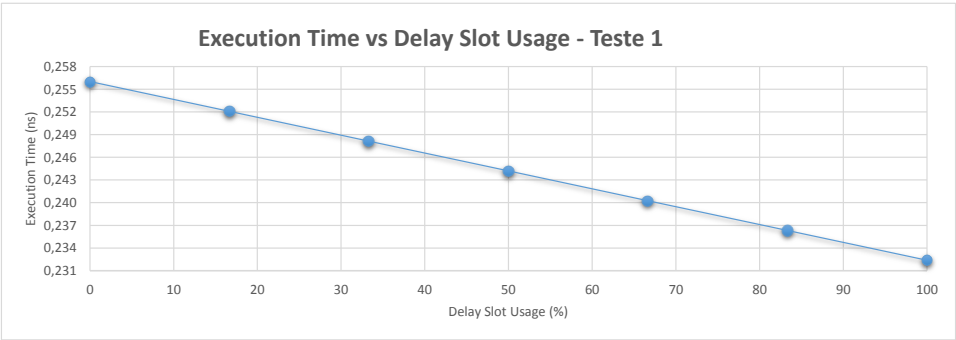
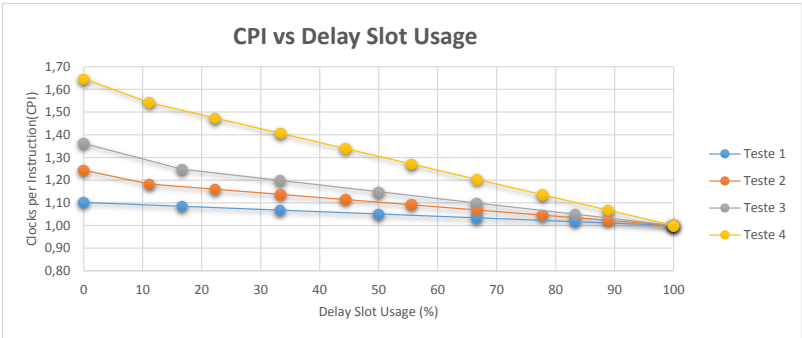
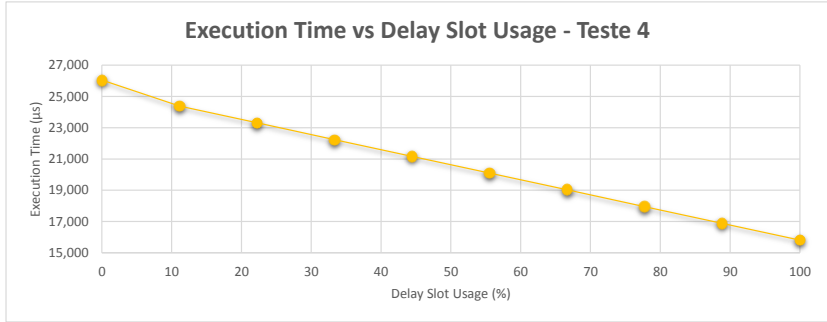
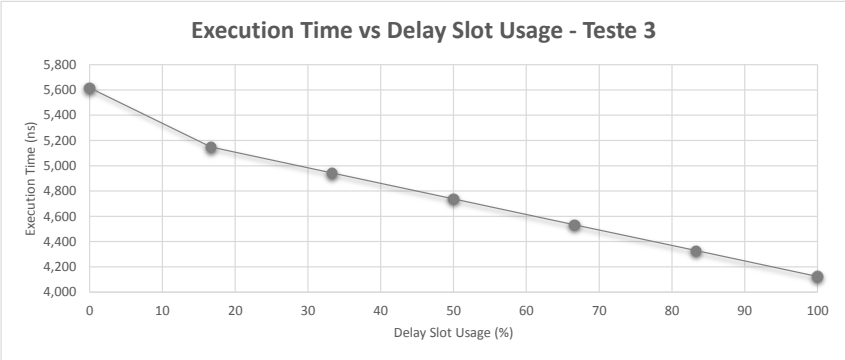
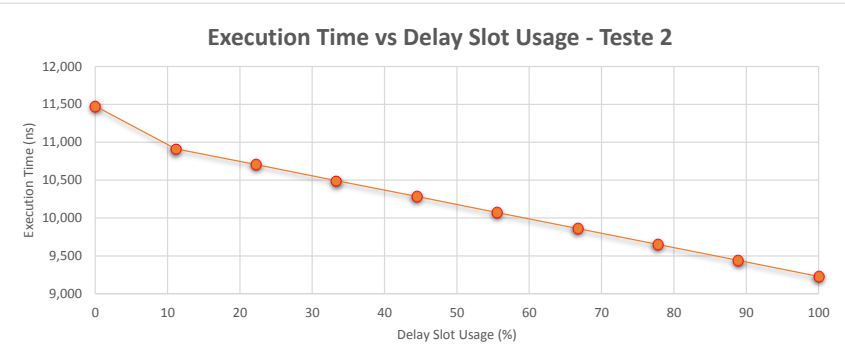


# Teste	Delay Slot Usage (%)	# Operations	# Cycles	# Nop	# Jumps	# Override	Jump Miss Rate (%)	CPI	Execution Time (ns)	Speed Up	Frequency (MHz)
1	0	59	65	6	7	0	0	1,10	0,256	1,000	253,89
	16,67	59	64	5				1,08	0,252	1,016	
	33,3	59	63	4				1,07	0,248	1,032	
	50	59	62	3				1,05	0,244	1,048	
	66,6	59	61	2				1,03	0,240	1,066	
	83,3	59	60	1				1,02	0,236	1,083	
	100	59	59	0				1	0,232	1,102	
2	0	2343	2914	481	523	90	17,21	1,24	11,477	1,000	
	11,11	2343	2771	428				1,18	10,914	1,052	
	22,22	2343	2718	375				1,16	10,705	1,072	
	33,33	2343	2664	321				1,14	10,493	1,094	
	44,44	2343	2611	268				1,11	10,284	1,116	
	55,56	2343	2557	214				1,09	10,071	1,140	
	66,67	2343	2504	161				1,07	9,863	1,164	
	77,78	2343	2450	107				1,05	9,650	1,189	
	88,89	2343	2397	54				1,02	9,441	1,216	
	100	2343	2343	0				1,00	9,228	1,244	
3	0	1047	1426	311	279	68	24,37	1,36	5,617	1,000	
	16,67	1047	1307	260				1,25	5,148	1,091	
	33,3	1047	1255	208				1,20	4,943	1,136	
	50	1047	1203	156				1,15	4,738	1,185	
	66,6	1047	1151	104				1,10	4,533	1,239	
	83,3	1047	1099	52				1,05	4,329	1,298	
	100	1047	1047	0				1,00	4,124	1,362	
4	0	4014670	6613297	2447628	2656355	150999	5,684	1,65	26047,883	1,000	
	11,11	4014670	6190340	2175670				1,54	24381,976	1,068	
	22,22	4014670	5918381	1903711				1,47	23310,808	1,117	
	33,33	4014670	5646422	1631752				1,41	22239,639	1,171	
	44,44	4014670	5374464	1359794				1,34	21168,475	1,231	
	55,56	4014670	5102505	1087835				1,27	20097,306	1,296	
	66,67	4014670	4830546	815876				1,20	19026,137	1,369	
	77,78	4014670	4558588	543918				1,14	17954,973	1,451	
	88,89	4014670	4286629	271959				1,07	16883,804	1,543	
	100	4014670	4014670	0				1,00	15812,635	1,647	



0,172744722



0	427,5556
100	332,8889
200	250
300	178,3333
400	119,1111
500	71,33333
600	35,77778
700	11,88889
800	0
900	0
1000	259,1563
1100	173,42