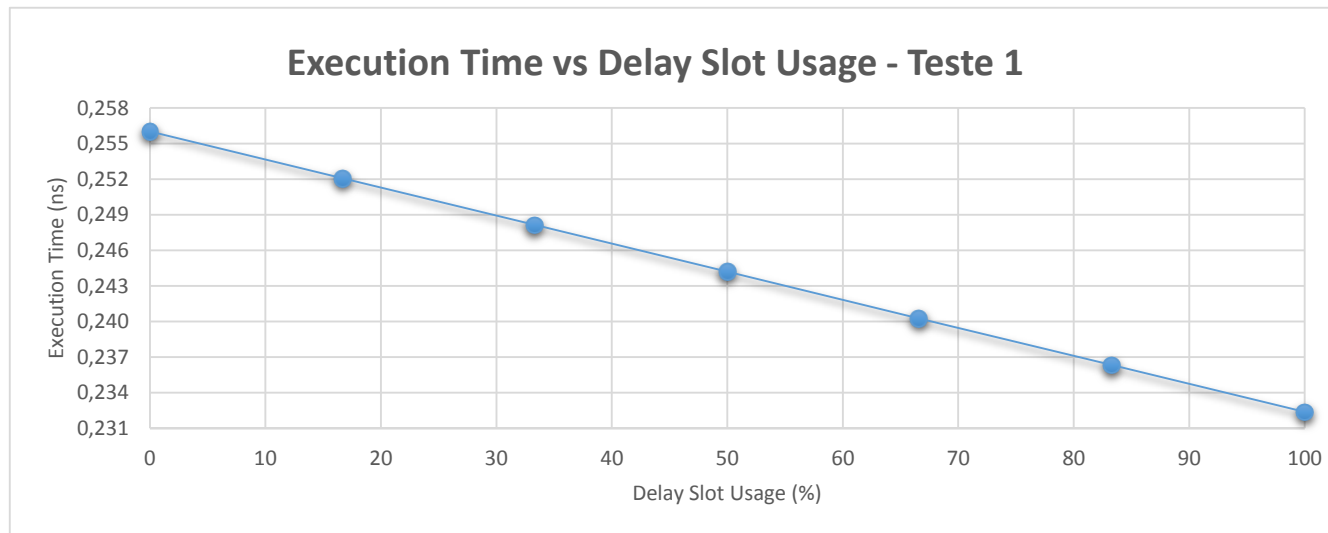
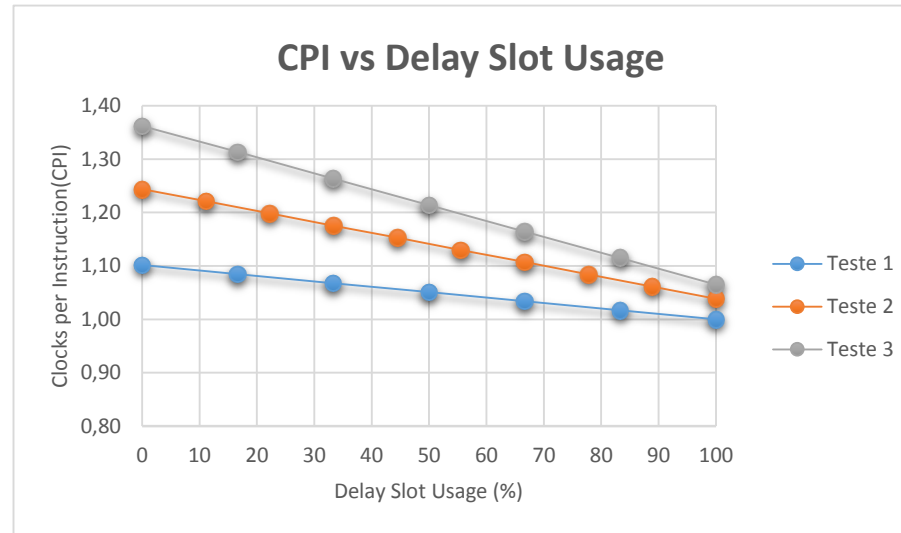
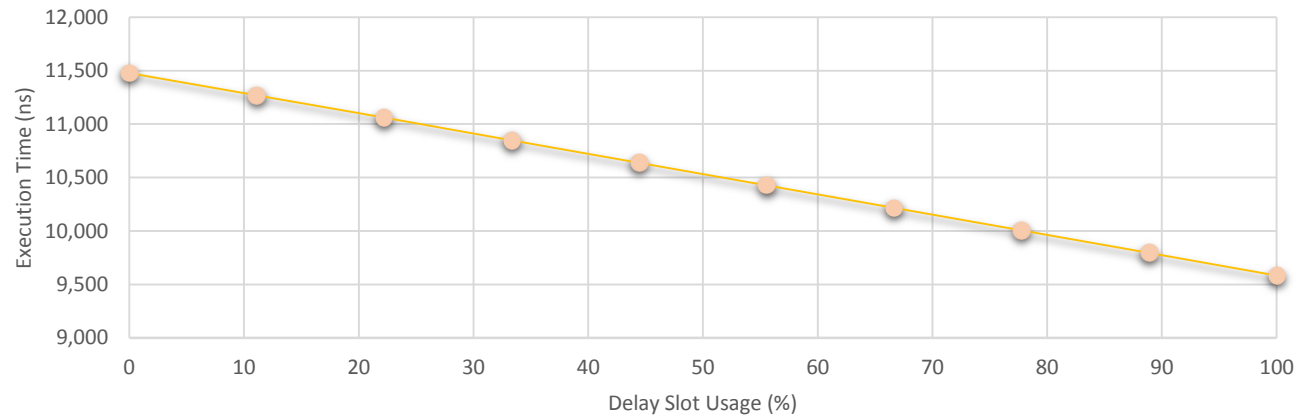


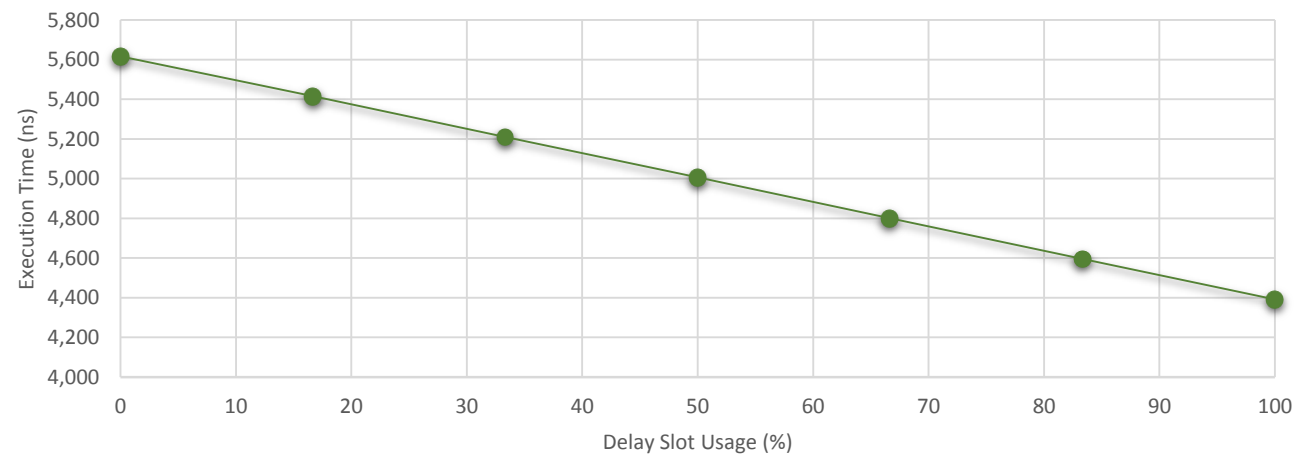
# Teste	Delay Slot Usage (%)	# Operations	# Cycles	# Nop	# Override	CPI	Execution Time (ns)	Frequency (MHz)
1	0	59	65	6	0	1,10	0,256	253,89
	16,67	59	64	5	0	1,08	0,252	
	33,3	59	63	4	0	1,07	0,248	
	50	59	62	3	0	1,05	0,244	
	66,6	59	61	2	0	1,03	0,240	
	83,3	59	60	1	0	1,02	0,236	
	100	59	59	0	0	1	0,232	
2	0	2343	2914	481	90	1,24	11,477	
	11,11	2343	2861	428	90	1,22	11,269	
	22,22	2343	2808	375	90	1,20	11,060	
	33,33	2343	2754	321	90	1,18	10,847	
	44,44	2343	2701	268	90	1,15	10,638	
	55,56	2343	2647	214	90	1,13	10,426	
	66,67	2343	2594	161	90	1,11	10,217	
	77,78	2343	2540	107	90	1,08	10,004	
	88,89	2343	2487	54	90	1,06	9,796	
	100	2343	2433	0	90	1,04	9,583	
3	0	1047	1426	311	68	1,36	5,617	
	16,67	1047	1375	260	68	1,31	5,416	
	33,3	1047	1323	208	68	1,26	5,211	
	50	1047	1271	156	68	1,21	5,006	
	66,6	1047	1219	104	68	1,16	4,801	
	83,3	1047	1167	52	68	1,11	4,596	
	100	1047	1115	0	68	1,06	4,392	



### Execution Time vs Delay Slot Usage - Teste 2



### Execution Time vs Delay Slot Usage - Teste 3





0	427,5556
100	332,8889
200	250
300	178,3333
400	119,1111
500	71,33333
600	35,77778
700	11,88889
800	0
900	0
1000	259,1563
1100	173,42