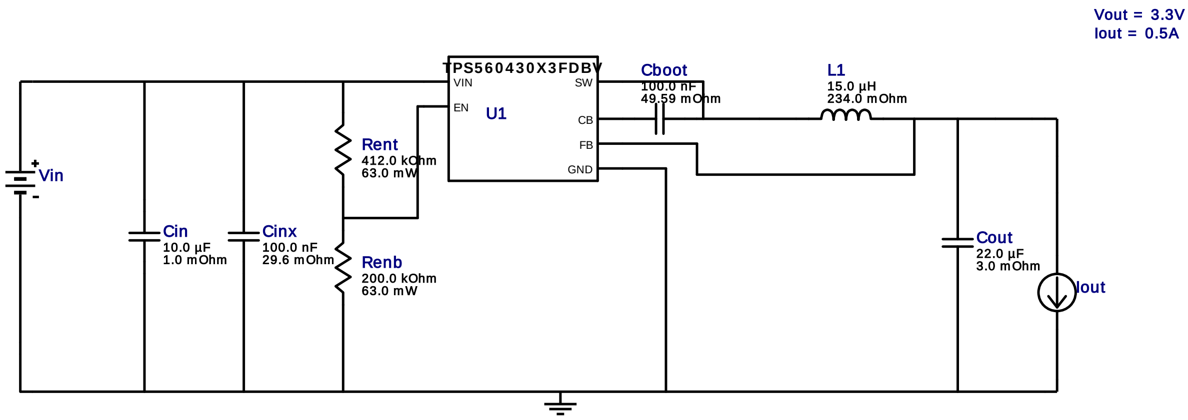
TEXAS INSTRUMENTS

WEBENCH ® Design Report

VinMin = 5.0V
VinMax = 35.0V
Vout = 3.3V
Iout = 0.5A

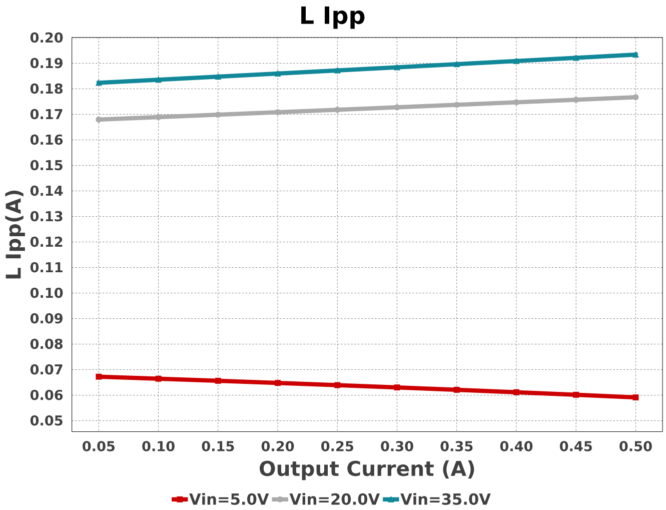
Device = TPS560430X3FDBVR
Topology = Buck
Created = 2025-07-24 04:22:03.682
BOM Cost = $1.18
BOM Count = 8
Total Pd = 0.49W

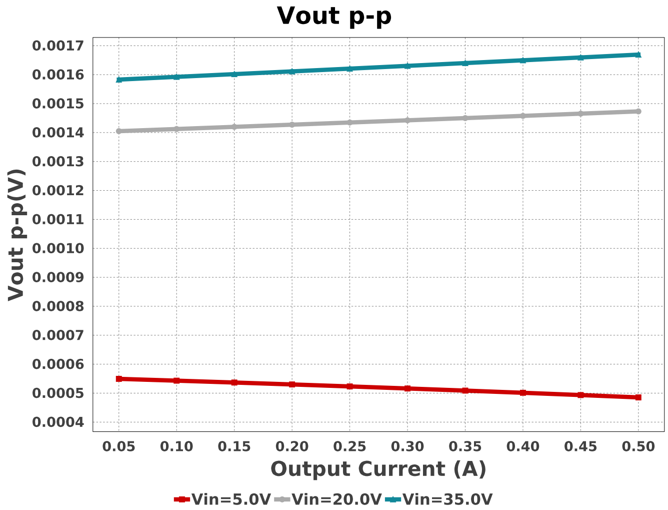
Design : 11 TPS560430X3FDBVR
TPS560430X3FDBVR 5V-35V to 3.30V @ 0.5A

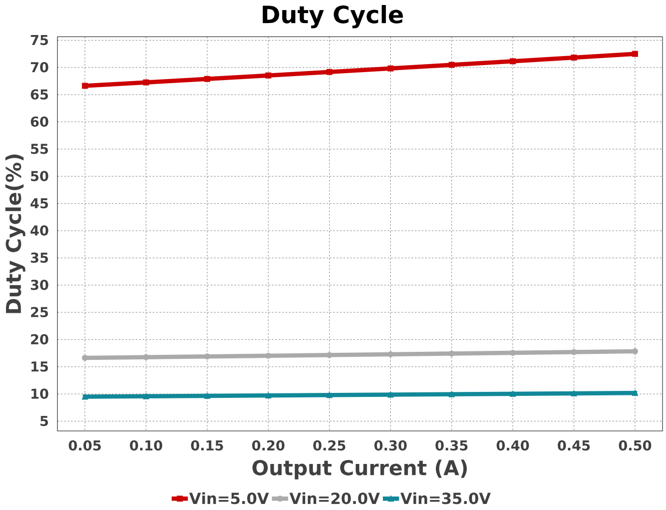


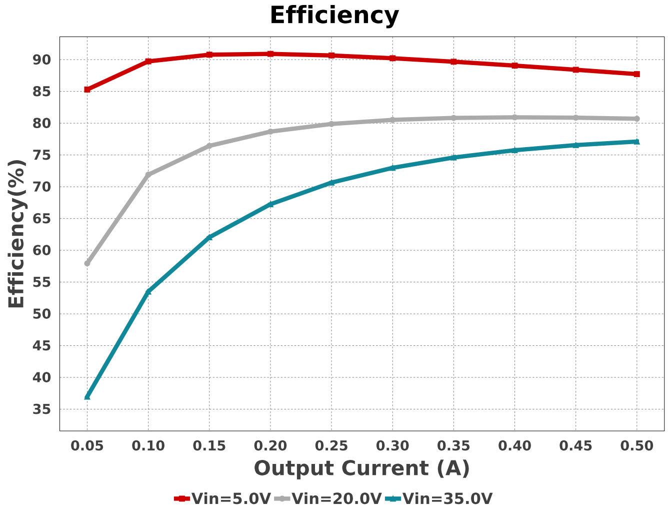
Electrical BOM

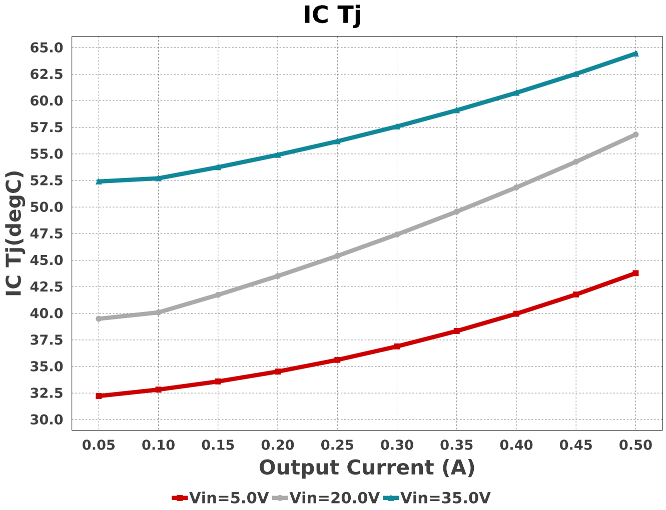
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cboot	TDK	C1608X7S2A104K080AB Series= X7S	Cap= 100.0 nF ESR= 49.59 mOhm VDC= 100.0 V IRMS= 751.62 mA	1	$0.03	0603 5 mm^2
Cin	MuRata	GRM32ER71J106KA12L Series= X7R	Cap= 10.0 uF ESR= 1.0 mOhm VDC= 63.0 V IRMS= 6.0 A	1	$0.30	1210_280 15 mm^2
Cinx	TDK	CGA3E2X7R1H104K080AA Series= X7R	Cap= 100.0 nF ESR= 29.6 mOhm VDC= 50.0 V IRMS= 971.99 mA	1	$0.01	0603 5 mm^2
Cout	MuRata	GRM21BR61A226ME44L Series= X5R	Cap= 22.0 uF ESR= 3.0 mOhm VDC= 10.0 V IRMS= 3.84 A	1	$0.09	0805 7 mm^2
L1	Coilcraft	LPS4018-153MRB	L= 15.0 µH 234.0 mOhm	1	$0.35	LPS4018 24 mm^2
Renb	Vishay-Dale	CRCW0402200KFKED Series= CRCW..e3	Res= 200.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2
Rent	Vishay-Dale	CRCW0402412KFKED Series= CRCW..e3	Res= 412.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2
U1	Texas Instruments	TPS560430X3FDBVR	Switcher	1	$0.38	DBV0006A 15 mm^2

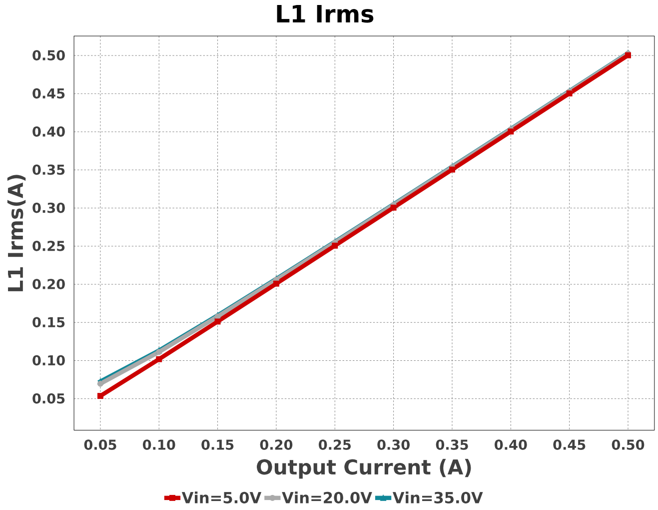

L Ipp
L Ipp(A)
Output Current (A)
Vin=5.0V Vin=20.0V Vin=35.0V

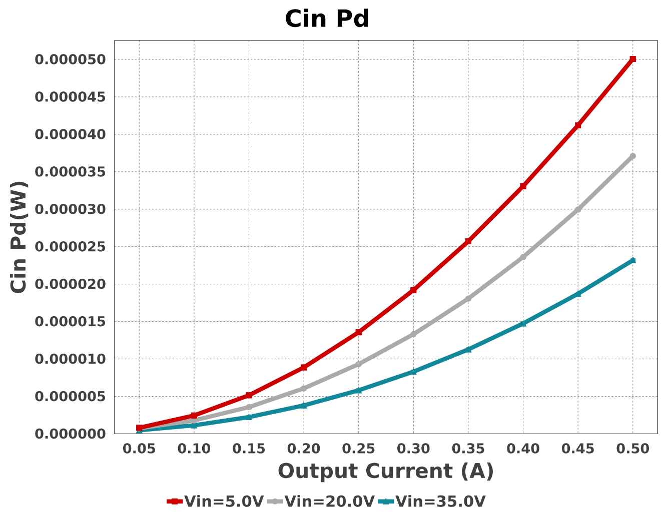

Vout p-p
Vout p-p(V)
Output Current (A)
Vin=5.0V Vin=20.0V Vin=35.0V

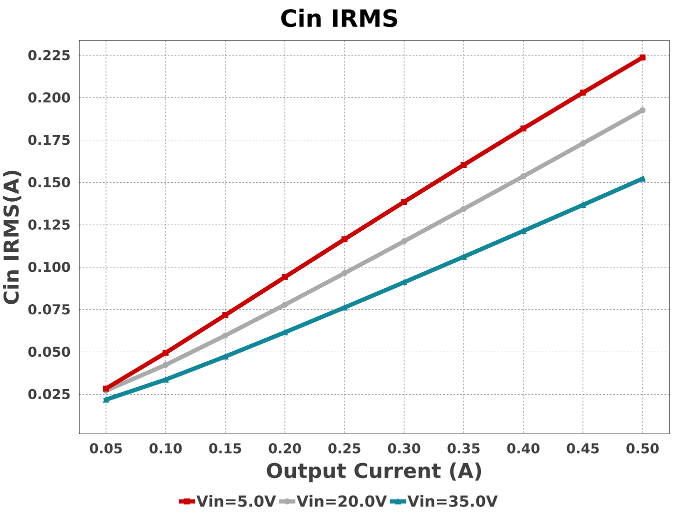

Duty Cycle
Duty Cycle(%)
Output Current (A)
Vin=5.0V Vin=20.0V Vin=35.0V

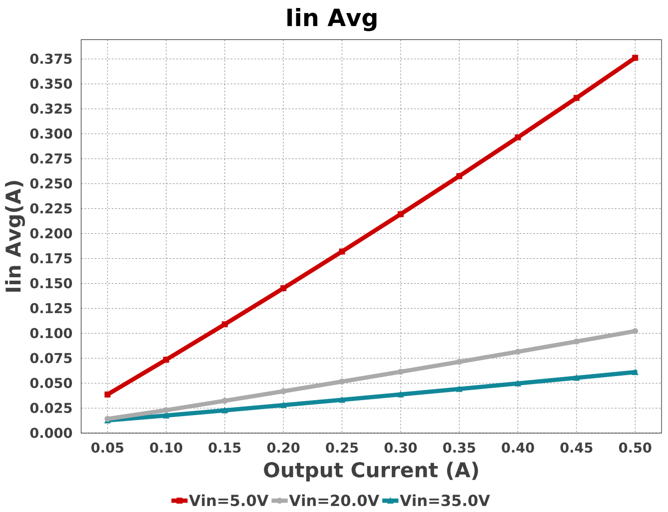

Efficiency
Efficiency(%)
Output Current (A)
Vin=5.0V Vin=20.0V Vin=35.0V

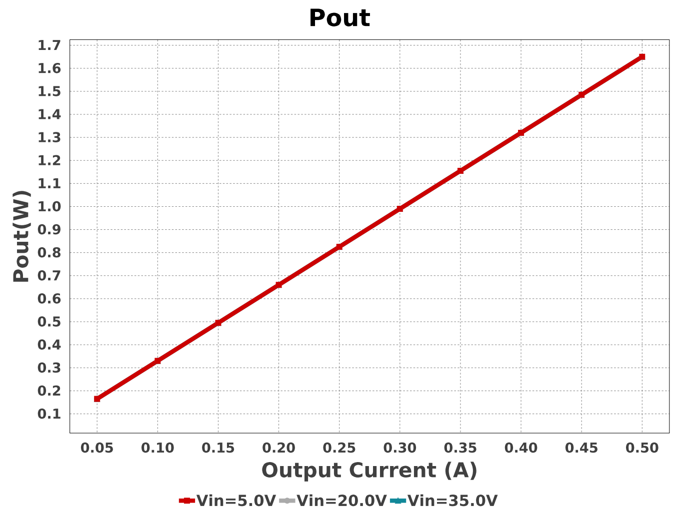

IC Tj
IC Tj(degC)
Output Current (A)
Vin=5.0V Vin=20.0V Vin=35.0V

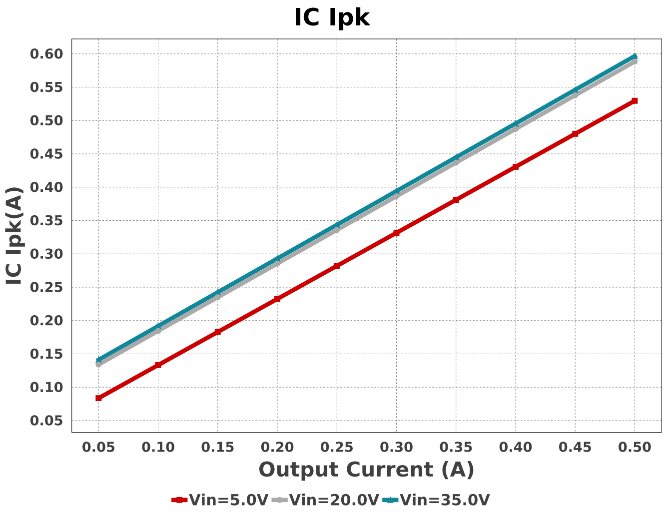

L1 Irms
L1 Irms(A)
Output Current (A)
Vin=5.0V Vin=20.0V Vin=35.0V

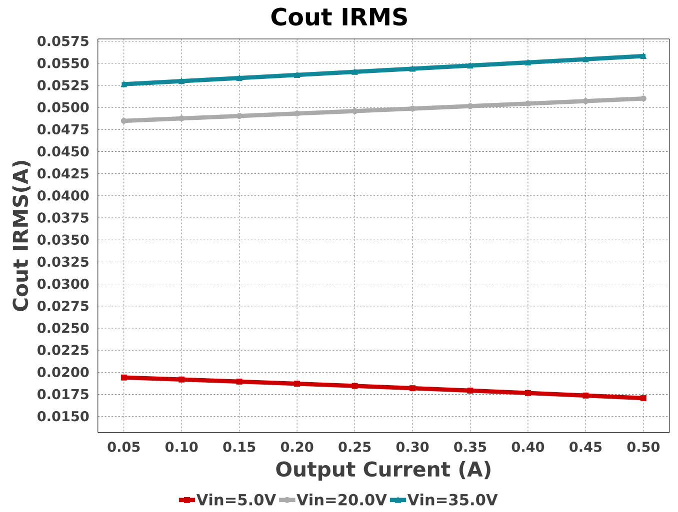

Cin Pd
Cin Pd(W)
Output Current (A)
Vin=5.0V Vin=20.0V Vin=35.0V

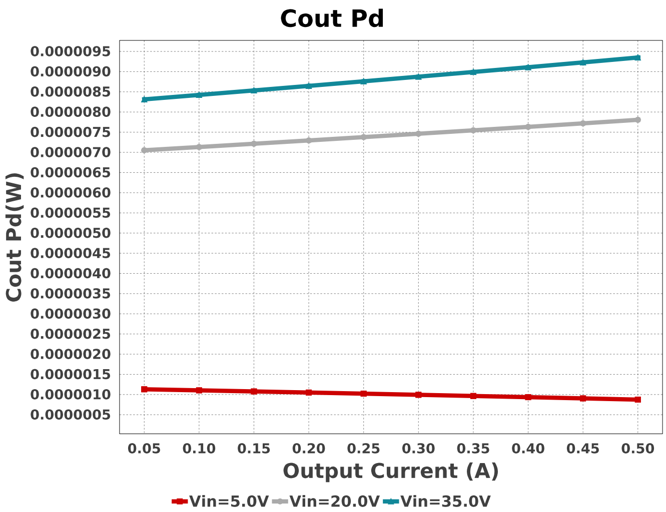

Cin IRMS
Cin IRMS(A)
Output Current (A)
Vin=5.0V Vin=20.0V Vin=35.0V

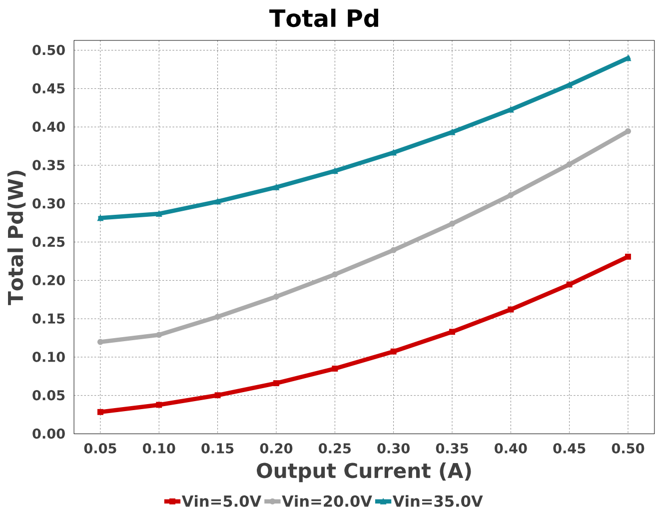

Iin Avg
Iin Avg(A)
Output Current (A)
Vin=5.0V Vin=20.0V Vin=35.0V

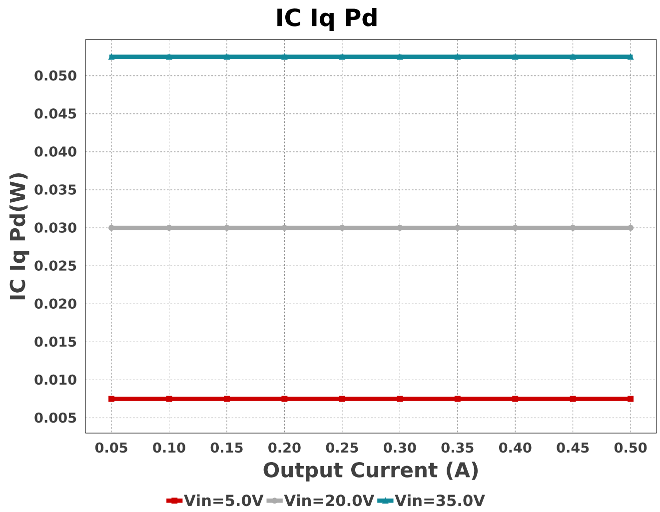

Pout
Pout(W)
Output Current (A)
Vin=5.0V Vin=20.0V Vin=35.0V

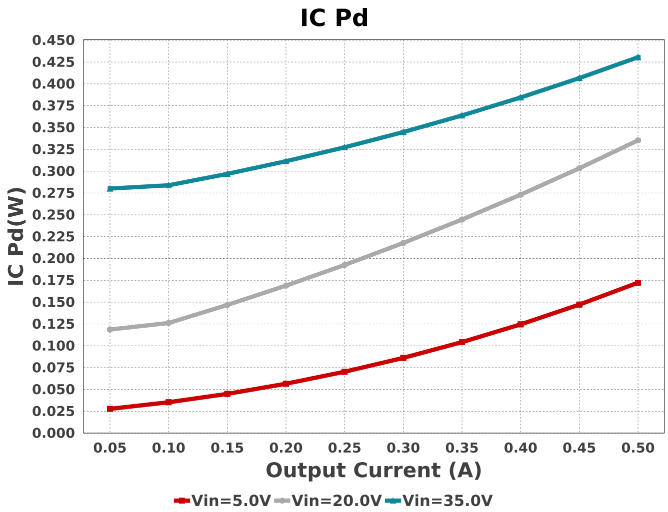

IC Ipk
IC Ipk(A)
Output Current (A)
Vin=5.0V Vin=20.0V Vin=35.0V

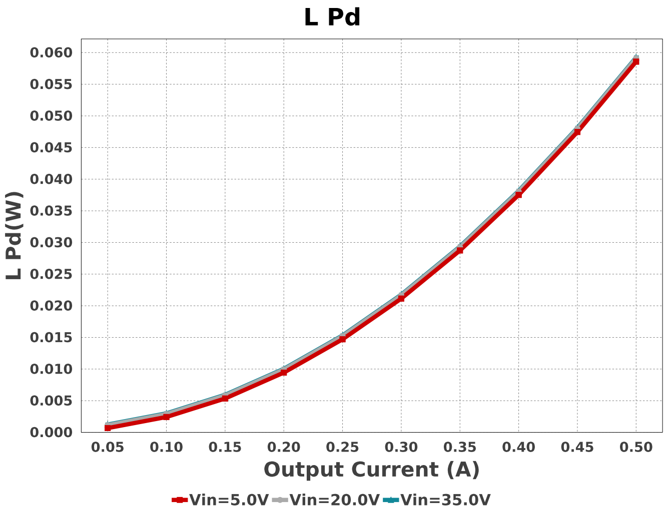

Cout IRMS
Cout IRMS(A)
Output Current (A)
Vin=5.0V Vin=20.0V Vin=35.0V

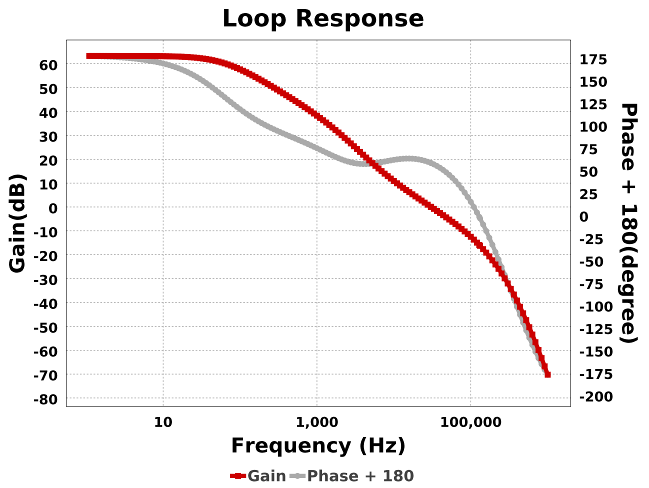












Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	152.245 mA	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	23.179 µW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	55.827 mA	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	9.35 µW	Capacitor	Output capacitor power dissipation
5.	IC Ipk	596.694 mA	IC	Peak switch current in IC
6.	IC Iq Pd	52.5 mW	IC	IC Iq Pd
7.	IC Pd	430.49 mW	IC	IC power dissipation
8.	IC Tj	64.439 degC	IC	IC junction temperature
9.	ICThetaJA Effective	80.0 degC/W	IC	Effective IC Junction-to-Ambient Thermal Resistance
10.	Iin Avg	61.136 mA	IC	Average input current
11.	L Ipp	193.39 mA	Inductor	Peak-to-peak inductor ripple current

#	Name	Value	Category	Description
12.	L Pd	59.229 mW	Inductor	Inductor power dissipation
13.	L1 Irms	503.107 mA	Inductor	Inductor ripple current
14.	Cin Pd	23.179 µW	Power	Input capacitor power dissipation
15.	Cout Pd	9.35 µW	Power	Output capacitor power dissipation
16.	IC Pd	430.49 mW	Power	IC power dissipation
17.	L Pd	59.229 mW	Power	Inductor power dissipation
18.	Total Pd	489.748 mW	Power	Total Power Dissipation
19.	BOM Count	8	System Information	Total Design BOM count
20.	Cross Freq	30.481 kHz	System Information	Bode plot crossover frequency
21.	Duty Cycle	10.181 %	System Information	Duty cycle
22.	Efficiency	77.112 %	System Information	Steady state efficiency
23.	FootPrint	76.0 mm^2	System Information	Total Foot Print Area of BOM components
24.	Frequency	1.1 MHz	System Information	Switching frequency
25.	Gain Marg	-15.935 dB	System Information	Bode Plot Gain Margin
26.	Iout	500.0 mA	System Information	Iout operating point
27.	Low Freq Gain	63.273 dB	System Information	Gain at 1Hz
28.	Mode	CCM	System Information	PWM/PFM Mode
29.	Phase Marg	59.805 deg	System Information	Bode Plot Phase Margin
30.	Pout	1.65 W	System Information	Total output power
31.	Total BOM	$1.18	System Information	Total BOM Cost
32.	Vin	35.0 V	System Information	Vin operating point
33.	Vout p-p	1.669 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	500.0 m	Maximum Output Current
VinMax	35.0	Maximum input voltage
VinMin	5.0	Minimum input voltage
Vout	3.3	Output Voltage
base_pn	TPS560430X3F	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

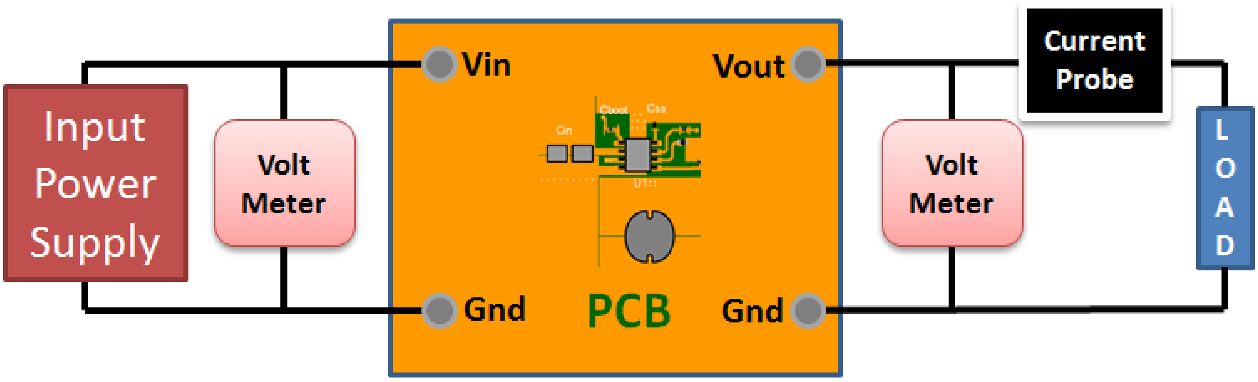
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 5.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum Iout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

1. Master key : A6C26876E8D3351BBD2F1B0082A933BD[v1]

2. **TPS560430X3F** Product Folder : http://www.ti.com/product/TPS560430 : contains the data sheet and other resources.