

Pinout diagram of the STM32F407VGT6 microcontroller. The diagram shows the 144 pins of the package, organized into two columns. The left column (pins 1-72) includes signals like SCL1, SDA1, AREF, u_LED, *12, *11, u_D10, R_D09, u_D8, GND, *7, *6, *5, *4, u_X-MIN, u_X-MAX, u_UART_TX, u_UART_RX, u_Y-MIN, u_Y-MAX, TX2/T17, RX2/T17, u_Z-MIN, u_Z-MAX, u_SDA, u_SCL, and 5Vx1. The right column (pins 73-144) includes signals like u_SCL, u_SDA, J1A, IOREF, RESET, J1G, J1F, TX0/RX0, J1E, u_Z-EN, A9, A10, A11, A12, u_THERMO, u_THERM1, THERM2, u_Z-DIR, u_Y-DIR, u_X-DIR, A3, A4, A5, u_Y-STEP, u_X-STEP, and 3Vx1. The diagram also shows the internal connections between the pins and the microcontroller's internal blocks, including the I/O blocks, the internal registers, and the internal logic. The pins are color-coded: green for digital, red for analog, and blue for power. The diagram is labeled with 'Digital', 'Analog', and 'Power' sections.

The diagram shows the pinout of the ADXL345 accelerometer, organized into Digital and Analog sections. It includes pin numbers, pin names, and functions.

Digital Pins:

- Pin 1:** `D21/SCL` (I2C SCL)
- Pin 2:** `D20/SDA` (I2C SDA)
- Pin 3:** `AREF` (Analog Reference)
- Pin 4:** `r_LED` (LED output)
- Pin 5:** `r_12` (Digital output)
- Pin 6:** `r_81` (Digital output)
- Pin 7:** `r_11` (Digital output)
- Pin 8:** `r_D10` (Digital output)
- Pin 9:** `r_D9` (Digital output)
- Pin 10:** `r_DB` (Digital output)
- Pin 11:** `Digital` (General digital pin)
- Pin 12:** `GND` (Ground)
- Pin 13:** `r_7` (Digital output)
- Pin 14:** `r_6` (Digital output)
- Pin 15:** `r_5` (Digital output)
- Pin 16:** `r_4` (Digital output)
- Pin 17:** `r_3` (Digital output)
- Pin 18:** `r_2` (Digital output)
- Pin 19:** `r_1` (Digital output)
- Pin 20:** `r_0` (Digital output)
- Pin 21:** `r_23` (Digital output)
- Pin 22:** `r_22` (Digital output)
- Pin 23:** `r_21` (Digital output)
- Pin 24:** `r_20` (Digital output)
- Pin 25:** `r_19` (Digital output)
- Pin 26:** `r_18` (Digital output)
- Pin 27:** `r_17` (Digital output)
- Pin 28:** `r_16` (Digital output)
- Pin 29:** `r_15` (Digital output)
- Pin 30:** `r_14` (Digital output)
- Pin 31:** `r_13` (Digital output)
- Pin 32:** `r_12` (Digital output)
- Pin 33:** `r_11` (Digital output)
- Pin 34:** `r_10` (Digital output)
- Pin 35:** `r_9` (Digital output)
- Pin 36:** `r_8` (Digital output)
- Pin 37:** `r_7` (Digital output)
- Pin 38:** `r_6` (Digital output)
- Pin 39:** `r_5` (Digital output)
- Pin 40:** `r_4` (Digital output)
- Pin 41:** `r_3` (Digital output)
- Pin 42:** `r_2` (Digital output)
- Pin 43:** `r_1` (Digital output)
- Pin 44:** `r_0` (Digital output)
- Pin 45:** `r_23` (Digital output)
- Pin 46:** `r_22` (Digital output)
- Pin 47:** `r_21` (Digital output)
- Pin 48:** `r_20` (Digital output)
- Pin 49:** `r_19` (Digital output)
- Pin 50:** `r_18` (Digital output)
- Pin 51:** `r_17` (Digital output)
- Pin 52:** `r_16` (Digital output)
- Pin 53:** `r_15` (Digital output)
- Pin 54:** `r_14` (Digital output)
- Pin 55:** `r_13` (Digital output)
- Pin 56:** `r_12` (Digital output)
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- Pin 62:** `r_6` (Digital output)
- Pin 63:** `r_5` (Digital output)
- Pin 64:** `r_4` (Digital output)
- Pin 65:** `r_3` (Digital output)
- Pin 66:** `r_2` (Digital output)
- Pin 67:** `r_1` (Digital output)
- Pin 68:** `r_0` (Digital output)
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- Pin 100:** `r_16` (Digital output)
- Pin 101:** `r_15` (Digital output)
- Pin 102:** `r_14` (Digital output)
- Pin 103:** `r_13` (Digital output)
- Pin 104:** `r_12` (Digital output)
- Pin 105:** `r_11` (Digital output)
- Pin 106:** `r_10` (Digital output)
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- Pin 115:** `r_1` (Digital output)
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- Pin 121:** `r_19` (Digital output)
- Pin 122:** `r_18` (Digital output)
- Pin 123:** `r_17` (Digital output)
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- Pin 129:** `r_11` (Digital output)
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- Pin 134:** `r_6` (Digital output)
- Pin 135:** `r_5` (Digital output)
- Pin 136:** `r_4` (Digital output)
- Pin 137:** `r_3` (Digital output)
- Pin 138:** `r_2` (Digital output)
- Pin 139:** `r_1` (Digital output)
- Pin 140:** `r_0` (Digital output)
- Pin 141:** `r_23` (Digital output)
- Pin 142:** `r_22` (Digital output)
- Pin 143:** `r_21` (Digital output)
- Pin 144:** `r_20` (Digital output)
- Pin 145:** `r_19` (Digital output)
- Pin 146:** `r_18` (Digital output)
- Pin 147:** `r_17` (Digital output)
- Pin 148:** `r_16` (Digital output)
- Pin 149:** `r_15` (Digital output)
- Pin 150:</**

The image contains three circuit diagrams related to the PTMUX4053PWR component.

Top Diagram: PTMUX4053PWR Schematic
 This diagram shows the internal structure of the PTMUX4053PWR, which is a 16-channel multiplexer. The input pins are labeled 6 (*EN), 9 (h_SEL3), 10 (PGA-THERM_EN_n), 12 (r_THERMO), 13 (op_mux_THERMO), 14 (r_THERM1), 15 (h_S3A), 16 (h_S3B), and 17 (5V_sel). The output pins are labeled D3, D1, SEL1, and 11. The internal structure shows a series of multiplexers (S1A, S1B, S2A, S2B, S3A, S3B) that route the input signals to the output pins. The output pins are also labeled with their corresponding digital values: D3 (4), D1 (14), SEL1 (15), and 11 (1).

Middle Diagram: Pull-down Circuit
 This diagram shows a pull-down circuit for the `f_mux_FPGA-THERM_EN_n` signal. The signal is connected to a 4.7k resistor, which is then connected to a 3p3V_sel input. The output of the resistor is connected to the `f_mux_FPGA-THERM_EN_n` signal. The signal is also connected to a 1V output.

Bottom Diagram: Voltage Divider
 This diagram shows a voltage divider circuit for the 5V_VSS_sel input. The input is connected to a 1k resistor, which is then connected to a 5k resistor. The output of the 5k resistor is connected to the 5V_VSS_sel input. The output of the divider is labeled `1V`.

[illegible]

JP13
Jumper_2_Open
u_SCL_1 o_2_r_SCL

JP20
Jumper_2_Open
u_SDA_1 o_2_r_SDA

Mounting Holes

The diagram shows four mounting holes arranged in a 2x2 grid. The top-left hole is labeled H5 MountingHole_Pad, the top-right is H7 MountingHole_Pad, the bottom-left is H6 MountingHole_Pad, and the bottom-right is H8 MountingHole_Pad. Green lines connect H5 and H6 to a common ground plane symbol on the left. Red lines connect H7 and H8 to a common power plane symbol on the right.

