

vending.v 運行指令後的截圖

```
setup> cirread -v design/SoCV/vending/vending.v
Error: circuit already exists!!

setup> print info -verbose
Modules in current design: \vendingMachine(242 wires, 71 cells)
=====
MUX7
AND3
ADD22
SUB9
MUL8
EQ9
NOT2
LT1
GE8
-----
PI7
PO7
=====

setup> █
```

vending.aig 運行指令後的截圖

```
setup> cirread -aig design/SoCV/vending/vending.aig
Converted 0 1-valued FFs and 48 DC-valued FFs.

setup> print info -verbose
[ERROR]: Please read the word-level design first !!
[ERROR]: Use "cirprint" to print the aig info.

setup> █
```

由於該指令不支援 aig 格式的檔案，必須更換指令成 cirprint 才能運行，如圖：

```
setup> cirread -aig design/SoCV/vending/vending.aig
Converted 0 1-valued FFs and 48 DC-valued FFs.

setup> cirprint

Circuit Statistics
=====
PI          60
PO          17
LATCH       49
AIG         3036
-----
Total       3162

setup> █
```

vending.blif 運行指令後的截圖

```
setup> cirread -replace -blif design/SoCV/vending/vending.blif
Note: original circuit is replaced...

setup> print info -verbose
Modules in current design: \vendingMachine(550 wires, 177 cells)
=====
MUX58
AND2
ADD22
SUB9
MUL6
EQ7
NOT7
LT1
GE8
-----
PI12
PO17
=====

setup> |
```