

WC: Write controle

0 : write enabled o : write inhibited

E2: Chip Enable = 63

Device addressing:

Device	type	identifier		Chip Enable		address	R/w	
67	66	PZ	64	63	62	61	bo	
1	0	1	0	E2	A9	48		

Binary: Hex 10100000 -> 0xA0