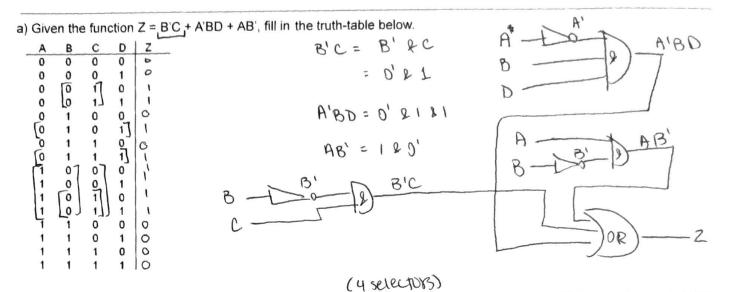
Recitation#9: Mux Application

CS232 Spring 2021

When: March 26 at 2:00 pm



b) Given the truth table of part (a), implement Z using a single 16:1 multiplexer shown below. Make sure to clearly label all inputs and outputs.

OUT 8 16:1 MUX Ь S3 S2 S1 S0 5,