



**Matthias Jobst**

- German, English, Spanish speaking
- Python
- C++
- Verilog
- Tensorflow

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**CeTI**  
Centre for Tactile Internet  
with Human-in-the-Loop

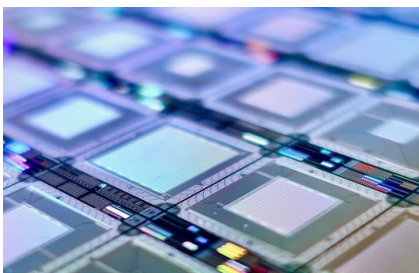
PhD Student (1y) at Chair of Highly-Parallel VLSI Systems and Neuro-Microelectronics

### Research Interest:

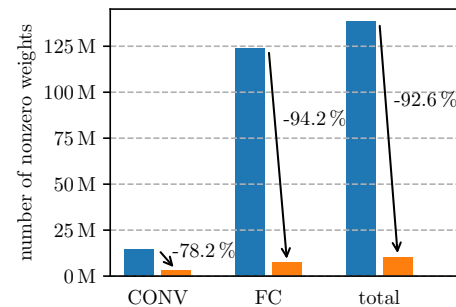
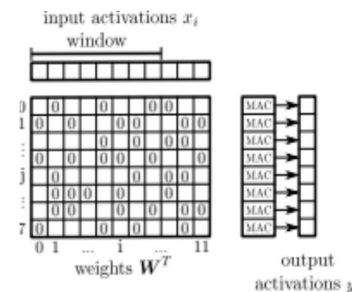
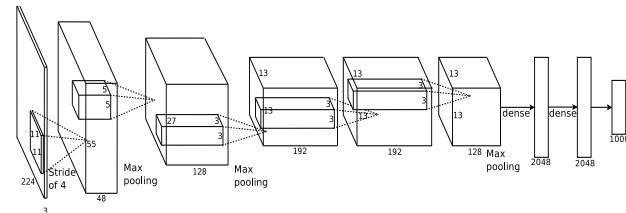
- Utilizing sparsity to improve efficiency of neural networks
- Design energy efficient hardware with support for sparsity

### Applications:

- Robotics, bio-medical, etc.



<https://www.flickr.com/photos/jurvetson/25215169297>



MS thesis (Electrical Engineering):  
Hardware Accelerator for Sparse  
Neural Network Inference