John Danison

ECET 32900 – Lab 8

03/28/2025

Goal:

The goal of this lab was to learn via hands-on methods, how to design a power system for a given embedded system application using Texas Instruments' WEBENCH product.

Output:

WEBENCH report including the schematic, the graphs, the BOM, and other information is attached at the end of this report.

Conclusion:

I learned that designing a custom power system is **significantly** easier to do than I ever knew about. This portion of embedded system hardware design is one that I struggle to get correct on every single personal project that I have ever done. I will absolutely be using this for my own future use in power design. I love the instant ability to have the schematic, BOM, and output graphs all for free.

Proof of Signature:

References

Texas Instruments. (n.d.). *WEBENCH® power designer*. Retrieved March 28, 2025, from https://webench.ti.com/power-designer/

Purdue University. (2025). *ECET 32900 Lab 8 instructional documents*. Purdue University.