

Digital Clock - JEHHEJ



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Summary:

A LED digital clock

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1 INTRODUCTION

2 WORKFLOW



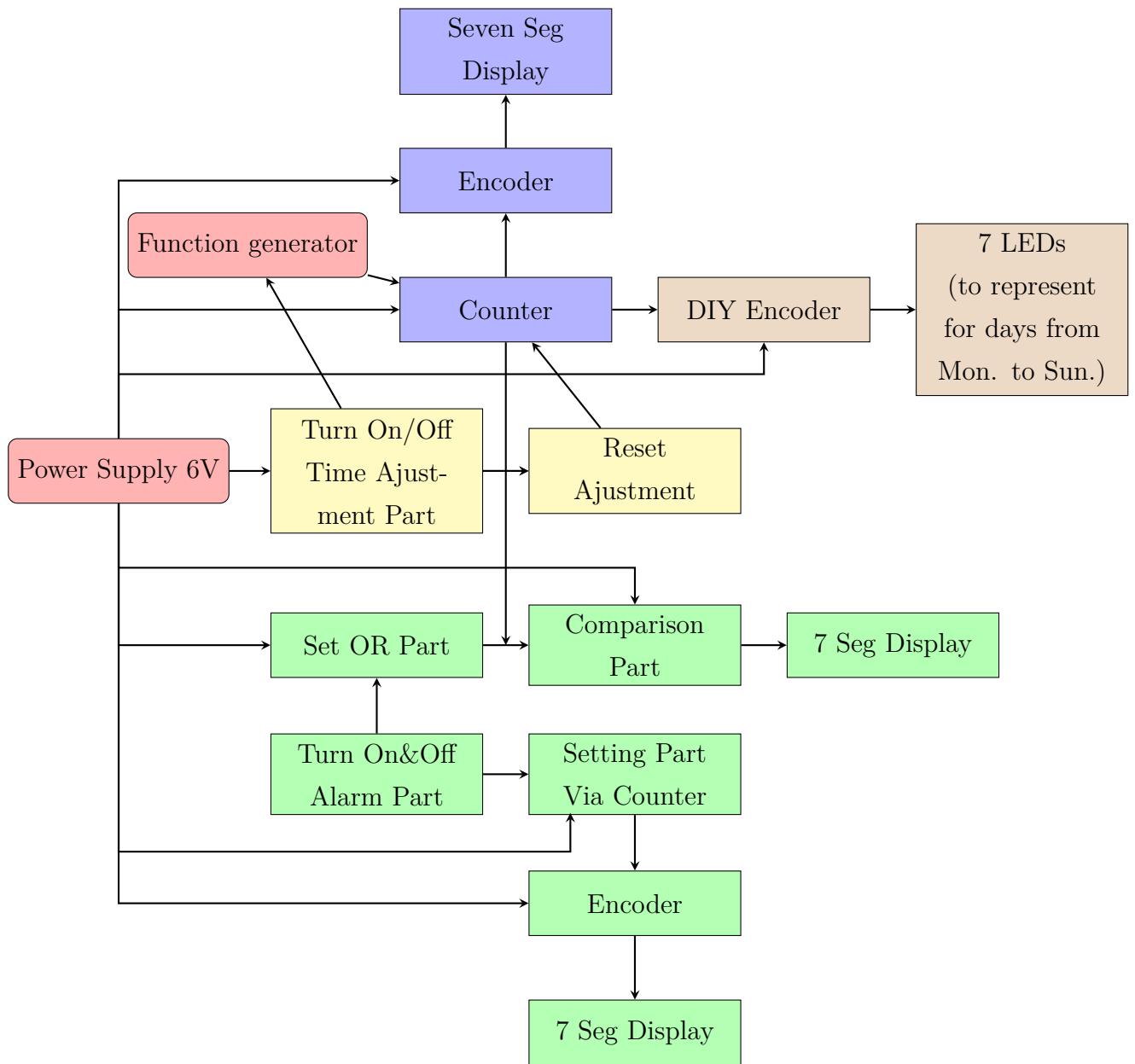
3 COMPONENTS

TABLE 1: COMPONENT LIST.

Name	Type	Quantity
Battery Box		1
Timer555		1
Capactior	$0.01\mu F$	1
	$0.1\mu F$	1
Resistor	$1k\Omega$	1
	$2k\Omega$	1
	100Ω	3
JK Flip Flop	74HC112	27
Seven Seg Display		12
Seven Seg Encoder	74LS48D	12
Switch		10
NOT Gate	74AS04M	20
NAND Gate	7400N	20
XOR Gate	7486N	10
OR Gate	7432N	5
Piezo Buzzer		1
CMOS	IRF510	4
LED	White	10

4 CIRCUIT PRINCIPLE

5 BLOCK SCHEMATIC



FRAME 1: COLOR BLOCK

Red Block: Input

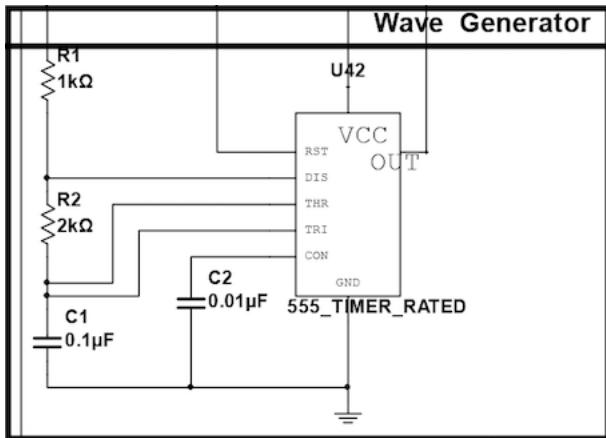
Purple Block: Basic Digital Clock

Yellow Block: Time Adjust Part

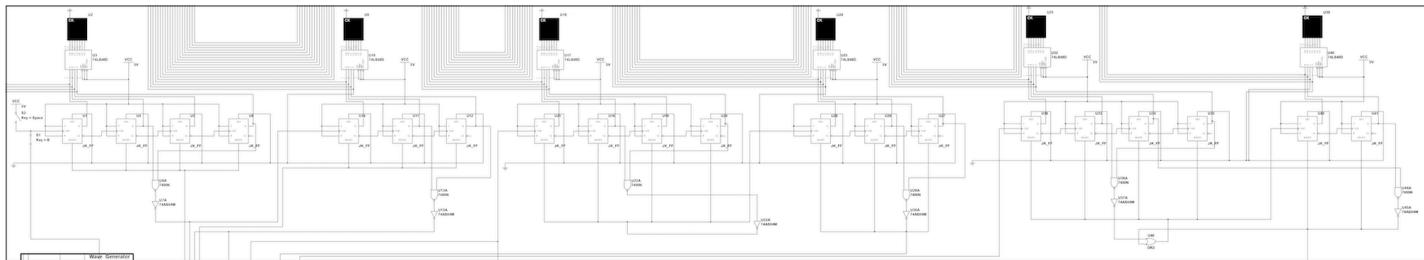
Brown Block: Show-Day Part

Green Block: Alarm Part

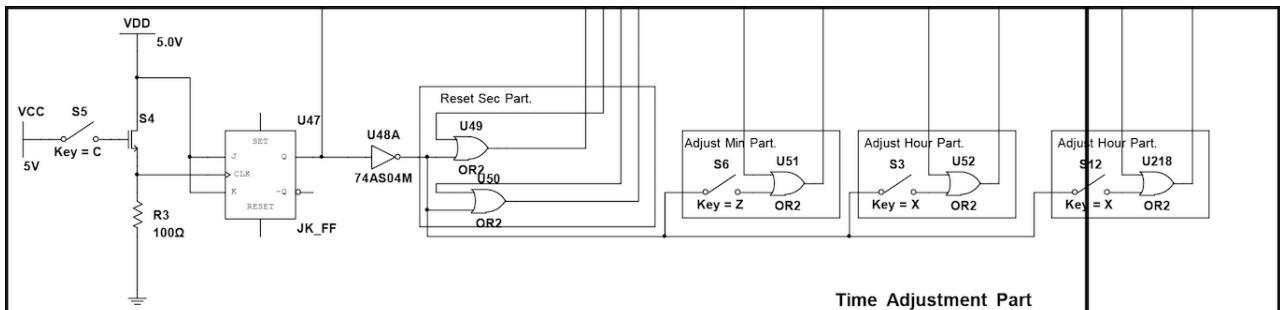
5.1 INPUT



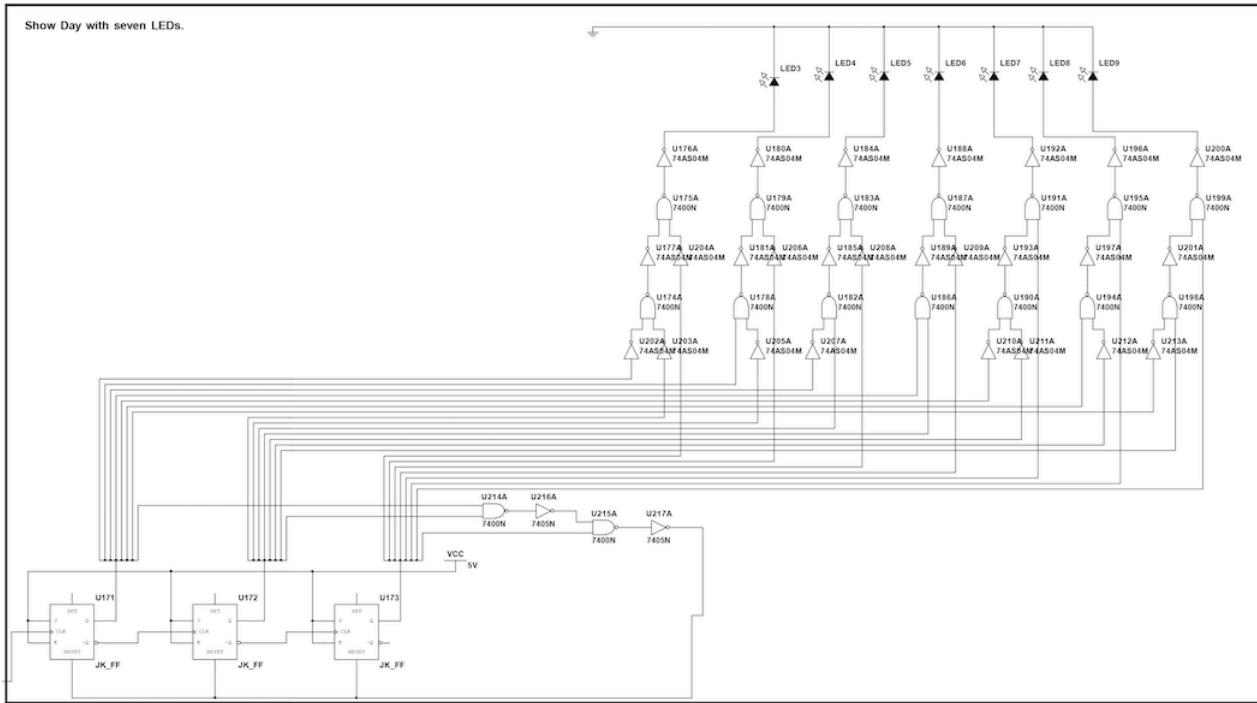
5.2 BASIC DIGITAL CLOCK



5.3 TIME ADJUST PART



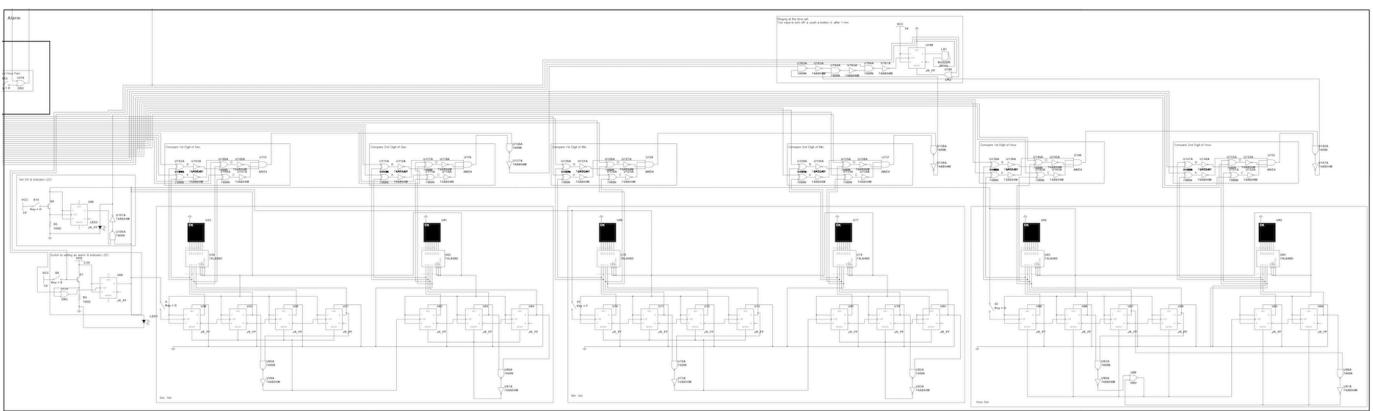
5.4 SHOW-DAY PART



FRAME 2: TIPS

Using 7 Led Lights to indicate From Mon. to Sun.

5.5 ALARM PART



6 SCHEMATIC DIAGRAM

