

# EE800/820 Weekly Status Report

**Student:** Jude Eschete

**Project Advisor:** Bernard Yett

**Week of:** February 2, 2026

**Project:** FPGA-Based Signal Processing System

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## Accomplishments This Week

- Ordered and received the FPGA development board for the project
- Created the project GitHub repository: [https://github.com/JEschete2651/EE800\\_820](https://github.com/JEschete2651/EE800_820)
- Developed a LaTeX template for the research proposal following course requirements
- Created weekly report template
- Set up project paper template from IEEE.org

## Goals for Next Week

- Collect and review literature sources on FPGA-based radar signal processing
- Complete the research proposal sections (Introduction, Problem Description, Expected Contributions, Project Timeline)
- Submit final proposal by February 10 deadline
  - Give Dr. Yett time to review prior to submission, no later than Thurs. 2026/02/05

## Potential Blockers

- Identifying sufficient relevant literature sources for the Introduction and Problem Description sections
- Identifying additional hardware and ordering in a timely manner