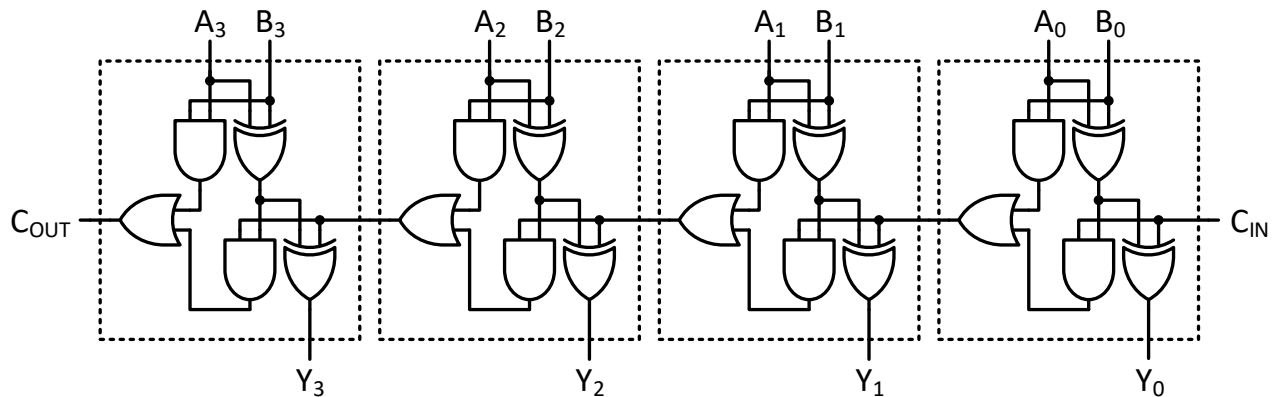Use contraction to simplify the binary adder circuit shown below. Assume it is being used to perform an **increment-by-6** operation on the A input (i.e. Y = A + 6). Assume that the carry-out is not needed. Redraw the simplified circuit in the space provided. Write your name(s) on the lines provided, then scan this page and attach it to the Canvas assignment as a **PDF** or **JPG** file.

You are <u>not</u> required to implement this in Quartus.

Full Name: _____

Full Name: _____