

Inhwan Jeong

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South Korea



OBJECTIVE

Highly motivated engineering undergraduate with a strong academic background in Nano-Optoelectronics (GPA 4.16/4.50). Seeking a position as a **Semiconductor Process Engineer** to utilize hands-on research experience in Micro-LED fabrication and yield optimization strategies.

EDUCATION

Hanyang University | Ansan, South Korea *Bachelor of Science in Nano-Optoelectronics* |
Expected Graduation: March 2027

- **GPA:** 4.16 / 4.50

RESEARCH EXPERIENCE

Advanced Nanoelectronics Research Laboratory | Hanyang University *Undergraduate Researcher* | Mar 2024 – Jul 2024

- **Micro-LED Fabrication & Analysis:** Conducted fabrication processes for Micro-LED devices and analyzed their electrical characteristics using semiconductor measurement equipment.
- **Yield & Performance Optimization:** Researched and identified key process variables to improve device yield and enhance optical/electrical performance.

AWARDS & HONORS

Excellence Award | Nov 2022 *Campus Patent Universiade (Host: Korea Invention Promotion Association)*

- **Topic:** Neuromorphic Technology Applications for Memory Products
- **Sponsor:** SK Hynix