FPGA Example Design: Xilinx Zynq Board

Project Team

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1 Project Goals

This project aims to create a simple FPGA example design using the JITX circuit board design tool. The primary objectives are:

- Design a basic Xilinx Zynq board

- Implement essential components including Zynq SoC, DDR4 memory, and Flash memory

- Integrate a simple ADC input fed from a Coax connector

- Establish a foundation for more complex FPGA designs

- Demonstrate the capabilities of JITX in FPGA board design

The design will focus on core functionality while maintaining simplicity, serving as a starting point for future expansions and modifications.