Project 2 Proposal

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A. Java implementation of the CHIP-8 instruction set.

B. Software project which implements the CHIP-8 instruction set on a simulated 8-bit processor.

C. We intend to implement an object oriented simulation of a CHIP-8 architecture. It should accurately simulate each opcode of the CHIP-8 ISA with optimal efficiency. Depending on the complexity, we intend to implement additional features including additional instructions or a visual simulation.

D. Interactive software which is capable of running a user provided input file containing CHIP-8 instructions. A report detailing analytics will be provided if deemed necessary.

E. As a group we intend to outline and define the simulated hardware and then divide up implementing the hardware components and opcodes in software.