# Ion Gauge Controller

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### 1 Introduction

This project is a Bayard-Alpert vacuum gauge controller designed to work within the EuroMeasure system.

#### 1.1 Bayard-Alpert gauge

Bayard-Alpert gauge is a type of a vacuum gauge that uses gas ionization through electron impact as a way to measure high vacuums in the range of  $1 \cdot 10^{-3}$  Torr -  $1 \cdot 10^{-10}$  Torr or  $1 \cdot 10^{-1}$  Pa -  $1 \cdot 10^{-8}$  Pa. Electrons are boiled-off from the cathode which is a tungsten filament heated with a current passed through it. Electrons are then accelerated using a grid at a strong positive potential. Accelerated electrons going through the vacuum have a chance to struck a residual gas molecule and ionise it. This, now positively charged, ion is attracted by the anode at a slight negative potential. Current that flows into the anode due to those ions is therefore proportional to the pressure inside the gauge.

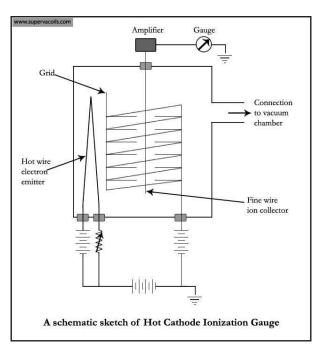


Figure 1: Hot ionisation vacuum gauge diagram

#### 1.2 EuroMeasure

EuroMeasure is a mechanical, electronic and protocol standard for measurement and control systems. It is developed at Wrocław University of Science and Technology faculty of Microsystems. This standard defines a module that can be connected with others on a single bus. It was designed to be compatible with standard Eurocard enclousures as well as to be cheap to manufacture. Each module consists of one or more PCB's, each taking up one module width. Each PCB can be connected to a backplane using two IDC10 connectors providing power and control signals and can also have an additional debug connector.

Additional connectors and other parts needed for operation can be mounted on the module front panel, which is attached to the PCB's.

## 2 Requirements

From the theory of operation of the vacuum gauge arise several requirements. The controller needs to heat the cathode, polarize all the electrodes in relation to each other and measure currents flowing in and out of the electrodes. This device is expected to operate with SJ2 vacuum gauge. Gauge parameters per its datasheet are as follows:

Cathode current: 1,5 A±0,1 A
Cathode emission current: 5 mA

Grid voltage: 200 V
Anode voltage: -25 V

• Sensitivity:  $16 \frac{\text{mA}}{\text{mA} \cdot \text{Torr}}$ 

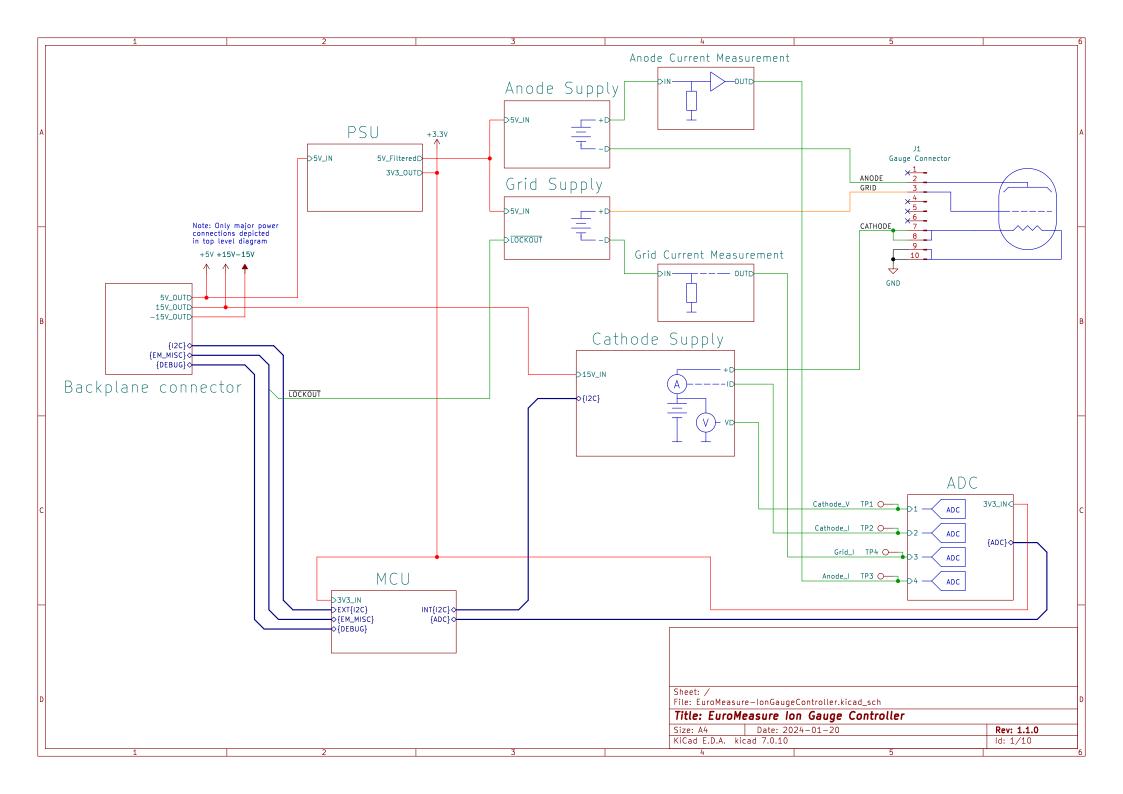
• Working range:  $1 \cdot 10^{-7}$  Torr -  $1 \cdot 10^{-3}$  Torr

It is expected that cathode current is adjusted to stabilize electron emission current at the required level. Using working range, emission current and sensitivity, anode current range can be calculated to be around:  $10\,\mathrm{nA}$  -  $100\,\mathrm{\mu A}$ . Cathode voltage at specified current was measured to be around 8 V. In summary the device needs to supply continously adjustable cathode current, polarize the grid and measure grid current, polarize the anode and measure anode current. It would be beneficial if parameters of the device were adjustable, as most Bayard-Alpert gauges have similar requirements and this would allow to support other models.

As the device is going to be a part of EuroMeasure system it needs to conform to its specification. It is expected that it will take up one module width, therefore it should consist of one  $10\times10\,\mathrm{cm}$  PCB with max component height of 16 mm at the top and 2 mm at the bottom. There are four M3 mounting holes at the corners of the PCB, 5 mm from the edge. There are three right-angle IDC10 connectors at the back edge of the board. One for power ( $\pm15\,\mathrm{V}$  and  $\pm5\,\mathrm{V}$ ), one for data (I2C communication and high voltage lockout) and one for debugging (SWD and UART). Connector for the vacuum gauge should be mounted to the front panel. The device is expected to operate in standard laboratory conditions. It should emit as little as possible EMI, and should not backfeed noise onto  $\pm15\,\mathrm{V}$  power supply rails and should disable hazardous voltages when lockout line is not energized.

#### 3 Schematic

Block diagram of the design is presented on the next page. It also serves as top level schematic sheet. Starting with the gauge it is connected to the controller using a single connector. Each of the electrodes is supplied by a separate switchmode power supply. Anode and grid power supplies are constant voltage, not controllable power supplies with galvanicly isolated output. This allows for easy current measurement on the low side of the supply. Cathode supply is programmable constant voltage switchmode supply. Using current measurement data from the grid, microcontroller can modify output voltage to stabilize emission current at required level. To allow for cathode health monitoring, its voltage as well as current are monitored. As cathode response is not very fast due to its thermal mass it was decided to use a fully digital control loop, as it will be easier to implement and add cathode safety features that disallow accidental cathode burnout. Every important parameter is digitized in the ADC block and processed by the microcontroller. Power and interface are connected through the backplane connector. To minimize backfed noise, voltages suppling power supplies are filtered.



#### 3.1 Anode supply

To generate anode voltage SN6507 push-pull transformer driver was selected. It was already tested in a previous project and offered required low-noise operation due to configurable slew rate. To pair with the driver 750316856 transformer was selected. It offered high enough turn ratio of 4,67 (It was decided to set this voltage pretty high to be able to modify output voltage in the future). Output voltage was calculated using equation 1:

$$V_{out} = 2 \cdot V_{in} \cdot n_{ratio} = 2 \cdot 5 \,\text{V} \cdot 4,67 = 46,7 \,\text{V}$$
 (1)

To not saturate the core, sufficiently high switching voltage is required. Manufacturer specifies transformer "voltage-time" to be 15 V µs. Minimum frequency is calculated using equation 2.

$$F_{min} = \frac{V_{in}}{VT} = \frac{5 \text{ V}}{15 \text{ V \mu s}} = 333 \text{ kHz}$$
 (2)

To have sufficient margin operating frequency was set to  $500\,\mathrm{kHz}$ . To do this, resistor R16 was set to  $22\,\mathrm{k}\Omega$  in accordance to the specification. Programmable current limit was set to  $500\,\mathrm{mA}$  with R22, and soft start time was set to  $3.8\,\mathrm{ms}$  according to equation 3.

$$T_{ss} = \frac{C_{17}}{275 \,\mu\text{A} - \frac{0.6}{R_{17}}} = 3.8 \,\text{ms} \tag{3}$$

Undervoltage lockout was set to be around 3,8 V (eq. 4).

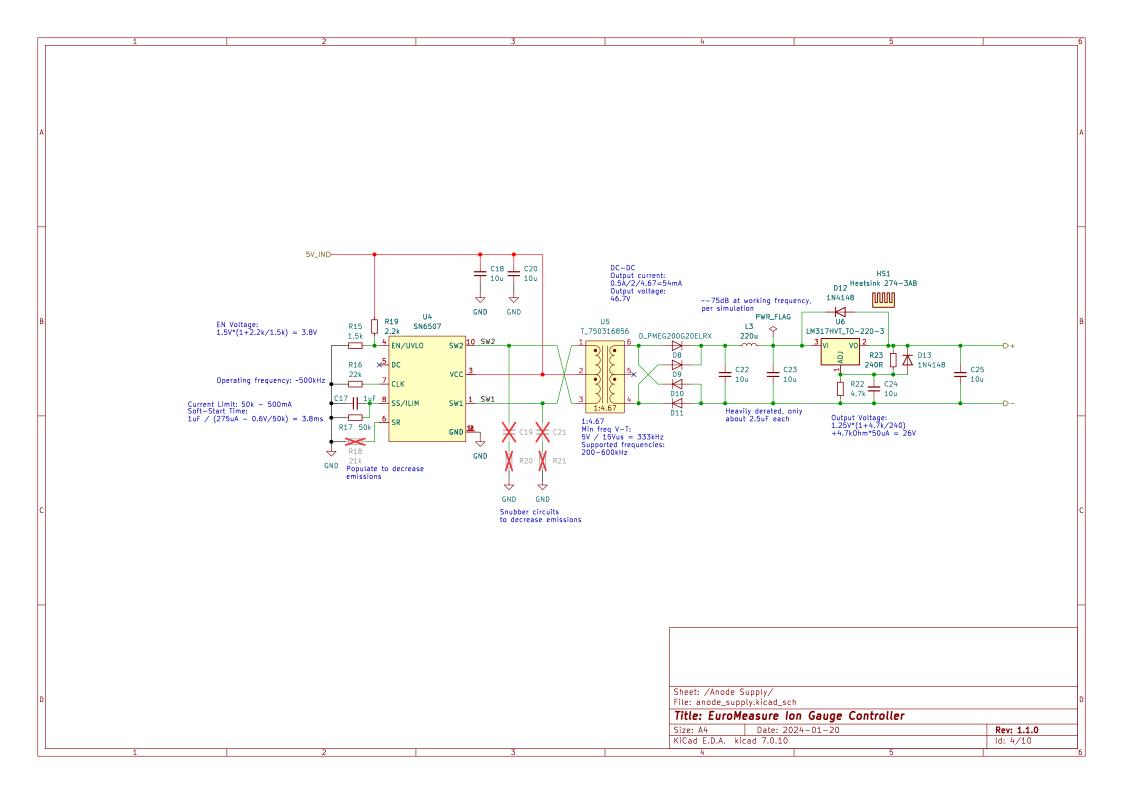
$$U_{UVLO} = 1.5 \,\text{V} \cdot \left(1 + \frac{R_{19}}{R_{15}}\right) = 3.8 \,\text{V}$$
 (4)

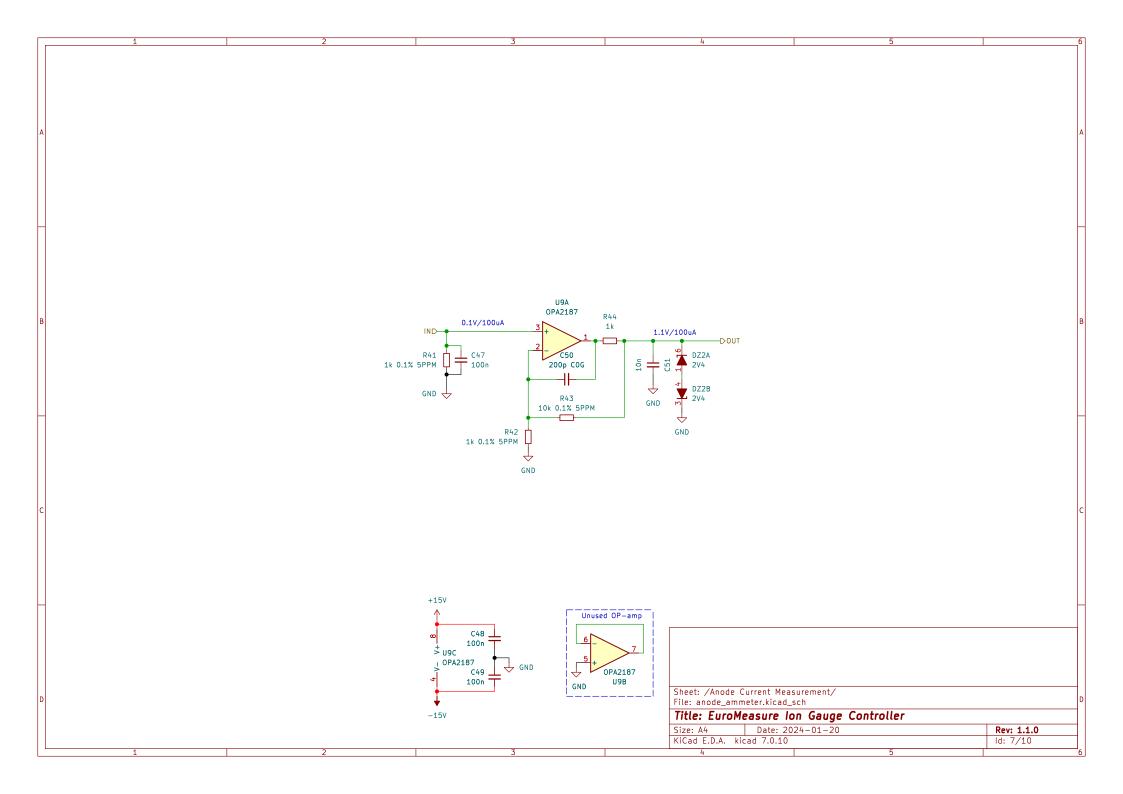
Voltage from the transformer is rectified with a full bridge rectifier build using "low-emission" PMEG200G20 diodes. Their low forward voltage and reverse charge limit switching noise. Voltage is then filtered using capacitors and an inductor. LTSpice simulations show around  $-75\,\mathrm{dB}$  damping at switching frequency. It is very important to take into account capacitor derating due to DC bias. To further reduce noise and stabilize voltage level, an adjustable voltage regulator is used. It's output voltage is set using R22 and R22 according to the equation 5. Diodes D12 and D13 are added for IC protection from capacitor discharge, following manufacurer recommendation.

$$V_{out} = 1.25 \,\text{V} \cdot \left(1 + \frac{R_{22}}{R_{21}}\right) + 50 \,\mu\text{A} \cdot R_{22} \tag{5}$$

#### 3.2 Anode current measurement

Anode current measurement is the most critical measurement in the whole device. Due to this extra care must be taken. Transimpedance amplifier configuration was considered, however stability concerns dictated the change to the current configuration. Anode current is measured using R41 1 k $\Omega$  resistor. This gives voltage of 0,1 V for full range of 100  $\mu$ A. This voltage shift should not influence the measurement significantly as it is only a 0,4 % change. Due to voltage in the low measurement range being only 10  $\mu$ V a low offset precision op-amp is needed for amplification of the signal. OPA2187 was selected as it specifies low offset voltage of 10  $\mu$ V with drift of only 0,001  $\mu$ V/K and bias current of 100 pA. It is used to provide a gain of x11 to more fully utilise ADC range. Diode DZ3 is added for ADC protection.



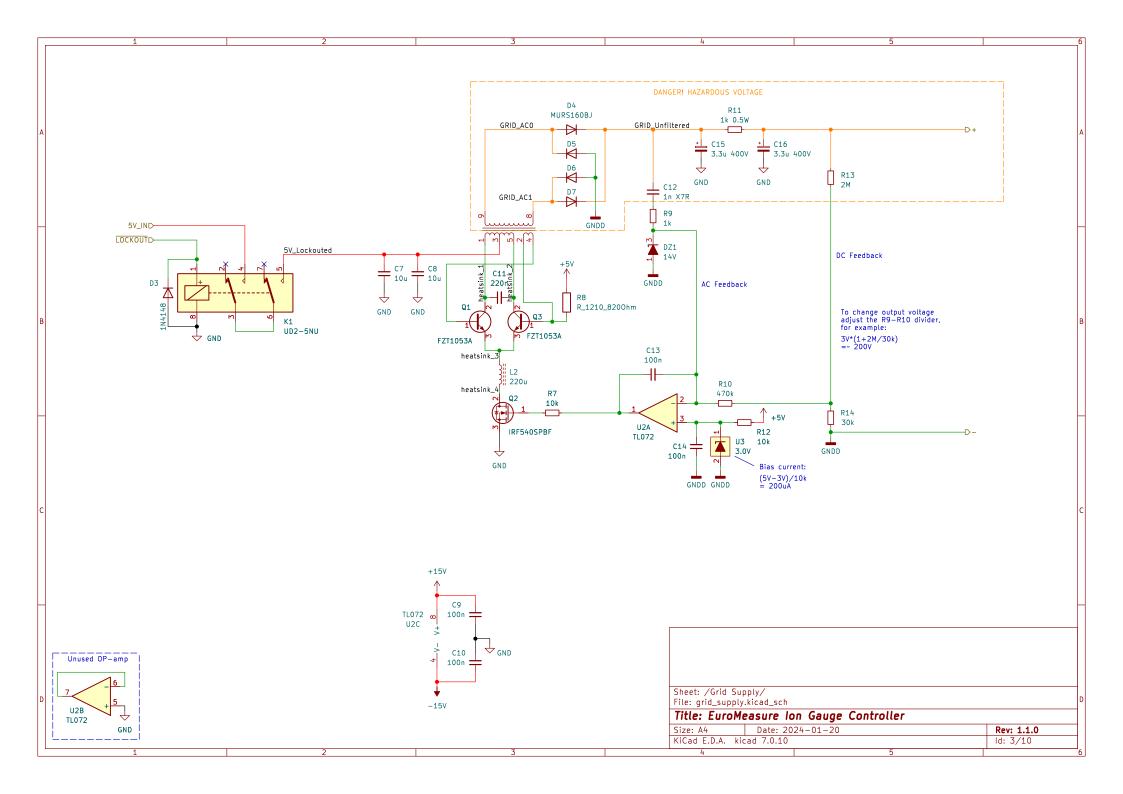


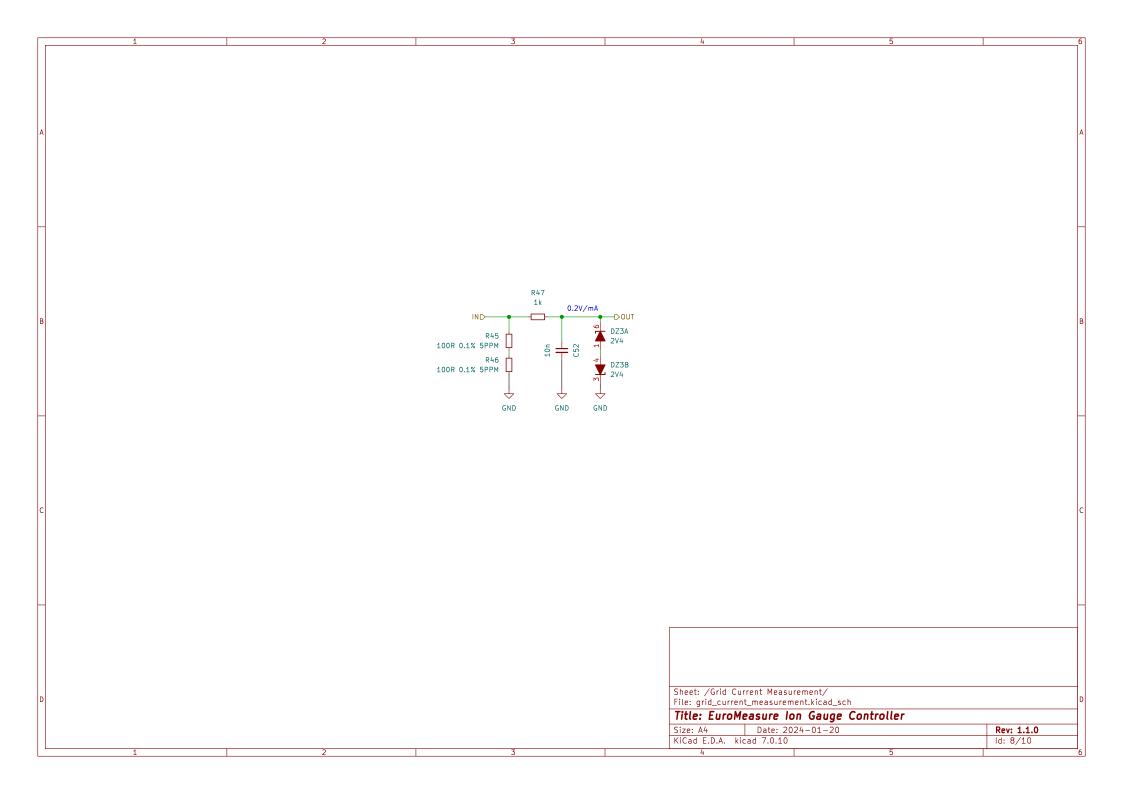
#### 3.3 Grid supply

Grid requires the highest of all needed voltages. To generate it, Royer converter topology was selected due to its very low noise. The whole supply is heavily inspired by AN-118 note by Jim Williams. Resonant Royer converter is a self-oscillating circuit that uses core saturation as means of limiting the oscillation. Due to it's sinusoidal waveforms, no high frequency switching noise is generated. Main LC tank is created with the transformer primary winding and capacitor C11. Transistors Q1 and Q3 are driven alternately in sync with the LC tank by a transformer feedback winding. Transformer output is rectified with a full bridge rectifier and further filtered. The circuit has two feedback paths, one AC and one DC. DC path is used to accurately control the output voltage, while AC improves transient response by bypassing the output filter. The resonant converter output amplitude is controlled by controlling current going to the converter with Q2. Output voltage is settable by changing the resistive divider in the DC feedback path (R13 and R14).

#### 3.4 Grid current measurement

Due to low precision needed, relatively high current and output voltage, measuring the grid current by means of a simple resistor is sufficient. Voltage from the resistor is filtered and limited by DZ3 diode to protect the ADC. With two  $100\,\Omega$  resistors, the circuit has a sensitivity of  $0.2\,\mathrm{V/mA}$  which gives  $1\,\mathrm{V}$  for nominal emission current, while ADC range is  $1.2\,\mathrm{V}$ .





#### 3.5 Cathode supply

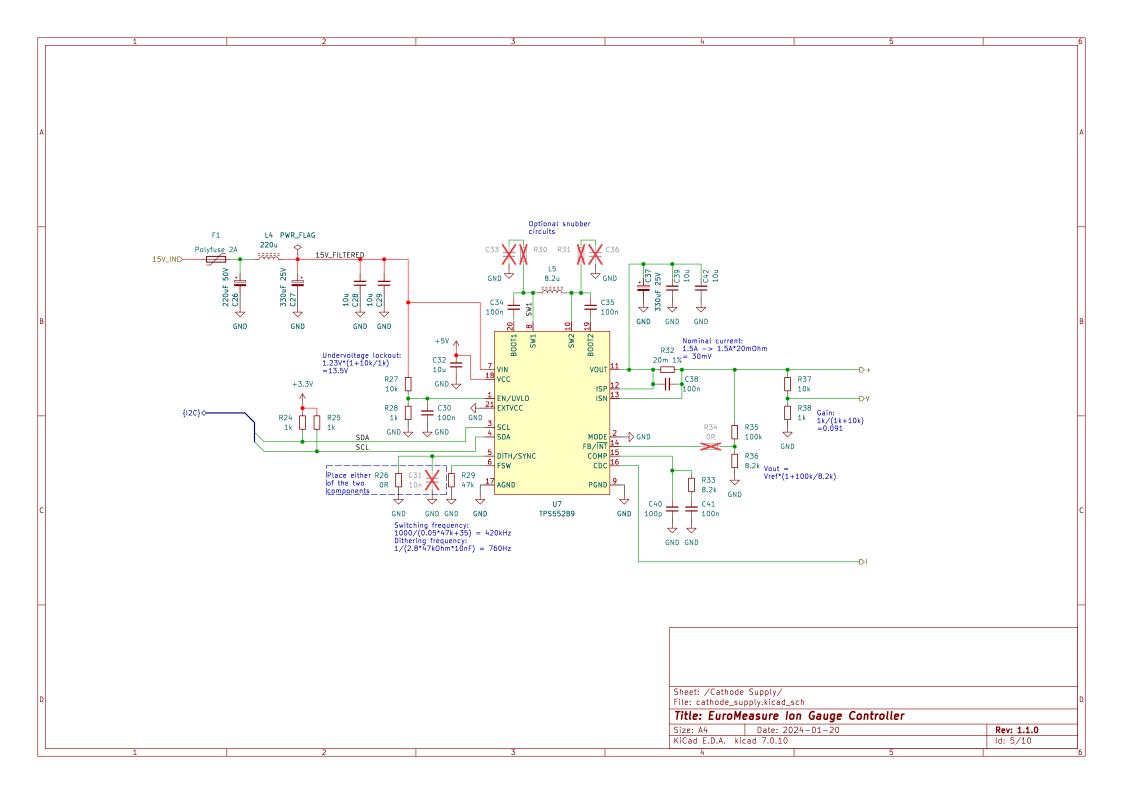
Cathode supply has to provide the most wattage in comparison to anode and grid supplies. Its parameters are also least crucial as it is only used to heat the cathode and it doesn't need galvanic isolation for precise current measurement. It however needs to be controllable to stabilize the emission current, but this control loop doesn't have to work at high frequency. It was decided to use a modern integrated DCDC converter IC. TPS55289 was selected. It allows to control the output voltage from I2C bus without complicated circuitry. It also has undervoltage protection, current monitoring, overcurrent protection and more. The IC is implemented according to the datasheet and using spreadsheet provided by the manufacturer. To provide the required power it was decided to use the 15 V rail. To not backfeed noise onto this rail, a filer is used. Capacitors C27 and C37 were selected to have very low ESR to limit ripple. Undervoltage lockout was set using R27 and R28 to 13,8 V according to the equation 6.

$$U_{UVLO} = 1.23 \,\text{V} \cdot \left(1 + \frac{R_{27}}{R_{28}}\right) = 13.8 \,\text{V}$$
 (6)

In case of too much switching noise, additional snubber circuits are planned however not populated right now. Frequency dithering is disabled, however to enable it R26 needs to be replaced with a capacitor. Example value in 10 nF. External feedback is also not connected by default as it most probably won't be necessary. To enable it R34 resistor needs to be populated and it needs to be enabled in IC configuration registers. Address of the IC is set to 0x75 by connecting MODE pin to ground. Switching frequency is set using R29 and its value is calculated using equation 7.

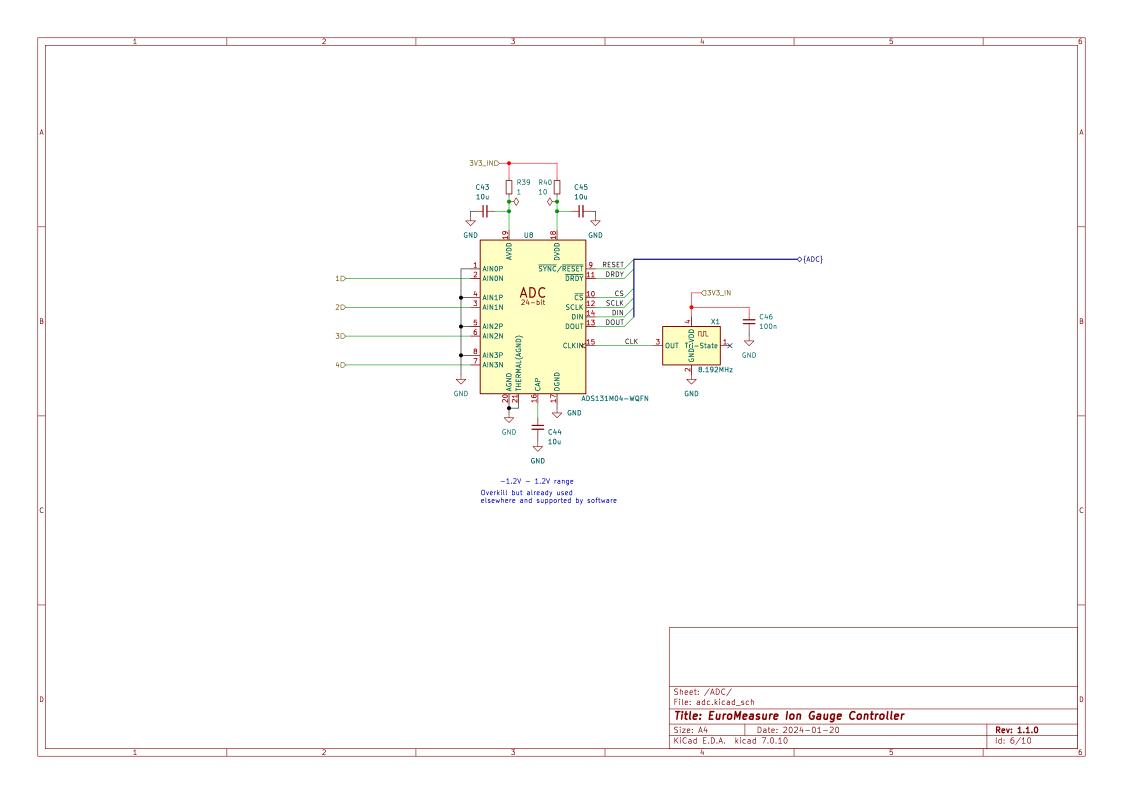
$$f = \frac{1000}{0.05 * R_{29} + 35} = 420 \,\text{kHz} \tag{7}$$

To reduce heating of the IC, 5 V is supplied to it externaly, instead of relying on internal voltage regulator. To do this EXTVCC pin had to be connected to logic low, and 5V needed to be connected to VCC. Current measurement functionality of the IC is also utilised. Current is measured using a shunt resistor R32. A small value of  $20\,\mathrm{m}\Omega$  was selected to limit the voltage drop to  $30\,\mathrm{m}V$ . Voltage drop on the resistor is amplified in the IC by a factor of 20 and output on pin CDC. Output voltage is also measured by the ADC to control whether the converter is operating correctly.



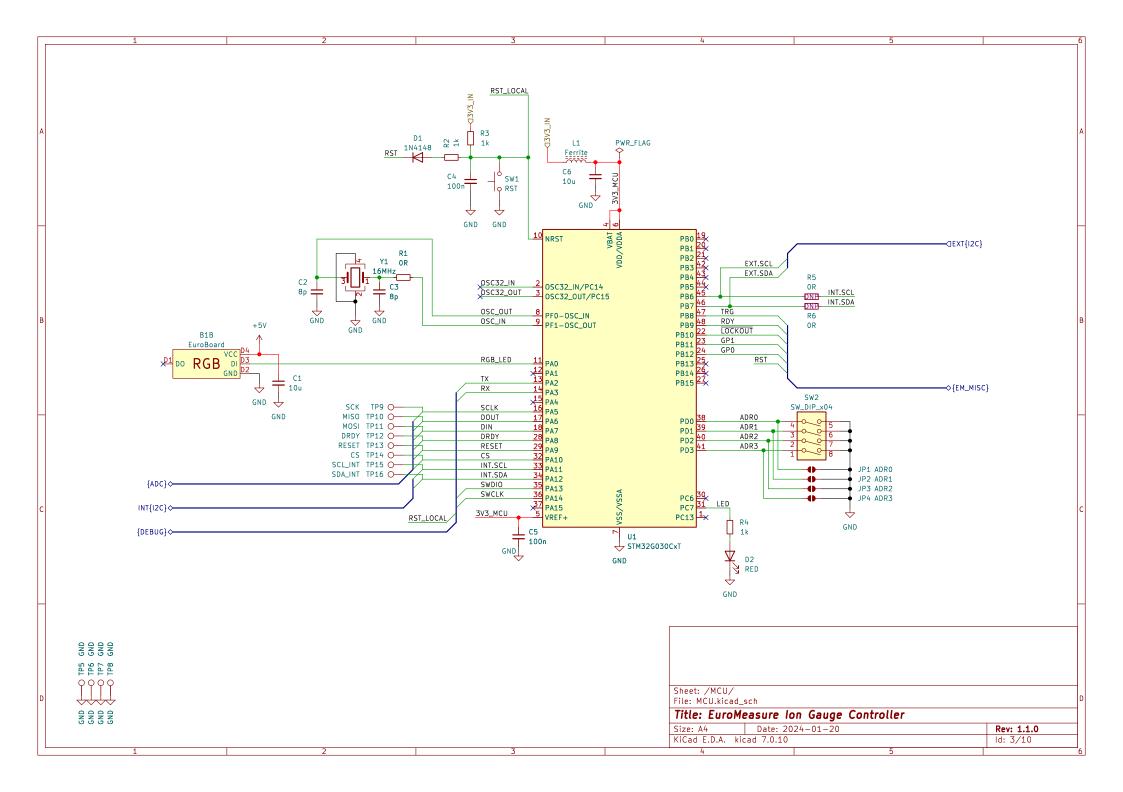
#### 3.6 ADC

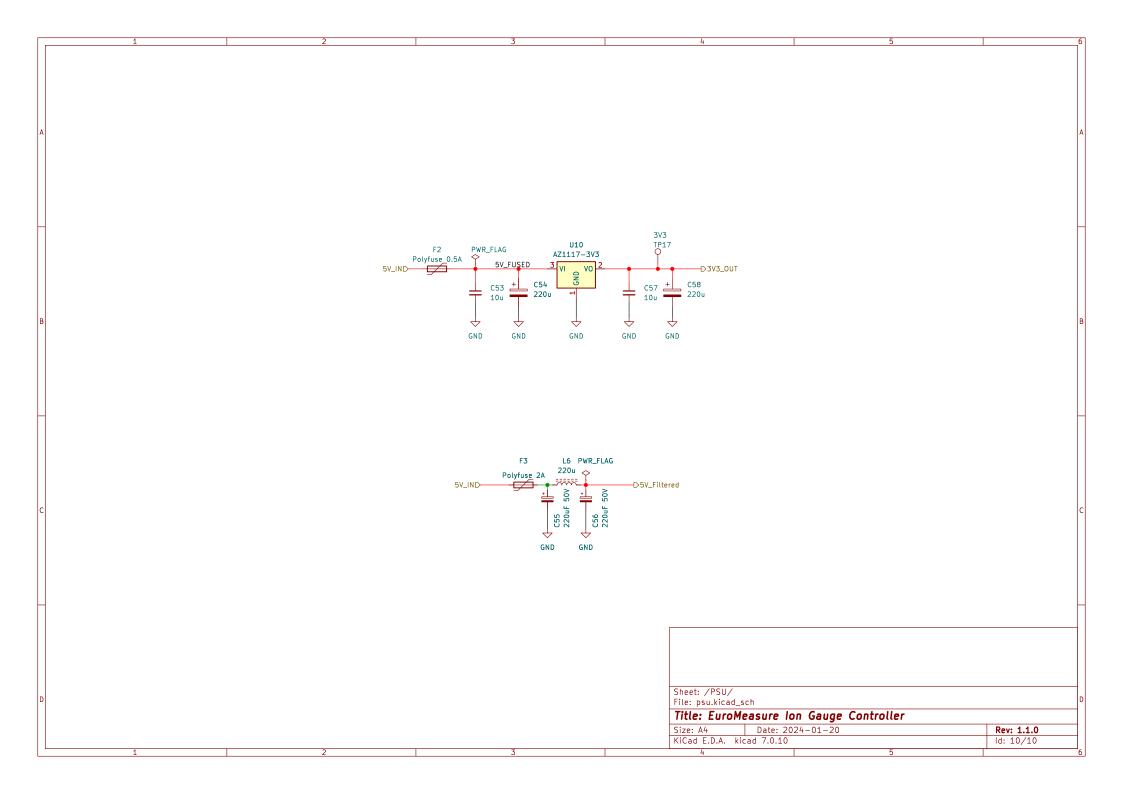
To measure all required voltages a dedicated analog to digital converter is used. There are 4 signals that need to be digitized: anode current, grid current, cathode current and cathode voltage. ADS131M04 was selected as it offers 4 simultaneous channels, precision required for anode current measurement without ranging and was already used in other module of EuroMeasure system. Its a 24 bit sigma delta ADC with integrated digital averaging and filtering, high precision voltage reference and programmable gain amplifier. It communicates with the microcontroller using 3,3 V SPI interface and two additional signals: DRDY(data ready) and RESET. It required an external clock source. It is implemented using a 8,192 MHz oscillator, as specified in the datasheet. It is powered from 3,3 V rail with additional filtering. All signals connected to the IC are already in the correct  $\pm 1,2$  V range.

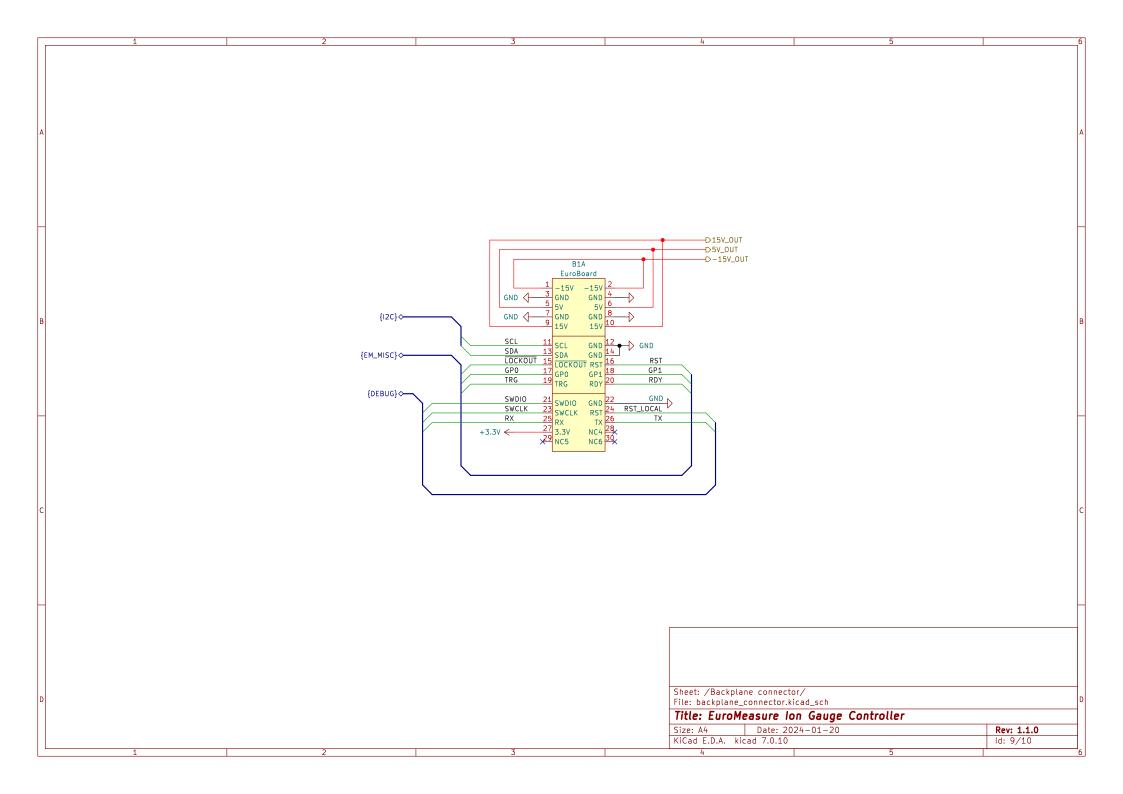


#### 3.7 Microcontroller

An STM32G0 is used as a microcontroller on this board. It is used in almost all other EuroMeasure modules. Its implementation is also standard between them. It's clocked with a 16 MHz oscillator. It is powered using 3,3 V rail generated using a voltage regulator. It's connected to all digital interfaces on the board: DCDC's I2C, ADC's SPI and EuroMeasure signals connected from backplane. It is also connected to a debug connector with UART and SWD for programming and debuging. For status indication one red LED and one programmable RGB led are used. All of the digital interfaces use 3,3 V logic so no voltage translation is needed. To select adress in the EuroMeasure system a DIP switch is used or, alternatively solderable jumpers.







### 4 Layout

The controller is layed out on a single  $100 \times 100 \text{mm}$  two layer PCB. Almost all components needed to be placed on the top side of the PCB due to height limitations. Mounting hole as well as backplane connector positions are specified by the EuroMeasure standard. Microcontroller as well as PSU sections are used on other boards therefore their layout is a copy of already existing designs and is placed on the right side of the board, near the backplane connection. The most noise sensitive measurement is that of anode current so this circuit is placed further from potential noise emitting sections such as cathode power supply. To also limit the emissions, all high current loops in the DCDC converters are routed as small as possible and a ground plane is used on the bottom layer with via stitching used where it is not continous. Via stiching was also used around the anode power supply to make sure that none of the currents from other converters go near it. Some components on the board, such as power transistors, voltage regulators and cathode DCDC converter IC produce a lot of heat, therefore additional copper pours are placed on their heat dissapating pins. To increase separation of copper from the high voltage nets in grid power supply a keep-out zone was created where no copper pour will be generated.

#### 5 Software

Main factor driving the software design was the need to conform to the EuroMeasure standard. Within this standard each module has an address and a set of commands that it can execute. Each command can have multiple arguments and it also can return multiple values. Each module also must implement a basic set of commands used for automatic functionality discovery. When interrogated is should describe all commands it implements. Commands are sent through an I2C interface in a binary format. Most of the EuroMeasure interface is implemented in a dedicated library, so functionality such as standard commands, I2C interface handling and command registration doesn't need to be reimplemented. Additional commands that need to be implemented are:

- Switching cathode heating ON/OFF
- Reading the pressure
- Reading diagnostic values, such as emission current and heating power

Apart from listening to the commands, this module has to continuously execute a control loop for stabilizing the emission current and monitor the safety of the cathode which should be disabled in case of too high pressure. Main flow of the program is represented in figure 2, however due to heavy use of interrupts, it doesn't adequately describe the full operation. To do that aditional diagrams are needed presented in figure 3 and 4 which describe I2C interupt handler and ADC interupt handler accordingly.

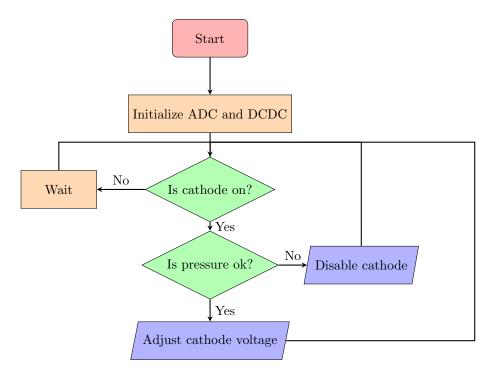


Figure 2: Main flow diagram

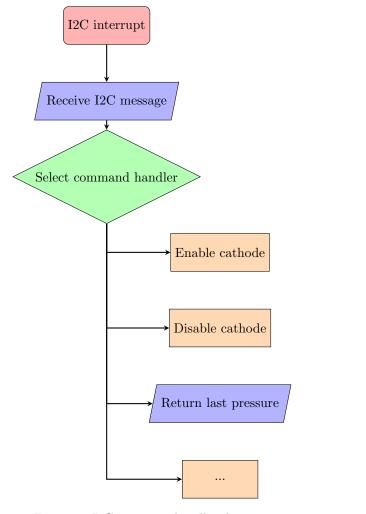


Figure 3: I2C interrupt handler diagram

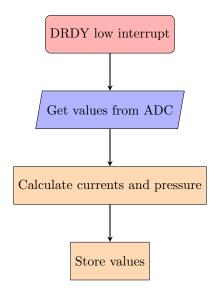


Figure 4: ADC interrupt handler diagram

### 5.1 Code snippets

#### 5.2 ADC data acquisition

Listing 1 shows code responsible for acquiring samples from the ADC. It is called within interrupt handler subroutine for DRDY pin change. To download the data from ADC an SPI transaction is performed. ADC is configured to communicate in 24-bit words, therefore each word takes up 3 bytes. ADC talks with standardized transactions, every frame send by the ADC consists of response from last command (if no command was issued it returns STATUS register contents), data from each of 4 channels and a CRC code. Endianess of ADC data doesn't match MCU endianess therefore bytes need to be swaped. Special consideration also needs to be taken as data is supplied in two's complement format and in 24-bit format, whereas native word size for the MCU is 32-bit.  $SPI_t ransaction function is written to handle bidirectional data transfer.$