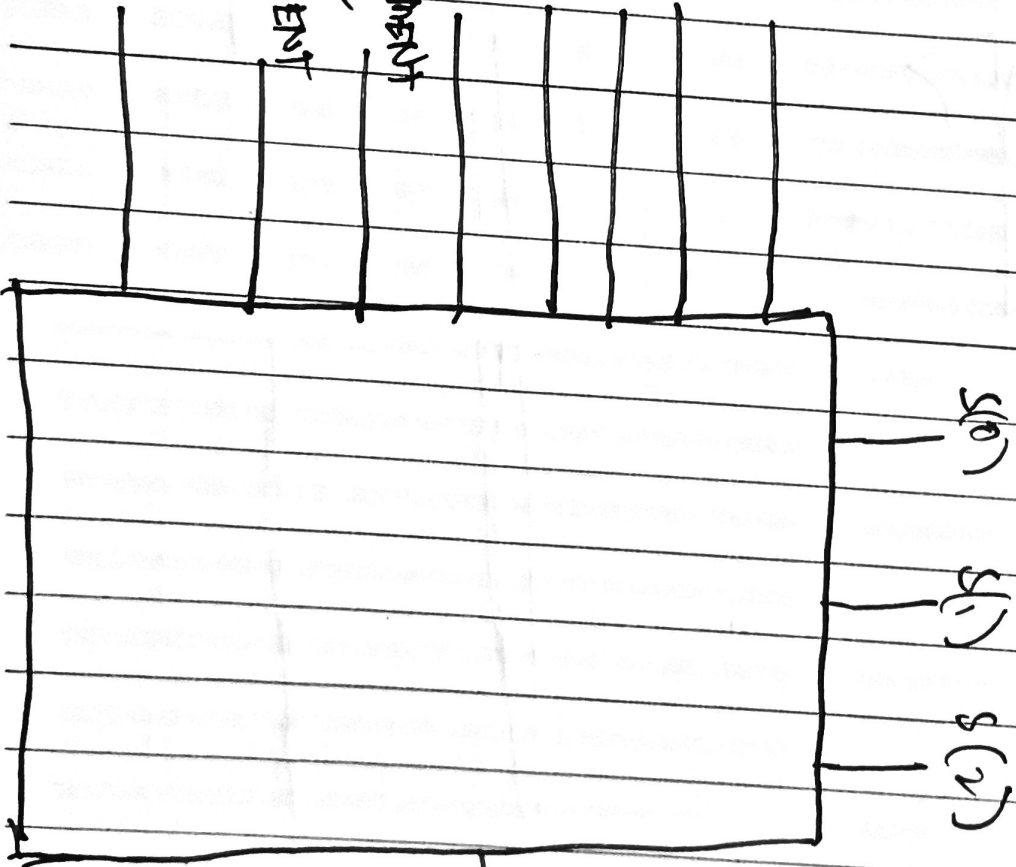


MUX

8 to 1

AND SIGNAL
OR SIGNAL
XOR SIGNAL
NOT SIGNAL
ADD SIGNAL
FC - COMPLEMENT SIGNAL
FC - COMPLEMENT SIGNAL
INCREMENT SIGNAL



k (0 to 1)