C/Sondalezas 14 6ºA, 29010, Málaga
630 12 81 34
jlopezjodar@gmail.com

PROFESSIONAL PROFILE

Electronic engineer with experience developing firmware for mission-critical embedded systems, including power subsystems for CubeSat satellites. I bring detailed low-level debugging capabilities and effective collaboration across hardware and software teams. Seeking to continue developing my expertise in high-impact technology projects.

SKILLS

- Analytical approach
- Team work
- Lab equipment operation
- Flexible schedule
- Critical thinking
- Problem-solving

TECHNICAL SKILLS

- C/C++
- Python programming
- Micro-controllers
- FPGAs
- Usage of linux operating systems
- VHDL Programming
- Altium
- Knowledge in power electronics.
- Circuit analysis

Juan López Jódar

EXPERIENCIA

04 2024 - Actual
Firmware engineer DHV | Malaga

- Developing and testing of firmware for electrical power subsytems in small satellites.
- Use of C for micro-controllers.
- Use of communications protocols via CAN (CAN-CSP and CAN-TS)
- Use of digital communications protocol (I2C, SPI,UART)
- Meetings with customers, to clarify technical aspects of the products firmware

Septiembre 2022 - Julio 2023
Research Intern UPM | Madrid

- I developed an optimization algorithm for EV charging that balanced electricity cost minimization with battery longevity, accounting for dynamic pricing and battery degradation constraints.

Julio 2022 - Septiembre 2022
Intern Premo | Málaga

- Developed robotic arm control software for live demonstration at the Messe München electronics exhibition.

FORMACIÓN

2024
Master in industrial electronics | Electronics
Universidad Politécnica de Madrid, Madrid

- I completed a Master's degree in Industrial Electronics, specializing in Power Electronics, with relevant coursework in embedded systems and microcontroller programming

2023
Industrial electronics degree | Electronics
Universidad de Málaga, Málaga

LANGUAGES

- Spanish: Native speaker.
- English: Advanced speaker (C1 Cambridge)

CERTIFICATIONS

- Mastering RTOS: Hands on FreeRTOS and STM32Fx with Debugging (FastBit Embedded Brain Academy)
- Introduction to FPGA Design for Embedded Systems (University of Colorado Boulder)
- Python Data structure (Michigan University)
- Using Python to Access Web Data (Michigan university)