딥뉴럴넷빌더 – 얼바나 샴페인

ABS

* DNN fpga accelerator design에 필요한거
* RTL programming
* Hardware verification
* Precise resource allocation
* Target Domain, board
* Cloud device – KU115
* Edge device - XC7Z045
* Target Net
* Alex
* ZF
* VGG16
* YOLO
* Sol : Fine grained layer based pipeline architecture

INTRO

* Why FPGA?
* Flexibility ( compared with ASIC )
* Improved latency & energy consumption ( compared with GPU )