



adc_control VHDL file names

`o_DATA_A = ADC_A_DATA_ADC_CONTROL_TO_ADC_SAMPLE_COUNTER`

`o_DATA_B = ADC_B_DATA_ADC_CONTROL_TO_ADC_SAMPLE_COUNTER`

`o_DATA_RDY = ADC_A_AND_B_DATA_READY_ADC_CONTROL_TO_ADC_SAMPLE_COUNTER`