

Iterative Compilation

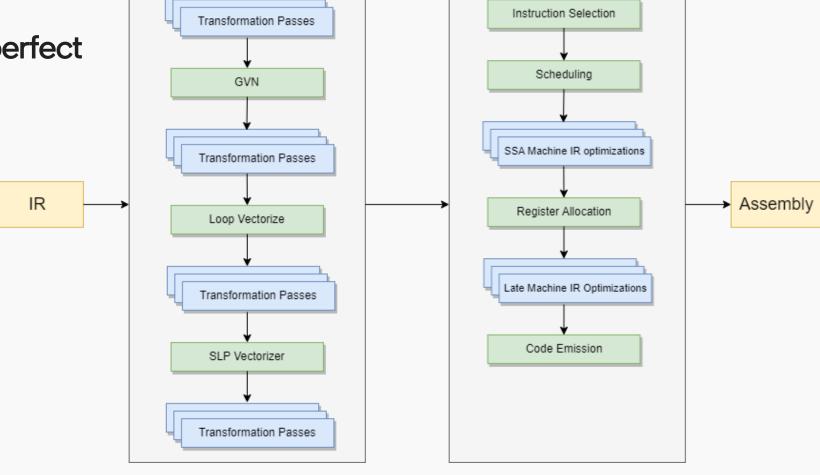
Give The Compiler A Second Chance

Ziv Ben Zion

May 10, 2023

Motivation

Compiler decisions aren't perfect



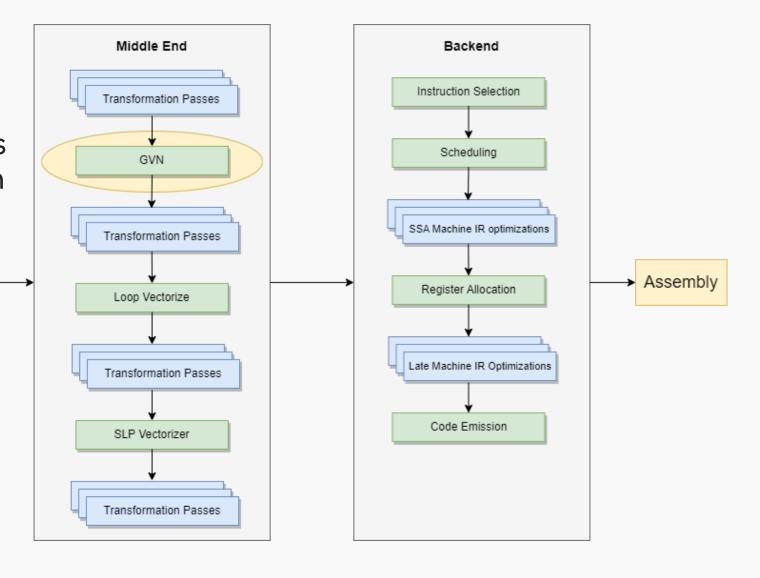


Middle End

Backend

Motivation - Example

Data Reuse – Loop Instructions Partial Redundancy Elimination



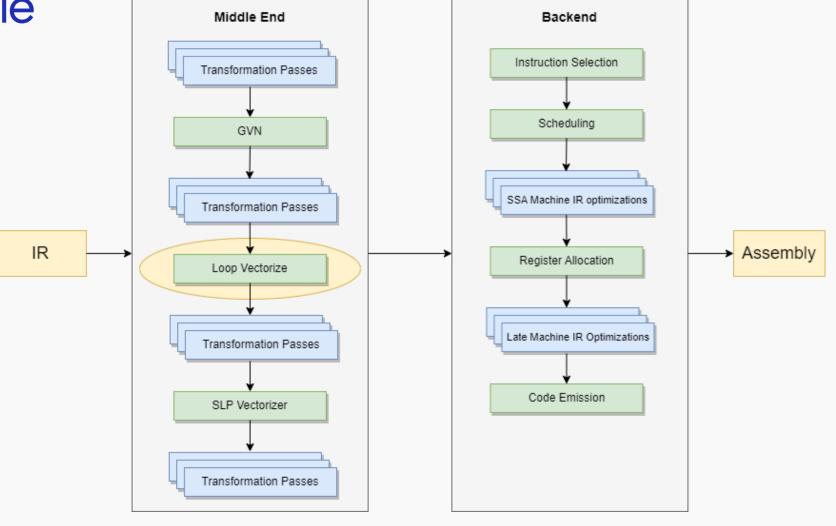


IR

Motivation - Example

Two main decisions:

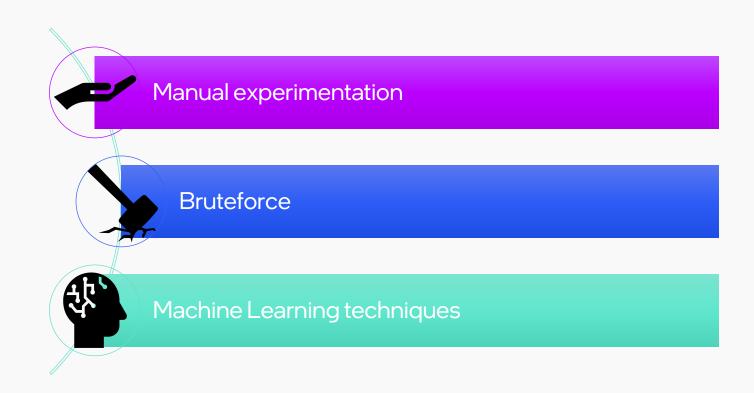
- VF Vectorization Factor
- UF Unroll Factor





Iterative Compilation

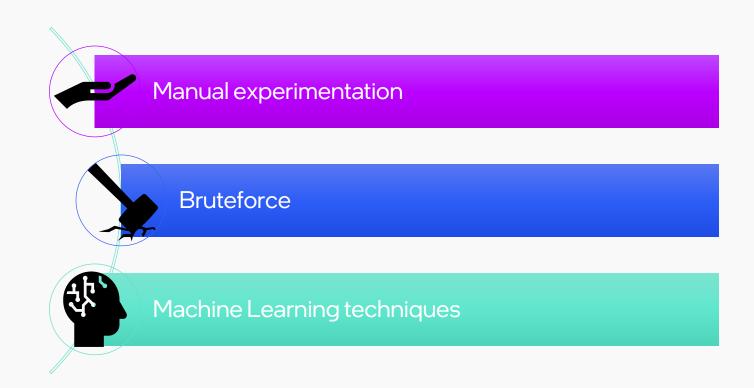
Navigating the search space





Iterative Compilation

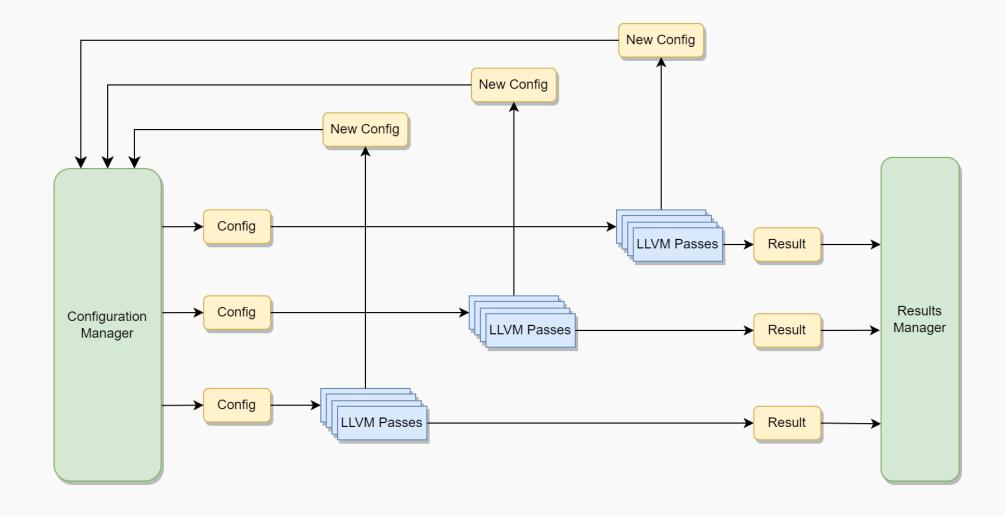
Navigating the search space



Why not let the compiler decide?



Implementation



Implementation



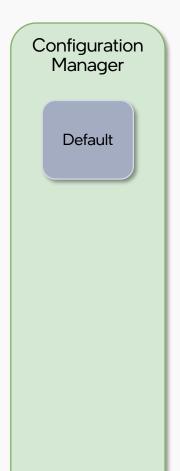
LLVM API

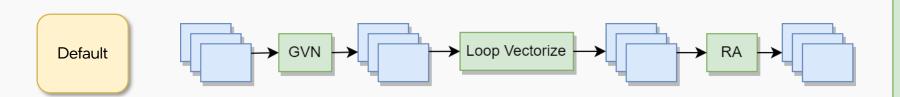
```
void IterativeComp::registerDecision(const std::string &ID, IterativeConfig &Config);
Optional<int> IterativeComp::getDecision(const std::string &ID, DecisionType Ty);
void IterativeComp::setResult(int Result);
```

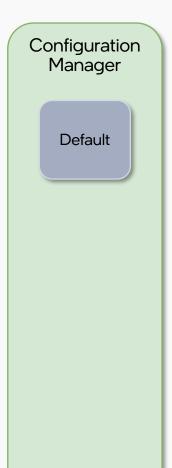


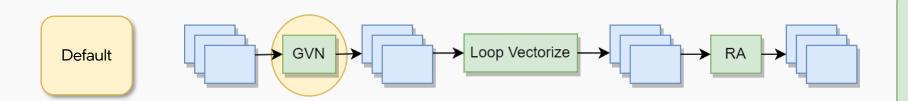
Implementation

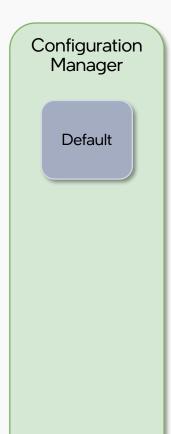


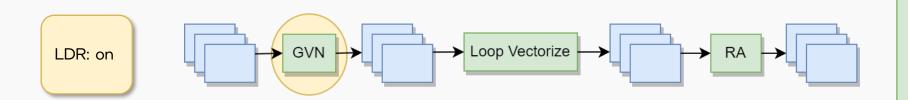




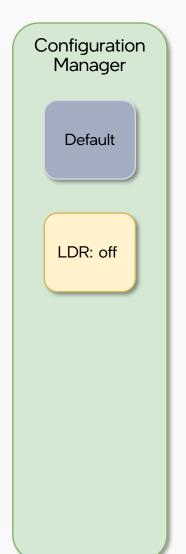


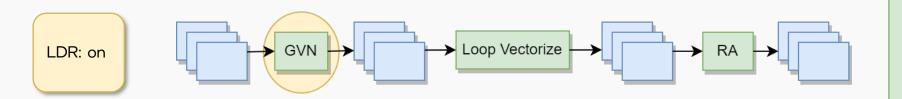




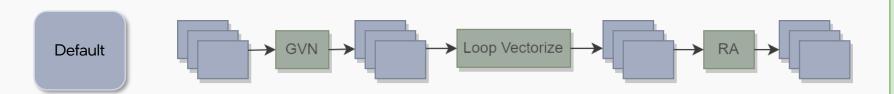




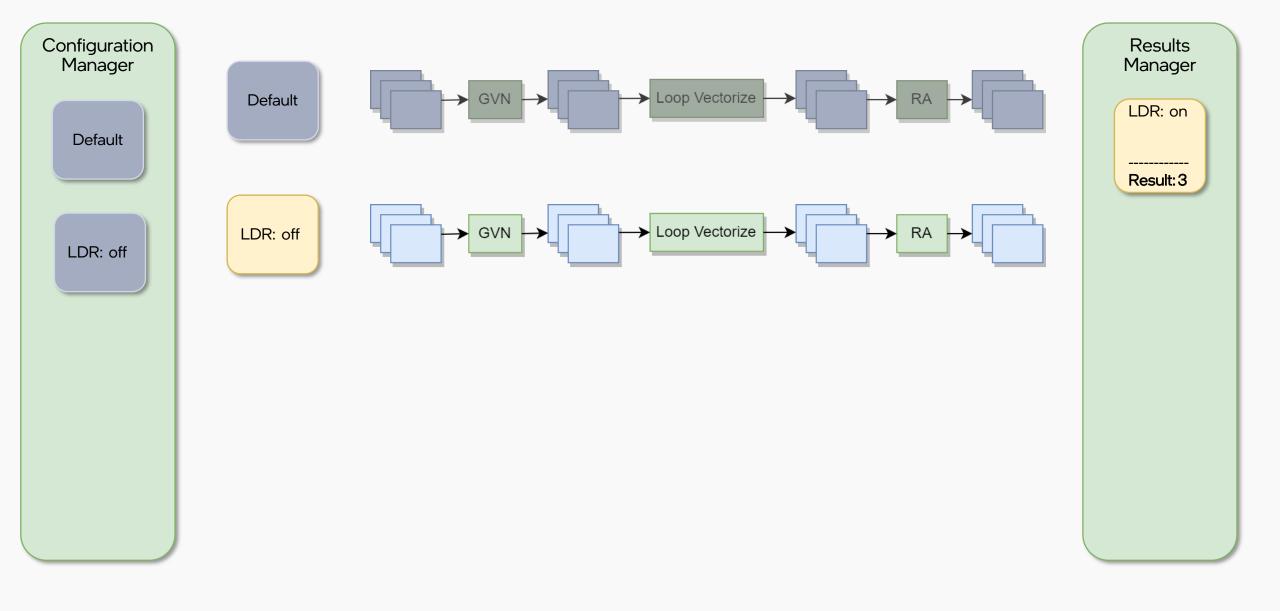


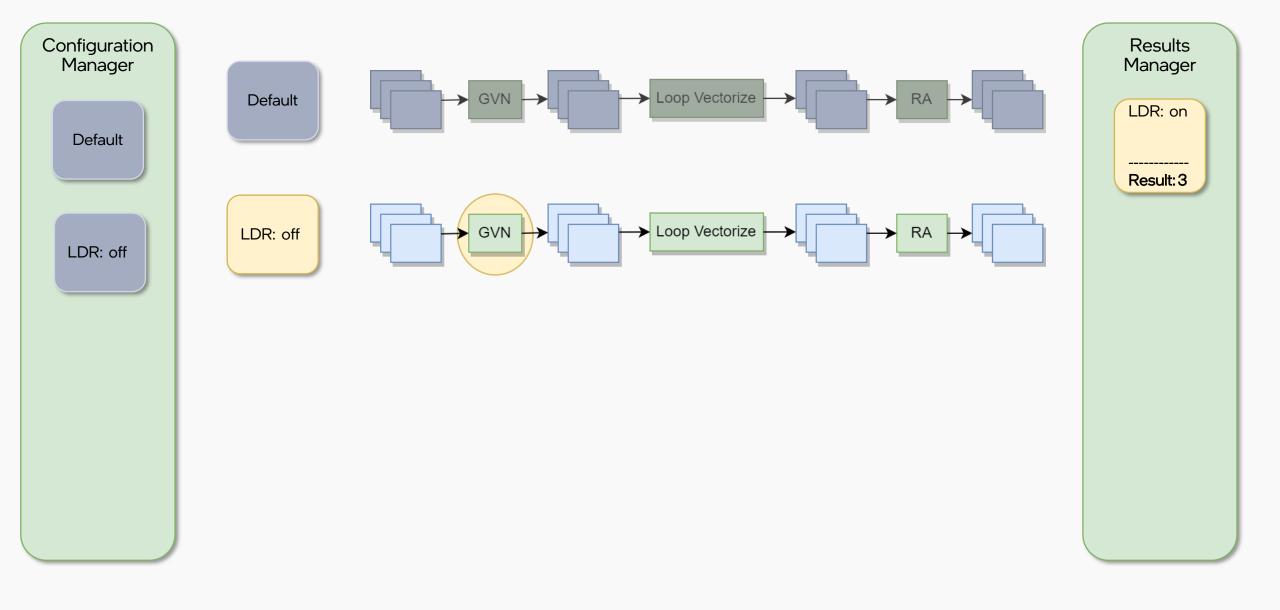


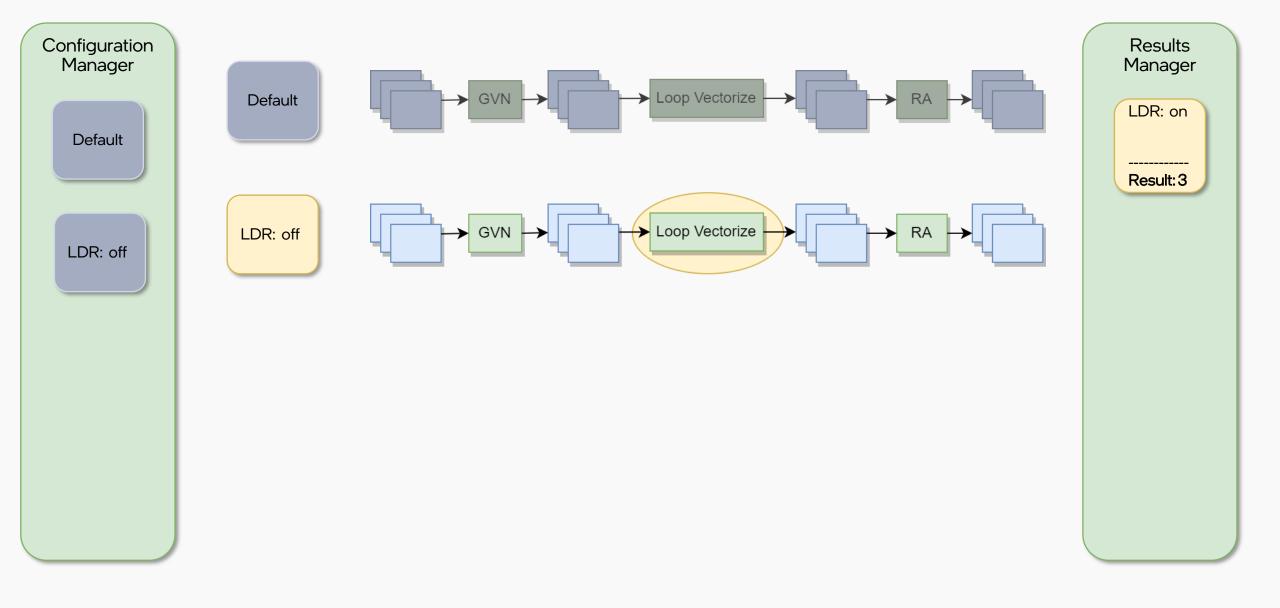




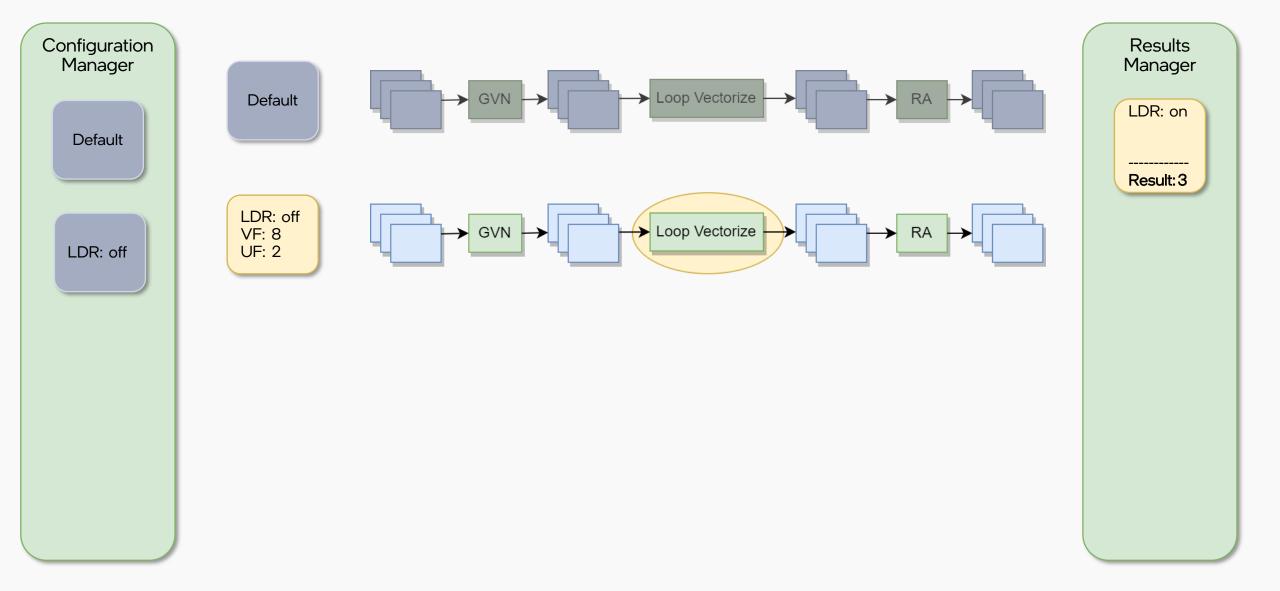
Results Manager LDR: on Result:3

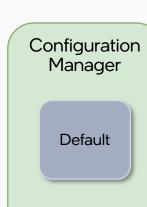






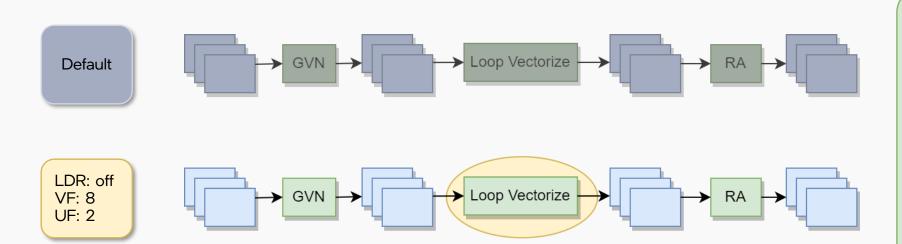






LDR: off

LDR: off VF: 16 UF: 1

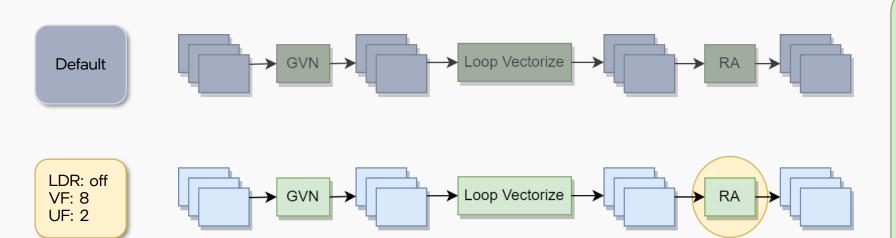


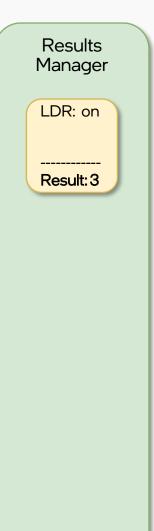
Results Manager LDR: on Result:3



LDR: off

LDR: off VF: 16 UF: 1







Default

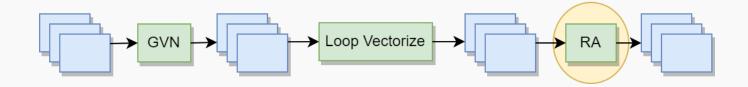
LDR: off

LDR: off VF: 16 UF: 1

LDR: off VF: 8 UF: 1 Default



LDR: off VF: 8 UF: 2



Results Manager

LDR: on



Default

LDR: off

LDR: off VF: 16 UF: 1

LDR: off VF: 8 UF: 1 Default

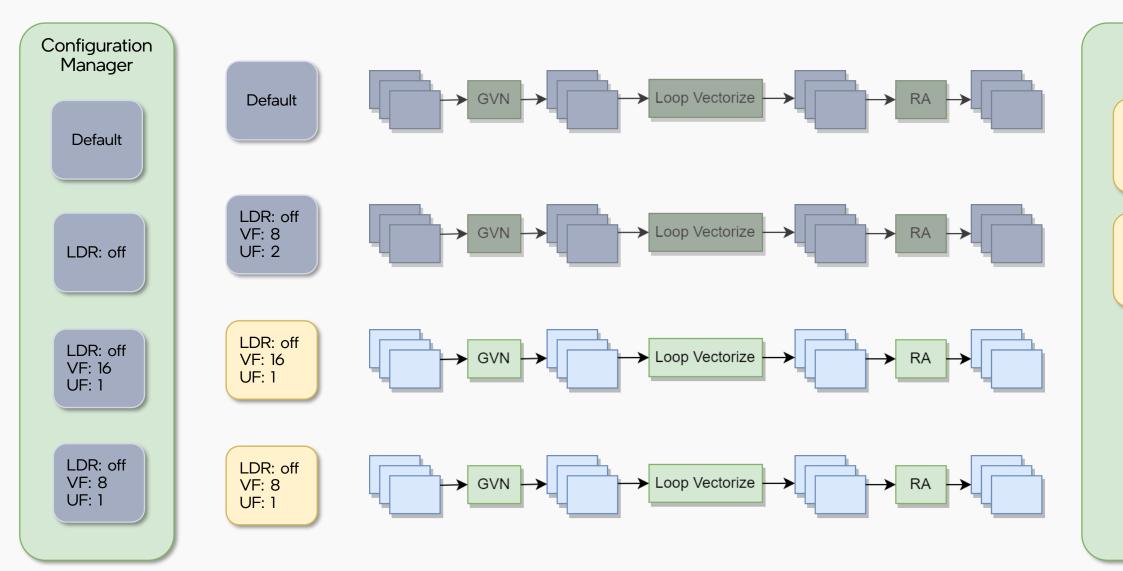
LDR: off
VF: 8
UF: 2

Results Manager

LDR: on

Result: 3

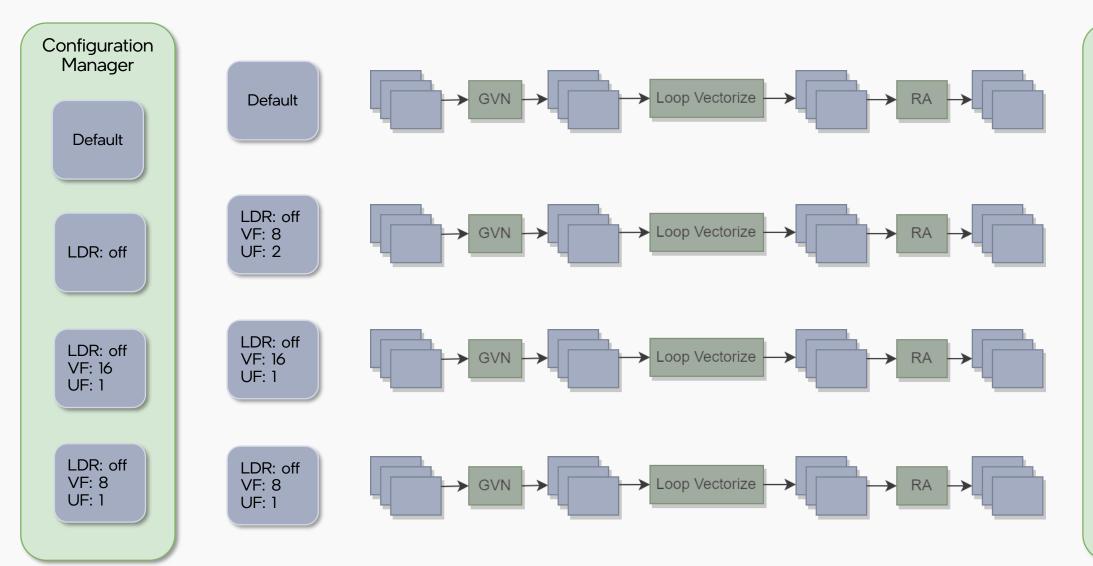
LDR: off VF: 8 UF: 2



LDR: on

Result: 3

LDR: off VF: 8 UF: 2



Results Manager LDR: on Result:3 LDR: off VF: 8 UF: 2 Result: 5 LDR: off VF: 16 UF: 1 Result:9

> LDR: off VF: 8 UF: 1

Conclusion

- Utilize compiler knowledge in iterative compilation
- Easy to use
- Simple implementation





Thank you!

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