Dominance is not a Tree Towards More Precise Dominance Relations

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Outline

- Dominator Tree
- Non-Tree Dominance
- Dominator Grove
- Empirical Results
- The Good, The Bad, and The Ugly
- Formalization
- Concurrency
- Questions





Control Flow Graph (CFG)

```
define f(cond){
  entry:
    br cond, a, b
  a:
    br cond, b, c
  b:
    В
    br cond, c, a
  c:
    ret
```





Dominator Tree









Dominator Tree

```
define f(cond){
  entry:
    br cond, a, b
  a:
    br cond, b, c
  b:
    В
    br cond, c, a
  c:
    ret
```

CFG



Dominator Tree







Dominance Partial Order

```
define f(cond){
  entry:
    br cond, a, b
  a:
    br cond, b, c
  b:
    В
    br cond, c, a
  c:
    ret
```

CFG



Dominance



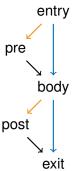




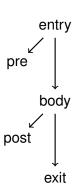
Dominator Tree

```
define f(cond){
  entry:
    br cond, pre, body
  pre:
    PRE
    br body
  body:
    BODY
    br cond, post, exit
  post:
    POST
    br exit
  exit:
    ret
```

CFG



Dominator Tree



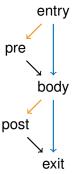




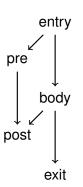
Dominance Partial Order

```
define f(cond){
  entry:
    br cond, pre, body
  pre:
    PRE
    br body
  body:
    BODY
    br cond, post, exit
  post:
    POST
    br exit
  exit:
    ret
```

CFG



Dominance







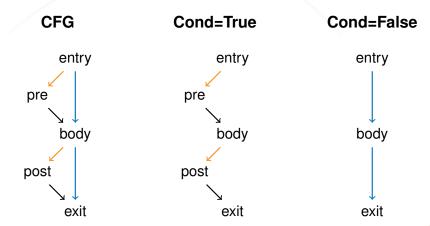
Dominator Grove

Idea: Use shared SSA condition variables to do case analysis-based dominance queries





Dominator Grove Example







Empirical Observations

How often do non-tree domination relations occur in practice?

llvm-test-suite (imprecise measurements)

- \sim 15% of LLVM modules
- $\approx 0.15\%$ of total calls to dominates





The Good: licm-control-flow-hoisting

Input

DomTree

DomGrove

```
define f(cond){
                     define f(cond){
  entry:
                       entry:
    br loop
                         inv = ...
  loop:
                         br loop
    br cond, if,
                       loop:
        then
                         br cond, if,
  if:
                             then
    inv = \dots
                       if:
    call f(inv)
                        call f(inv)
    br then
                         br then
  then:
                       then:
    . . .
                          . . .
    br ..., loop
                         br ..., loop
```

```
define f(cond){
  entry:
    br cond, pIf, loop
  pIf:
    inv = ...
    br loop
  loop:
    br cond, if, then
  if:
    call f(inv)
    br then
  then:
    br ..., loop
}
```





The Bad

- DomGrove updates/invalidation: Need changes on potentially all terminator instruction updates
- Unclear performance impact
- Iterated dominance frontier
- Transforms





The Ugly

Implicit assumptions of dominance tree structure

Example: Region Analysis





Formalization

Valid Paths

LLVM ⊆ Conditional CFG ⊆ CFG

Dominance Relation

CFG ⊂ Conditional CFG ⊂ LLVM





Concurrency







Concurrency









Questions?





Answers?



