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1
2  ./bomb:      file format elf64-x86-64
3
4
5  Disassembly of section .init:
6
7  0000000000400ac0 <_init>:
8      400ac0:  48 83 ec 08          sub    $0x8,%rsp
9      400ac4:  e8 f3 01 00 00      callq 400cbc <call_gmon_start>
10     400ac9:  48 83 c4 08          add    $0x8,%rsp
11     400acd:  c3                  retq
12
13  Disassembly of section .plt:
14
15  0000000000400ad0 <.plt>:
16     400ad0:  ff 35 1a 25 20 00    pushq 0x20251a(%rip)          # 602ff0 ↗
17     <_GLOBAL_OFFSET_TABLE_+0x8>
18     400ad6:  ff 25 1c 25 20 00    jmpq  *0x20251c(%rip)        # 602ff8 ↗
19     <_GLOBAL_OFFSET_TABLE_+0x10>
20     400adc:  0f 1f 40 00          nopl  0x0(%rax)
21
22  0000000000400ae0 <getenv@plt>:
23     400ae0:  ff 25 1a 25 20 00    jmpq  *0x20251a(%rip)        # 603000 ↗
24     <getenv@GLIBC_2.2.5>
25     400ae6:  68 00 00 00 00      pushq $0x0
26     400aeb:  e9 e0 ff ff ff      jmpq  400ad0 <.plt>
27
28  0000000000400af0 <__errno_location@plt>:
29     400af0:  ff 25 12 25 20 00    jmpq  *0x202512(%rip)        # 603008 ↗
30     <__errno_location@GLIBC_2.2.5>
31     400af6:  68 01 00 00 00      pushq $0x1
32     400afb:  e9 d0 ff ff ff      jmpq  400ad0 <.plt>
33
34  0000000000400b00 <strcpy@plt>:
35     400b00:  ff 25 0a 25 20 00    jmpq  *0x20250a(%rip)        # 603010 ↗
36     <strcpy@GLIBC_2.2.5>
37     400b06:  68 02 00 00 00      pushq $0x2
38     400b0b:  e9 c0 ff ff ff      jmpq  400ad0 <.plt>
39
40  0000000000400b10 <puts@plt>:
41     400b10:  ff 25 02 25 20 00    jmpq  *0x202502(%rip)        # 603018 ↗
42     <puts@GLIBC_2.2.5>
43     400b16:  68 03 00 00 00      pushq $0x3
44     400b1b:  e9 b0 ff ff ff      jmpq  400ad0 <.plt>
45
46  0000000000400b20 <write@plt>:
47     400b20:  ff 25 fa 24 20 00    jmpq  *0x2024fa(%rip)        # 603020 ↗
48     <write@GLIBC_2.2.5>
49     400b26:  68 04 00 00 00      pushq $0x4
50     400b2b:  e9 a0 ff ff ff      jmpq  400ad0 <.plt>
51
52  0000000000400b30 <__stack_chk_fail@plt>:
53     400b30:  ff 25 f2 24 20 00    jmpq  *0x2024f2(%rip)        # 603028 ↗

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    <__stack_chk_fail@GLIBC_2.4>
47  400b36:  68 05 00 00 00      pushq  $0x5
48  400b3b:  e9 90 ff ff ff      jmpq   400ad0 <.plt>
49
50  0000000000400b40 <alarm@plt>:
51  400b40:  ff 25 ea 24 20 00    jmpq   *0x2024ea(%rip)      # 603030 ↗
    <alarm@GLIBC_2.2.5>
52  400b46:  68 06 00 00 00      pushq  $0x6
53  400b4b:  e9 80 ff ff ff      jmpq   400ad0 <.plt>
54
55  0000000000400b50 <close@plt>:
56  400b50:  ff 25 e2 24 20 00    jmpq   *0x2024e2(%rip)      # 603038 ↗
    <close@GLIBC_2.2.5>
57  400b56:  68 07 00 00 00      pushq  $0x7
58  400b5b:  e9 70 ff ff ff      jmpq   400ad0 <.plt>
59
60  0000000000400b60 <read@plt>:
61  400b60:  ff 25 da 24 20 00    jmpq   *0x2024da(%rip)      # 603040 ↗
    <read@GLIBC_2.2.5>
62  400b66:  68 08 00 00 00      pushq  $0x8
63  400b6b:  e9 60 ff ff ff      jmpq   400ad0 <.plt>
64
65  0000000000400b70 <__libc_start_main@plt>:
66  400b70:  ff 25 d2 24 20 00    jmpq   *0x2024d2(%rip)      # 603048 ↗
    <__libc_start_main@GLIBC_2.2.5>
67  400b76:  68 09 00 00 00      pushq  $0x9
68  400b7b:  e9 50 ff ff ff      jmpq   400ad0 <.plt>
69
70  0000000000400b80 <fgets@plt>:
71  400b80:  ff 25 ca 24 20 00    jmpq   *0x2024ca(%rip)      # 603050 ↗
    <fgets@GLIBC_2.2.5>
72  400b86:  68 0a 00 00 00      pushq  $0xa
73  400b8b:  e9 40 ff ff ff      jmpq   400ad0 <.plt>
74
75  0000000000400b90 <signal@plt>:
76  400b90:  ff 25 c2 24 20 00    jmpq   *0x2024c2(%rip)      # 603058 ↗
    <signal@GLIBC_2.2.5>
77  400b96:  68 0b 00 00 00      pushq  $0xb
78  400b9b:  e9 30 ff ff ff      jmpq   400ad0 <.plt>
79
80  0000000000400ba0 <gethostbyname@plt>:
81  400ba0:  ff 25 ba 24 20 00    jmpq   *0x2024ba(%rip)      # 603060 ↗
    <gethostbyname@GLIBC_2.2.5>
82  400ba6:  68 0c 00 00 00      pushq  $0xc
83  400bab:  e9 20 ff ff ff      jmpq   400ad0 <.plt>
84
85  0000000000400bb0 <__memmove_chk@plt>:
86  400bb0:  ff 25 b2 24 20 00    jmpq   *0x2024b2(%rip)      # 603068 ↗
    <__memmove_chk@GLIBC_2.3.4>
87  400bb6:  68 0d 00 00 00      pushq  $0xd
88  400bbb:  e9 10 ff ff ff      jmpq   400ad0 <.plt>
89
90  0000000000400bc0 <__memcpy_chk@plt>:

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91  400bc0:  ff 25 aa 24 20 00      jmpq    *0x2024aa(%rip)      # 603070 ↗
    <__memcpy_chk@GLIBC_2.3.4>
92  400bc6:  68 0e 00 00 00        pushq   $0xe
93  400bcb:  e9 00 ff ff ff        jmpq    400ad0 <.plt>
94
95  0000000000400bd0 <strtol@plt>:
96  400bd0:  ff 25 a2 24 20 00      jmpq    *0x2024a2(%rip)      # 603078 ↗
    <strtol@GLIBC_2.2.5>
97  400bd6:  68 0f 00 00 00        pushq   $0xf
98  400bdb:  e9 f0 fe ff ff        jmpq    400ad0 <.plt>
99
100 0000000000400be0 <fflush@plt>:
101 400be0:  ff 25 9a 24 20 00      jmpq    *0x20249a(%rip)      # 603080 ↗
    <fflush@GLIBC_2.2.5>
102 400be6:  68 10 00 00 00        pushq   $0x10
103 400beb:  e9 e0 fe ff ff        jmpq    400ad0 <.plt>
104
105 0000000000400bf0 <__isoc99_sscanf@plt>:
106 400bf0:  ff 25 92 24 20 00      jmpq    *0x202492(%rip)      # 603088 ↗
    <__isoc99_sscanf@GLIBC_2.7>
107 400bf6:  68 11 00 00 00        pushq   $0x11
108 400bfb:  e9 d0 fe ff ff        jmpq    400ad0 <.plt>
109
110 0000000000400c00 <__printf_chk@plt>:
111 400c00:  ff 25 8a 24 20 00      jmpq    *0x20248a(%rip)      # 603090 ↗
    <__printf_chk@GLIBC_2.3.4>
112 400c06:  68 12 00 00 00        pushq   $0x12
113 400c0b:  e9 c0 fe ff ff        jmpq    400ad0 <.plt>
114
115 0000000000400c10 <fopen@plt>:
116 400c10:  ff 25 82 24 20 00      jmpq    *0x202482(%rip)      # 603098 ↗
    <fopen@GLIBC_2.2.5>
117 400c16:  68 13 00 00 00        pushq   $0x13
118 400c1b:  e9 b0 fe ff ff        jmpq    400ad0 <.plt>
119
120 0000000000400c20 <exit@plt>:
121 400c20:  ff 25 7a 24 20 00      jmpq    *0x20247a(%rip)      # 6030a0 ↗
    <exit@GLIBC_2.2.5>
122 400c26:  68 14 00 00 00        pushq   $0x14
123 400c2b:  e9 a0 fe ff ff        jmpq    400ad0 <.plt>
124
125 0000000000400c30 <connect@plt>:
126 400c30:  ff 25 72 24 20 00      jmpq    *0x202472(%rip)      # 6030a8 ↗
    <connect@GLIBC_2.2.5>
127 400c36:  68 15 00 00 00        pushq   $0x15
128 400c3b:  e9 90 fe ff ff        jmpq    400ad0 <.plt>
129
130 0000000000400c40 <__fprintf_chk@plt>:
131 400c40:  ff 25 6a 24 20 00      jmpq    *0x20246a(%rip)      # 6030b0 ↗
    <__fprintf_chk@GLIBC_2.3.4>
132 400c46:  68 16 00 00 00        pushq   $0x16
133 400c4b:  e9 80 fe ff ff        jmpq    400ad0 <.plt>
134

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135 0000000000400c50 <sleep@plt>:
136 400c50: ff 25 62 24 20 00      jmpq    *0x202462(%rip)      # 6030b8 ↗
      <sleep@GLIBC_2.2.5>
137 400c56: 68 17 00 00 00        pushq   $0x17
138 400c5b: e9 70 fe ff ff        jmpq    400ad0 <.plt>
139
140 0000000000400c60 <__ctype_b_loc@plt>:
141 400c60: ff 25 5a 24 20 00      jmpq    *0x20245a(%rip)      # 6030c0 ↗
      <__ctype_b_loc@GLIBC_2.3>
142 400c66: 68 18 00 00 00        pushq   $0x18
143 400c6b: e9 60 fe ff ff        jmpq    400ad0 <.plt>
144
145 0000000000400c70 <__sprintf_chk@plt>:
146 400c70: ff 25 52 24 20 00      jmpq    *0x202452(%rip)      # 6030c8 ↗
      <__sprintf_chk@GLIBC_2.3.4>
147 400c76: 68 19 00 00 00        pushq   $0x19
148 400c7b: e9 50 fe ff ff        jmpq    400ad0 <.plt>
149
150 0000000000400c80 <socket@plt>:
151 400c80: ff 25 4a 24 20 00      jmpq    *0x20244a(%rip)      # 6030d0 ↗
      <socket@GLIBC_2.2.5>
152 400c86: 68 1a 00 00 00        pushq   $0x1a
153 400c8b: e9 40 fe ff ff        jmpq    400ad0 <.plt>
154
155 Disassembly of section .text:
156
157 0000000000400c90 <_start>:
158 400c90: 31 ed                  xor     %ebp,%ebp
159 400c92: 49 89 d1               mov     %rdx,%r9
160 400c95: 5e                     pop     %rsi
161 400c96: 48 89 e2               mov     %rsp,%rdx
162 400c99: 48 83 e4 f0            and     $0xfffffffffffffff0,%rsp
163 400c9d: 50                     push    %rax
164 400c9e: 54                     push    %rsp
165 400c9f: 49 c7 c0 a0 22 40 00    mov     $0x4022a0,%r8
166 400ca6: 48 c7 c1 10 22 40 00    mov     $0x402210,%rcx
167 400cad: 48 c7 c7 a0 0d 40 00    mov     $0x400da0,%rdi
168 400cb4: e8 b7 fe ff ff        callq   400b70 <__libc_start_main@plt>
169 400cb9: f4                     hlt
170 400cba: 90                     nop
171 400cbb: 90                     nop
172
173 0000000000400cbc <call_gmon_start>:
174 400cbc: 48 83 ec 08            sub     $0x8,%rsp
175 400cc0: 48 8b 05 19 23 20 00    mov     0x202319(%rip),%rax      # ↗
      602fe0 <__gmon_start__>
176 400cc7: 48 85 c0               test    %rax,%rax
177 400cca: 74 02                  je      400cce <call_gmon_start+0x12>
178 400ccc: ff d0                  callq   *%rax
179 400cce: 48 83 c4 08            add     $0x8,%rsp
180 400cd2: c3                     retq
181 400cd3: 90                     nop
182 400cd4: 90                     nop

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183  400cd5:  90                      nop
184  400cd6:  90                      nop
185  400cd7:  90                      nop
186  400cd8:  90                      nop
187  400cd9:  90                      nop
188  400cda:  90                      nop
189  400cdb:  90                      nop
190  400cdc:  90                      nop
191  400cdd:  90                      nop
192  400cde:  90                      nop
193  400cdf:  90                      nop
194
195  0000000000400ce0 <deregister_tm_clones>:
196  400ce0:  b8 47 37 60 00        mov     $0x603747,%eax
197  400ce5:  55                    push    %rbp
198  400ce6:  48 2d 40 37 60 00    sub     $0x603740,%rax
199  400cec:  48 83 f8 0e          cmp     $0xe,%rax
200  400cf0:  48 89 e5            mov     %rsp,%rbp
201  400cf3:  77 02              ja      400cf7 <deregister_tm_clones
      +0x17>
202  400cf5:  5d                    pop     %rbp
203  400cf6:  c3                    retq
204  400cf7:  b8 00 00 00 00      mov     $0x0,%eax
205  400cfc:  48 85 c0            test    %rax,%rax
206  400cff:  74 f4              je      400cf5 <deregister_tm_clones
      +0x15>
207  400d01:  5d                    pop     %rbp
208  400d02:  bf 40 37 60 00      mov     $0x603740,%edi
209  400d07:  ff e0              jmpq    *%rax
210  400d09:  0f 1f 80 00 00 00 00 nopl    0x0(%rax)
211
212  0000000000400d10 <register_tm_clones>:
213  400d10:  b8 40 37 60 00      mov     $0x603740,%eax
214  400d15:  55                    push    %rbp
215  400d16:  48 2d 40 37 60 00    sub     $0x603740,%rax
216  400d1c:  48 c1 f8 03          sar     $0x3,%rax
217  400d20:  48 89 e5            mov     %rsp,%rbp
218  400d23:  48 89 c2            mov     %rax,%rdx
219  400d26:  48 c1 ea 3f          shr     $0x3f,%rdx
220  400d2a:  48 01 d0            add     %rdx,%rax
221  400d2d:  48 d1 f8            sar     %rax
222  400d30:  75 02              jne     400d34 <register_tm_clones+0x24>
223  400d32:  5d                    pop     %rbp
224  400d33:  c3                    retq
225  400d34:  ba 00 00 00 00      mov     $0x0,%edx
226  400d39:  48 85 d2            test    %rdx,%rdx
227  400d3c:  74 f4              je      400d32 <register_tm_clones+0x22>
228  400d3e:  5d                    pop     %rbp
229  400d3f:  48 89 c6            mov     %rax,%rsi
230  400d42:  bf 40 37 60 00      mov     $0x603740,%edi
231  400d47:  ff e2              jmpq    *%rdx
232  400d49:  0f 1f 80 00 00 00 00 nopl    0x0(%rax)
233

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234 0000000000400d50 <__do_global_dtors_aux>:
235 400d50: 80 3d 01 2a 20 00 00    cmpb   $0x0,0x202a01(%rip)          # 237
        603758 <completed.6976>
236 400d57: 75 11                    jne     400d6a <__do_global_dtors_aux+0x1a>
237 400d59: 55                      push    %rbp
238 400d5a: 48 89 e5                mov     %rsp,%rbp
239 400d5d: e8 7e ff ff ff         callq   400ce0 <deregister_tm_clones>
240 400d62: 5d                      pop     %rbp
241 400d63: c6 05 ee 29 20 00 01    movb   $0x1,0x2029ee(%rip)          # 243
        603758 <completed.6976>
242 400d6a: f3 c3                  repz    retq
243 400d6c: 0f 1f 40 00            nopl    0x0(%rax)
244
245 0000000000400d70 <frame_dummy>:
246 400d70: 48 83 3d 90 20 20 00    cmpq   $0x0,0x202090(%rip)          # 248
        602e08 <__JCR_END__>
247 400d77: 00                      je      400d98 <frame_dummy+0x28>
248 400d78: 74 1e                    mov     $0x0,%eax
249 400d7a: b8 00 00 00 00          test    %rax,%rax
250 400d7f: 48 85 c0                je      400d98 <frame_dummy+0x28>
251 400d82: 74 14                    push    %rbp
252 400d84: 55                      mov     $0x602e08,%edi
253 400d85: bf 08 2e 60 00          mov     %rsp,%rbp
254 400d8a: 48 89 e5                callq   *%rax
255 400d8d: ff d0                  pop     %rbp
256 400d8f: 5d                      jmpq    400d10 <register_tm_clones>
257 400d90: e9 7b ff ff ff         nopl    (%rax)
258 400d95: 0f 1f 00                jmpq    400d10 <register_tm_clones>
259 400d98: e9 73 ff ff ff         nop
260 400d9d: 90                      nop
261 400d9e: 90                      nop
262 400d9f: 90                      nop
263
264 0000000000400da0 <main>:
265 400da0: 53                      push    %rbx
266 400da1: 83 ff 01                cmp     $0x1,%edi
267 400da4: 75 10                    jne     400db6 <main+0x16>
268 400da6: 48 8b 05 9b 29 20 00    mov     0x20299b(%rip),%rax          # 269
        603748 <stdin@@GLIBC_2.2.5>
269 400dad: 48 89 05 b4 29 20 00    mov     %rax,0x2029b4(%rip)          # 270
        603768 <infile>
270 400db4: eb 63                    jmp     400e19 <main+0x79>
271 400db6: 48 89 f3                mov     %rsi,%rbx
272 400db9: 83 ff 02                cmp     $0x2,%edi
273 400dbc: 75 3a                    jne     400df8 <main+0x58>
274 400dbe: 48 8b 7e 08             mov     0x8(%rsi),%rdi
275 400dc2: be b4 22 40 00          mov     $0x4022b4,%esi
276 400dc7: e8 44 fe ff ff         callq   400c10 <fopen@plt>
277 400dcc: 48 89 05 95 29 20 00    mov     %rax,0x202995(%rip)          # 278
        603768 <infile>
278 400dd3: 48 85 c0                test    %rax,%rax
279 400dd6: 75 41                    jne     400e19 <main+0x79>

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280 400dd8: 48 8b 4b 08      mov     0x8(%rbx),%rcx
281 400ddc: 48 8b 13         mov     (%rbx),%rdx
282 400ddf: be b6 22 40 00   mov     $0x4022b6,%esi
283 400de4: bf 01 00 00 00   mov     $0x1,%edi
284 400de9: e8 12 fe ff ff   callq   400c00 <__printf_chk@plt>
285 400dee: bf 08 00 00 00   mov     $0x8,%edi
286 400df3: e8 28 fe ff ff   callq   400c20 <exit@plt>
287 400df8: 48 8b 16         mov     (%rsi),%rdx
288 400dfb: be d3 22 40 00   mov     $0x4022d3,%esi
289 400e00: bf 01 00 00 00   mov     $0x1,%edi
290 400e05: b8 00 00 00 00   mov     $0x0,%eax
291 400e0a: e8 f1 fd ff ff   callq   400c00 <__printf_chk@plt>
292 400e0f: bf 08 00 00 00   mov     $0x8,%edi
293 400e14: e8 07 fe ff ff   callq   400c20 <exit@plt>
294 400e19: e8 84 05 00 00   callq   4013a2 <initialize_bomb>
295 400e1e: bf 38 23 40 00   mov     $0x402338,%edi
296 400e23: e8 e8 fc ff ff   callq   400b10 <puts@plt>
297 400e28: bf 78 23 40 00   mov     $0x402378,%edi
298 400e2d: e8 de fc ff ff   callq   400b10 <puts@plt>
299 400e32: e8 67 06 00 00   callq   40149e <read_line>
300 400e37: 48 89 c7         mov     %rax,%rdi
301 400e3a: e8 a1 00 00 00   callq   400ee0 <phase_1>
302 400e3f: e8 80 07 00 00   callq   4015c4 <phase_defused>
303 400e44: bf a8 23 40 00   mov     $0x4023a8,%edi
304 400e49: e8 c2 fc ff ff   callq   400b10 <puts@plt>
305 400e4e: e8 4b 06 00 00   callq   40149e <read_line>
306 400e53: 48 89 c7         mov     %rax,%rdi
307 400e56: e8 a1 00 00 00   callq   400efc <phase_2>
308 400e5b: e8 64 07 00 00   callq   4015c4 <phase_defused>
309 400e60: bf ed 22 40 00   mov     $0x4022ed,%edi
310 400e65: e8 a6 fc ff ff   callq   400b10 <puts@plt>
311 400e6a: e8 2f 06 00 00   callq   40149e <read_line>
312 400e6f: 48 89 c7         mov     %rax,%rdi
313 400e72: e8 cc 00 00 00   callq   400f43 <phase_3>
314 400e77: e8 48 07 00 00   callq   4015c4 <phase_defused>
315 400e7c: bf 0b 23 40 00   mov     $0x40230b,%edi
316 400e81: e8 8a fc ff ff   callq   400b10 <puts@plt>
317 400e86: e8 13 06 00 00   callq   40149e <read_line>
318 400e8b: 48 89 c7         mov     %rax,%rdi
319 400e8e: e8 79 01 00 00   callq   40100c <phase_4>
320 400e93: e8 2c 07 00 00   callq   4015c4 <phase_defused>
321 400e98: bf d8 23 40 00   mov     $0x4023d8,%edi
322 400e9d: e8 6e fc ff ff   callq   400b10 <puts@plt>
323 400ea2: e8 f7 05 00 00   callq   40149e <read_line>
324 400ea7: 48 89 c7         mov     %rax,%rdi
325 400eaa: e8 b3 01 00 00   callq   401062 <phase_5>
326 400eaf: e8 10 07 00 00   callq   4015c4 <phase_defused>
327 400eb4: bf 1a 23 40 00   mov     $0x40231a,%edi
328 400eb9: e8 52 fc ff ff   callq   400b10 <puts@plt>
329 400ebe: e8 db 05 00 00   callq   40149e <read_line>
330 400ec3: 48 89 c7         mov     %rax,%rdi
331 400ec6: e8 29 02 00 00   callq   4010f4 <phase_6>
332 400ecb: e8 f4 06 00 00   callq   4015c4 <phase_defused>

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333 400ed0: b8 00 00 00 00    mov     $0x0,%eax
334 400ed5: 5b                pop     %rbx
335 400ed6: c3                retq
336 400ed7: 90                nop
337 400ed8: 90                nop
338 400ed9: 90                nop
339 400eda: 90                nop
340 400edb: 90                nop
341 400edc: 90                nop
342 400edd: 90                nop
343 400ede: 90                nop
344 400edf: 90                nop
345
346 0000000000400ee0 <phase_1>:
347 400ee0: 48 83 ec 08      sub     $0x8,%rsp
348 400ee4: be 00 24 40 00    mov     $0x402400,%esi
349 400ee9: e8 4a 04 00 00    callq   401338 <strings_not_equal>
350 400eee: 85 c0            test    %eax,%eax
351 400ef0: 74 05            je      400ef7 <phase_1+0x17>
352 400ef2: e8 43 05 00 00    callq   40143a <explode_bomb>
353 400ef7: 48 83 c4 08      add     $0x8,%rsp
354 400efb: c3                retq
355
356 0000000000400efc <phase_2>:
357 400efc: 55                push    %rbp
358 400efd: 53                push    %rbx
359 400efe: 48 83 ec 28      sub     $0x28,%rsp
360 400f02: 48 89 e6          mov     %rsp,%rsi
361 400f05: e8 52 05 00 00    callq   40145c <read_six_numbers>
362 400f0a: 83 3c 24 01      cmpl    $0x1,(%rsp)
363 400f0e: 74 20            je      400f30 <phase_2+0x34>
364 400f10: e8 25 05 00 00    callq   40143a <explode_bomb>
365 400f15: eb 19            jmp     400f30 <phase_2+0x34>
366 400f17: 8b 43 fc          mov     -0x4(%rbx),%eax
367 400f1a: 01 c0            add     %eax,%eax
368 400f1c: 39 03            cmp     %eax,(%rbx)
369 400f1e: 74 05            je      400f25 <phase_2+0x29>
370 400f20: e8 15 05 00 00    callq   40143a <explode_bomb>
371 400f25: 48 83 c3 04      add     $0x4,%rbx
372 400f29: 48 39 eb          cmp     %rbp,%rbx
373 400f2c: 75 e9            jne     400f17 <phase_2+0x1b>
374 400f2e: eb 0c            jmp     400f3c <phase_2+0x40>
375 400f30: 48 8d 5c 24 04    lea     0x4(%rsp),%rbx
376 400f35: 48 8d 6c 24 18    lea     0x18(%rsp),%rbp
377 400f3a: eb db            jmp     400f17 <phase_2+0x1b>
378 400f3c: 48 83 c4 28      add     $0x28,%rsp
379 400f40: 5b                pop     %rbx
380 400f41: 5d                pop     %rbp
381 400f42: c3                retq
382
383 0000000000400f43 <phase_3>:
384 400f43: 48 83 ec 18      sub     $0x18,%rsp
385 400f47: 48 8d 4c 24 0c    lea     0xc(%rsp),%rcx

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386 400f4c: 48 8d 54 24 08      lea    0x8(%rsp),%rdx
387 400f51: be cf 25 40 00      mov    $0x4025cf,%esi
388 400f56: b8 00 00 00 00      mov    $0x0,%eax
389 400f5b: e8 90 fc ff ff      callq 400bf0 <__isoc99_sscanf@plt>
390 400f60: 83 f8 01            cmp    $0x1,%eax
391 400f63: 7f 05              jg     400f6a <phase_3+0x27>
392 400f65: e8 d0 04 00 00      callq 40143a <explode_bomb>
393 400f6a: 83 7c 24 08 07      cmpl   $0x7,0x8(%rsp)
394 400f6f: 77 3c              ja     400fad <phase_3+0x6a>
395 400f71: 8b 44 24 08      mov    0x8(%rsp),%eax
396 400f75: ff 24 c5 70 24 40 00 jmpq    *0x402470(,%rax,8)
397 400f7c: b8 cf 00 00 00      mov    $0xcf,%eax
398 400f81: eb 3b              jmp    400fbe <phase_3+0x7b>
399 400f83: b8 c3 02 00 00      mov    $0x2c3,%eax
400 400f88: eb 34              jmp    400fbe <phase_3+0x7b>
401 400f8a: b8 00 01 00 00      mov    $0x100,%eax
402 400f8f: eb 2d              jmp    400fbe <phase_3+0x7b>
403 400f91: b8 85 01 00 00      mov    $0x185,%eax
404 400f96: eb 26              jmp    400fbe <phase_3+0x7b>
405 400f98: b8 ce 00 00 00      mov    $0xce,%eax
406 400f9d: eb 1f              jmp    400fbe <phase_3+0x7b>
407 400f9f: b8 aa 02 00 00      mov    $0x2aa,%eax
408 400fa4: eb 18              jmp    400fbe <phase_3+0x7b>
409 400fa6: b8 47 01 00 00      mov    $0x147,%eax
410 400fab: eb 11              jmp    400fbe <phase_3+0x7b>
411 400fad: e8 88 04 00 00      callq 40143a <explode_bomb>
412 400fb2: b8 00 00 00 00      mov    $0x0,%eax
413 400fb7: eb 05              jmp    400fbe <phase_3+0x7b>
414 400fb9: b8 37 01 00 00      mov    $0x137,%eax
415 400fbe: 3b 44 24 0c      cmp    0xc(%rsp),%eax
416 400fc2: 74 05              je     400fc9 <phase_3+0x86>
417 400fc4: e8 71 04 00 00      callq 40143a <explode_bomb>
418 400fc9: 48 83 c4 18      add    $0x18,%rsp
419 400fcd: c3                retq
420
421 000000000400fce <func4>:
422 400fce: 48 83 ec 08      sub    $0x8,%rsp
423 400fd2: 89 d0            mov    %edx,%eax
424 400fd4: 29 f0            sub    %esi,%eax
425 400fd6: 89 c1            mov    %eax,%ecx
426 400fd8: c1 e9 1f          shr    $0x1f,%ecx
427 400fdb: 01 c8            add    %ecx,%eax
428 400fdd: d1 f8            sar    %eax
429 400fdf: 8d 0c 30          lea    (%rax,%rsi,1),%ecx
430 400fe2: 39 f9            cmp    %edi,%ecx
431 400fe4: 7e 0c            jle    400ff2 <func4+0x24>
432 400fe6: 8d 51 ff          lea    -0x1(%rcx),%edx
433 400fe9: e8 e0 ff ff ff      callq 400fce <func4>
434 400fee: 01 c0            add    %eax,%eax
435 400ff0: eb 15            jmp    401007 <func4+0x39>
436 400ff2: b8 00 00 00 00      mov    $0x0,%eax
437 400ff7: 39 f9            cmp    %edi,%ecx
438 400ff9: 7d 0c            jge    401007 <func4+0x39>

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439 400ffb: 8d 71 01          lea    0x1(%rcx),%esi
440 400ffe: e8 cb ff ff ff    callq 400fce <func4>
441 401003: 8d 44 00 01       lea    0x1(%rax,%rax,1),%eax
442 401007: 48 83 c4 08       add    $0x8,%rsp
443 40100b: c3               retq
444
445 000000000040100c <phase_4>:
446 40100c: 48 83 ec 18       sub    $0x18,%rsp
447 401010: 48 8d 4c 24 0c    lea    0xc(%rsp),%rcx
448 401015: 48 8d 54 24 08    lea    0x8(%rsp),%rdx
449 40101a: be cf 25 40 00    mov    $0x4025cf,%esi
450 40101f: b8 00 00 00 00    mov    $0x0,%eax
451 401024: e8 c7 fb ff ff    callq 400bf0 <__isoc99_sscanf@plt>
452 401029: 83 f8 02         cmp    $0x2,%eax
453 40102c: 75 07           jne    401035 <phase_4+0x29>
454 40102e: 83 7c 24 08 0e    cmpl   $0xe,0x8(%rsp)
455 401033: 76 05           jbe    40103a <phase_4+0x2e>
456 401035: e8 00 04 00 00    callq 40143a <explode_bomb>
457 40103a: ba 0e 00 00 00    mov    $0xe,%edx
458 40103f: be 00 00 00 00    mov    $0x0,%esi
459 401044: 8b 7c 24 08       mov    0x8(%rsp),%edi
460 401048: e8 81 ff ff ff    callq 400fce <func4>
461 40104d: 85 c0           test   %eax,%eax
462 40104f: 75 07           jne    401058 <phase_4+0x4c>
463 401051: 83 7c 24 0c 00    cmpl   $0x0,0xc(%rsp)
464 401056: 74 05           je     40105d <phase_4+0x51>
465 401058: e8 dd 03 00 00    callq 40143a <explode_bomb>
466 40105d: 48 83 c4 18       add    $0x18,%rsp
467 401061: c3               retq
468
469 0000000000401062 <phase_5>:
470 401062: 53               push   %rbx
471 401063: 48 83 ec 20       sub    $0x20,%rsp
472 401067: 48 89 fb         mov    %rdi,%rbx
473 40106a: 64 48 8b 04 25 28 00 mov    %fs:0x28,%rax
474 401071: 00 00
475 401073: 48 89 44 24 18    mov    %rax,0x18(%rsp)
476 401078: 31 c0           xor    %eax,%eax
477 40107a: e8 9c 02 00 00    callq 40131b <string_length>
478 40107f: 83 f8 06         cmp    $0x6,%eax
479 401082: 74 4e           je     4010d2 <phase_5+0x70>
480 401084: e8 b1 03 00 00    callq 40143a <explode_bomb>
481 401089: eb 47           jmp    4010d2 <phase_5+0x70>
482 40108b: 0f b6 0c 03       movzbl (%rbx,%rax,1),%ecx
483 40108f: 88 0c 24         mov    %cl,(%rsp)
484 401092: 48 8b 14 24       mov    (%rsp),%rdx
485 401096: 83 e2 0f         and    $0xf,%edx
486 401099: 0f b6 92 b0 24 40 00 movzbl 0x4024b0(%rdx),%edx
487 4010a0: 88 54 04 10       mov    %dl,0x10(%rsp,%rax,1)
488 4010a4: 48 83 c0 01       add    $0x1,%rax
489 4010a8: 48 83 f8 06       cmp    $0x6,%rax
490 4010ac: 75 dd           jne    40108b <phase_5+0x29>
491 4010ae: c6 44 24 16 00    movb   $0x0,0x16(%rsp)

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492 4010b3: be 5e 24 40 00      mov     $0x40245e,%esi
493 4010b8: 48 8d 7c 24 10      lea     0x10(%rsp),%rdi
494 4010bd: e8 76 02 00 00      callq  401338 <strings_not_equal>
495 4010c2: 85 c0               test    %eax,%eax
496 4010c4: 74 13               je      4010d9 <phase_5+0x77>
497 4010c6: e8 6f 03 00 00      callq  40143a <explode_bomb>
498 4010cb: 0f 1f 44 00 00      nopl    0x0(%rax,%rax,1)
499 4010d0: eb 07               jmp     4010d9 <phase_5+0x77>
500 4010d2: b8 00 00 00 00      mov     $0x0,%eax
501 4010d7: eb b2               jmp     40108b <phase_5+0x29>
502 4010d9: 48 8b 44 24 18      mov     0x18(%rsp),%rax
503 4010de: 64 48 33 04 25 28 00 xor     %fs:0x28,%rax
504 4010e5: 00 00
505 4010e7: 74 05               je      4010ee <phase_5+0x8c>
506 4010e9: e8 42 fa ff ff      callq  400b30 <__stack_chk_fail@plt>
507 4010ee: 48 83 c4 20          add     $0x20,%rsp
508 4010f2: 5b                  pop     %rbx
509 4010f3: c3                  retq
510
511 00000000004010f4 <phase_6>:
512 4010f4: 41 56               push    %r14
513 4010f6: 41 55               push    %r13
514 4010f8: 41 54               push    %r12
515 4010fa: 55                  push    %rbp
516 4010fb: 53                  push    %rbx
517 4010fc: 48 83 ec 50          sub     $0x50,%rsp
518 401100: 49 89 e5             mov     %rsp,%r13
519 401103: 48 89 e6             mov     %rsp,%rsi
520 401106: e8 51 03 00 00      callq  40145c <read_six_numbers>
521 40110b: 49 89 e6             mov     %rsp,%r14
522 40110e: 41 bc 00 00 00 00    mov     $0x0,%r12d
523 401114: 4c 89 ed             mov     %r13,%rbp
524 401117: 41 8b 45 00          mov     0x0(%r13),%eax
525 40111b: 83 e8 01             sub     $0x1,%eax
526 40111e: 83 f8 05             cmp     $0x5,%eax
527 401121: 76 05               jbe     401128 <phase_6+0x34>
528 401123: e8 12 03 00 00      callq  40143a <explode_bomb>
529 401128: 41 83 c4 01          add     $0x1,%r12d
530 40112c: 41 83 fc 06          cmp     $0x6,%r12d
531 401130: 74 21               je      401153 <phase_6+0x5f>
532 401132: 44 89 e3             mov     %r12d,%ebx
533 401135: 48 63 c3             movslq  %ebx,%rax
534 401138: 8b 04 84             mov     (%rsp,%rax,4),%eax
535 40113b: 39 45 00             cmp     %eax,0x0(%rbp)
536 40113e: 75 05               jne     401145 <phase_6+0x51>
537 401140: e8 f5 02 00 00      callq  40143a <explode_bomb>
538 401145: 83 c3 01             add     $0x1,%ebx
539 401148: 83 fb 05             cmp     $0x5,%ebx
540 40114b: 7e e8               jle     401135 <phase_6+0x41>
541 40114d: 49 83 c5 04          add     $0x4,%r13
542 401151: eb c1               jmp     401114 <phase_6+0x20>
543 401153: 48 8d 74 24 18      lea     0x18(%rsp),%rsi
544 401158: 4c 89 f0             mov     %r14,%rax

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545 40115b: b9 07 00 00 00    mov     $0x7,%ecx
546 401160: 89 ca             mov     %ecx,%edx
547 401162: 2b 10             sub     (%rax),%edx
548 401164: 89 10             mov     %edx,(%rax)
549 401166: 48 83 c0 04       add     $0x4,%rax
550 40116a: 48 39 f0          cmp     %rsi,%rax
551 40116d: 75 f1             jne     401160 <phase_6+0x6c>
552 40116f: be 00 00 00 00    mov     $0x0,%esi
553 401174: eb 21             jmp     401197 <phase_6+0xa3>
554 401176: 48 8b 52 08       mov     0x8(%rdx),%rdx
555 40117a: 83 c0 01          add     $0x1,%eax
556 40117d: 39 c8             cmp     %ecx,%eax
557 40117f: 75 f5             jne     401176 <phase_6+0x82>
558 401181: eb 05             jmp     401188 <phase_6+0x94>
559 401183: ba d0 32 60 00    mov     $0x6032d0,%edx
560 401188: 48 89 54 74 20     mov     %rdx,0x20(%rsp,%rsi,2)
561 40118d: 48 83 c6 04       add     $0x4,%rsi
562 401191: 48 83 fe 18       cmp     $0x18,%rsi
563 401195: 74 14             je      4011ab <phase_6+0xb7>
564 401197: 8b 0c 34          mov     (%rsp,%rsi,1),%ecx
565 40119a: 83 f9 01          cmp     $0x1,%ecx
566 40119d: 7e e4             jle     401183 <phase_6+0x8f>
567 40119f: b8 01 00 00 00    mov     $0x1,%eax
568 4011a4: ba d0 32 60 00    mov     $0x6032d0,%edx
569 4011a9: eb cb             jmp     401176 <phase_6+0x82>
570 4011ab: 48 8b 5c 24 20     mov     0x20(%rsp),%rbx
571 4011b0: 48 8d 44 24 28     lea     0x28(%rsp),%rax
572 4011b5: 48 8d 74 24 50     lea     0x50(%rsp),%rsi
573 4011ba: 48 89 d9          mov     %rbx,%rcx
574 4011bd: 48 8b 10          mov     (%rax),%rdx
575 4011c0: 48 89 51 08       mov     %rdx,0x8(%rcx)
576 4011c4: 48 83 c0 08       add     $0x8,%rax
577 4011c8: 48 39 f0          cmp     %rsi,%rax
578 4011cb: 74 05             je      4011d2 <phase_6+0xde>
579 4011cd: 48 89 d1          mov     %rdx,%rcx
580 4011d0: eb eb             jmp     4011bd <phase_6+0xc9>
581 4011d2: 48 c7 42 08 00 00 00 00    movq    $0x0,0x8(%rdx)
582 4011d9: 00
583 4011da: bd 05 00 00 00    mov     $0x5,%ebp
584 4011df: 48 8b 43 08       mov     0x8(%rbx),%rax
585 4011e3: 8b 00             mov     (%rax),%eax
586 4011e5: 39 03             cmp     %eax,(%rbx)
587 4011e7: 7d 05             jge     4011ee <phase_6+0xfa>
588 4011e9: e8 4c 02 00 00    callq   40143a <explode_bomb>
589 4011ee: 48 8b 5b 08       mov     0x8(%rbx),%rbx
590 4011f2: 83 ed 01          sub     $0x1,%ebp
591 4011f5: 75 e8             jne     4011df <phase_6+0xeb>
592 4011f7: 48 83 c4 50       add     $0x50,%rsp
593 4011fb: 5b               pop     %rbx
594 4011fc: 5d               pop     %rbp
595 4011fd: 41 5c             pop     %r12
596 4011ff: 41 5d             pop     %r13
597 401201: 41 5e             pop     %r14

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```

598 401203: c3                retq
599
600 0000000000401204 <fun7>:
601 401204: 48 83 ec 08      sub    $0x8,%rsp
602 401208: 48 85 ff         test   %rdi,%rdi
603 40120b: 74 2b           je     401238 <fun7+0x34>
604 40120d: 8b 17           mov     (%rdi),%edx
605 40120f: 39 f2           cmp     %esi,%edx
606 401211: 7e 0d           jle     401220 <fun7+0x1c>
607 401213: 48 8b 7f 08      mov     0x8(%rdi),%rdi
608 401217: e8 e8 ff ff ff   callq  401204 <fun7>
609 40121c: 01 c0           add     %eax,%eax
610 40121e: eb 1d           jmp     40123d <fun7+0x39>
611 401220: b8 00 00 00 00   mov     $0x0,%eax
612 401225: 39 f2           cmp     %esi,%edx
613 401227: 74 14           je     40123d <fun7+0x39>
614 401229: 48 8b 7f 10      mov     0x10(%rdi),%rdi
615 40122d: e8 d2 ff ff ff   callq  401204 <fun7>
616 401232: 8d 44 00 01      lea     0x1(%rax,%rax,1),%eax
617 401236: eb 05           jmp     40123d <fun7+0x39>
618 401238: b8 ff ff ff ff   mov     $0xffffffff,%eax
619 40123d: 48 83 c4 08      add     $0x8,%rsp
620 401241: c3                retq
621
622 0000000000401242 <secret_phase>:
623 401242: 53              push    %rbx
624 401243: e8 56 02 00 00   callq  40149e <read_line>
625 401248: ba 0a 00 00 00   mov     $0xa,%edx
626 40124d: be 00 00 00 00   mov     $0x0,%esi
627 401252: 48 89 c7         mov     %rax,%rdi
628 401255: e8 76 f9 ff ff   callq  400bd0 <strtol@plt>
629 40125a: 48 89 c3         mov     %rax,%rbx
630 40125d: 8d 40 ff         lea     -0x1(%rax),%eax
631 401260: 3d e8 03 00 00   cmp     $0x3e8,%eax
632 401265: 76 05           jbe     40126c <secret_phase+0x2a>
633 401267: e8 ce 01 00 00   callq  40143a <explode_bomb>
634 40126c: 89 de           mov     %ebx,%esi
635 40126e: bf f0 30 60 00   mov     $0x6030f0,%edi
636 401273: e8 8c ff ff ff   callq  401204 <fun7>
637 401278: 83 f8 02         cmp     $0x2,%eax
638 40127b: 74 05           je     401282 <secret_phase+0x40>
639 40127d: e8 b8 01 00 00   callq  40143a <explode_bomb>
640 401282: bf 38 24 40 00   mov     $0x402438,%edi
641 401287: e8 84 f8 ff ff   callq  400b10 <puts@plt>
642 40128c: e8 33 03 00 00   callq  4015c4 <phase_defused>
643 401291: 5b              pop     %rbx
644 401292: c3                retq
645 401293: 90              nop
646 401294: 90              nop
647 401295: 90              nop
648 401296: 90              nop
649 401297: 90              nop
650 401298: 90              nop

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651 401299: 90 nop
652 40129a: 90 nop
653 40129b: 90 nop
654 40129c: 90 nop
655 40129d: 90 nop
656 40129e: 90 nop
657 40129f: 90 nop
658
659 00000000004012a0 <sig_handler>:
660 4012a0: 48 83 ec 08 sub $0x8,%rsp
661 4012a4: bf c0 24 40 00 mov $0x4024c0,%edi
662 4012a9: e8 62 f8 ff ff callq 400b10 <puts@plt>
663 4012ae: bf 03 00 00 00 mov $0x3,%edi
664 4012b3: e8 98 f9 ff ff callq 400c50 <sleep@plt>
665 4012b8: be 82 25 40 00 mov $0x402582,%esi
666 4012bd: bf 01 00 00 00 mov $0x1,%edi
667 4012c2: b8 00 00 00 00 mov $0x0,%eax
668 4012c7: e8 34 f9 ff ff callq 400c00 <__printf_chk@plt>
669 4012cc: 48 8b 3d 6d 24 20 00 mov 0x20246d(%rip),%rdi # 7
670 603740 <stdout@@GLIBC_2.2.5>
670 4012d3: e8 08 f9 ff ff callq 400be0 <fflush@plt>
671 4012d8: bf 01 00 00 00 mov $0x1,%edi
672 4012dd: e8 6e f9 ff ff callq 400c50 <sleep@plt>
673 4012e2: bf 8a 25 40 00 mov $0x40258a,%edi
674 4012e7: e8 24 f8 ff ff callq 400b10 <puts@plt>
675 4012ec: bf 10 00 00 00 mov $0x10,%edi
676 4012f1: e8 2a f9 ff ff callq 400c20 <exit@plt>
677
678 00000000004012f6 <invalid_phase>:
679 4012f6: 48 83 ec 08 sub $0x8,%rsp
680 4012fa: 48 89 fa mov %rdi,%rdx
681 4012fd: be 92 25 40 00 mov $0x402592,%esi
682 401302: bf 01 00 00 00 mov $0x1,%edi
683 401307: b8 00 00 00 00 mov $0x0,%eax
684 40130c: e8 ef f8 ff ff callq 400c00 <__printf_chk@plt>
685 401311: bf 08 00 00 00 mov $0x8,%edi
686 401316: e8 05 f9 ff ff callq 400c20 <exit@plt>
687
688 000000000040131b <string_length>:
689 40131b: 80 3f 00 cmpb $0x0,(%rdi)
690 40131e: 74 12 je 401332 <string_length+0x17>
691 401320: 48 89 fa mov %rdi,%rdx
692 401323: 48 83 c2 01 add $0x1,%rdx
693 401327: 89 d0 mov %edx,%eax
694 401329: 29 f8 sub %edi,%eax
695 40132b: 80 3a 00 cmpb $0x0,(%rdx)
696 40132e: 75 f3 jne 401323 <string_length+0x8>
697 401330: f3 c3 repz retq
698 401332: b8 00 00 00 00 mov $0x0,%eax
699 401337: c3 retq
700
701 0000000000401338 <strings_not_equal>:
702 401338: 41 54 push %r12

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703 40133a: 55                push    %rbp
704 40133b: 53                push    %rbx
705 40133c: 48 89 fb          mov     %rdi,%rbx
706 40133f: 48 89 f5          mov     %rsi,%rbp
707 401342: e8 d4 ff ff ff    callq   40131b <string_length>
708 401347: 41 89 c4          mov     %eax,%r12d
709 40134a: 48 89 ef          mov     %rbp,%rdi
710 40134d: e8 c9 ff ff ff    callq   40131b <string_length>
711 401352: ba 01 00 00 00    mov     $0x1,%edx
712 401357: 41 39 c4          cmp     %eax,%r12d
713 40135a: 75 3f             jne     40139b <strings_not_equal+0x63>
714 40135c: 0f b6 03          movzbl  (%rbx),%eax
715 40135f: 84 c0             test    %al,%al
716 401361: 74 25             je      401388 <strings_not_equal+0x50>
717 401363: 3a 45 00          cmp     0x0(%rbp),%al
718 401366: 74 0a             je      401372 <strings_not_equal+0x3a>
719 401368: eb 25             jmp     40138f <strings_not_equal+0x57>
720 40136a: 3a 45 00          cmp     0x0(%rbp),%al
721 40136d: 0f 1f 00          nopl    (%rax)
722 401370: 75 24             jne     401396 <strings_not_equal+0x5e>
723 401372: 48 83 c3 01       add     $0x1,%rbx
724 401376: 48 83 c5 01       add     $0x1,%rbp
725 40137a: 0f b6 03          movzbl  (%rbx),%eax
726 40137d: 84 c0             test    %al,%al
727 40137f: 75 e9             jne     40136a <strings_not_equal+0x32>
728 401381: ba 00 00 00 00    mov     $0x0,%edx
729 401386: eb 13             jmp     40139b <strings_not_equal+0x63>
730 401388: ba 00 00 00 00    mov     $0x0,%edx
731 40138d: eb 0c             jmp     40139b <strings_not_equal+0x63>
732 40138f: ba 01 00 00 00    mov     $0x1,%edx
733 401394: eb 05             jmp     40139b <strings_not_equal+0x63>
734 401396: ba 01 00 00 00    mov     $0x1,%edx
735 40139b: 89 d0             mov     %edx,%eax
736 40139d: 5b               pop     %rbx
737 40139e: 5d               pop     %rbp
738 40139f: 41 5c             pop     %r12
739 4013a1: c3               retq
740
741 00000000004013a2 <initialize_bomb>:
742 4013a2: 48 83 ec 08       sub     $0x8,%rsp
743 4013a6: be a0 12 40 00    mov     $0x4012a0,%esi
744 4013ab: bf 02 00 00 00    mov     $0x2,%edi
745 4013b0: e8 db f7 ff ff    callq   400b90 <signal@plt>
746 4013b5: 48 83 c4 08       add     $0x8,%rsp
747 4013b9: c3               retq
748
749 00000000004013ba <initialize_bomb_solve>:
750 4013ba: f3 c3             repz retq
751
752 00000000004013bc <blank_line>:
753 4013bc: 55                push    %rbp
754 4013bd: 53                push    %rbx
755 4013be: 48 83 ec 08       sub     $0x8,%rsp

```



```

756 4013c2: 48 89 fb          mov    %rdi,%rbx
757 4013c5: eb 17            jmp    4013de <blank_line+0x22>
758 4013c7: e8 94 f8 ff ff    callq 400c60 <__ctype_b_loc@plt>
759 4013cc: 48 83 c3 01       add    $0x1,%rbx
760 4013d0: 48 0f be ed       movsbq %bpl,%rbp
761 4013d4: 48 8b 00          mov    (%rax),%rax
762 4013d7: f6 44 68 01 20    testb $0x20,0x1(%rax,%rbp,2)
763 4013dc: 74 0f            je     4013ed <blank_line+0x31>
764 4013de: 0f b6 2b          movzbl (%rbx),%ebp
765 4013e1: 40 84 ed          test   %bpl,%bpl
766 4013e4: 75 e1            jne    4013c7 <blank_line+0xb>
767 4013e6: b8 01 00 00 00    mov    $0x1,%eax
768 4013eb: eb 05            jmp    4013f2 <blank_line+0x36>
769 4013ed: b8 00 00 00 00    mov    $0x0,%eax
770 4013f2: 48 83 c4 08       add    $0x8,%rsp
771 4013f6: 5b              pop    %rbx
772 4013f7: 5d              pop    %rbp
773 4013f8: c3              retq
774
775 00000000004013f9 <skip>:
776 4013f9: 53              push   %rbx
777 4013fa: 48 63 05 5f 23 20 00 movslq 0x20235f(%rip),%rax      #
778 603760 <num_input_strings>
779 401401: 48 8d 3c 80      lea    (%rax,%rax,4),%rdi
780 401405: 48 c1 e7 04      shl    $0x4,%rdi
781 401409: 48 81 c7 80 37 60 00 add    $0x603780,%rdi
782 401410: 48 8b 15 51 23 20 00 mov    0x202351(%rip),%rdx      #
783 603768 <infile>
784 401417: be 50 00 00 00    mov    $0x50,%esi
785 40141c: e8 5f f7 ff ff    callq 400b80 <fgets@plt>
786 401421: 48 89 c3         mov    %rax,%rbx
787 401424: 48 85 c0         test   %rax,%rax
788 401427: 74 0c            je     401435 <skip+0x3c>
789 401429: 48 89 c7         mov    %rax,%rdi
790 40142c: e8 8b ff ff ff    callq 4013bc <blank_line>
791 401431: 85 c0            test   %eax,%eax
792 401433: 75 c5            jne    4013fa <skip+0x1>
793 401435: 48 89 d8         mov    %rbx,%rax
794 401438: 5b              pop    %rbx
795 401439: c3              retq
796
795 000000000040143a <explode_bomb>:
796 40143a: 48 83 ec 08      sub    $0x8,%rsp
797 40143e: bf a3 25 40 00    mov    $0x4025a3,%edi
798 401443: e8 c8 f6 ff ff    callq 400b10 <puts@plt>
799 401448: bf ac 25 40 00    mov    $0x4025ac,%edi
800 40144d: e8 be f6 ff ff    callq 400b10 <puts@plt>
801 401452: bf 08 00 00 00    mov    $0x8,%edi
802 401457: e8 c4 f7 ff ff    callq 400c20 <exit@plt>
803
804 000000000040145c <read_six_numbers>:
805 40145c: 48 83 ec 18      sub    $0x18,%rsp
806 401460: 48 89 f2         mov    %rsi,%rdx

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807 401463: 48 8d 4e 04      lea    0x4(%rsi),%rcx
808 401467: 48 8d 46 14      lea    0x14(%rsi),%rax
809 40146b: 48 89 44 24 08   mov     %rax,0x8(%rsp)
810 401470: 48 8d 46 10      lea    0x10(%rsi),%rax
811 401474: 48 89 04 24      mov     %rax,(%rsp)
812 401478: 4c 8d 4e 0c      lea    0xc(%rsi),%r9
813 40147c: 4c 8d 46 08      lea    0x8(%rsi),%r8
814 401480: be c3 25 40 00   mov     $0x4025c3,%esi
815 401485: b8 00 00 00 00   mov     $0x0,%eax
816 40148a: e8 61 f7 ff ff   callq  400bf0 <__isoc99_sscanf@plt>
817 40148f: 83 f8 05         cmp     $0x5,%eax
818 401492: 7f 05           jg      401499 <read_six_numbers+0x3d>
819 401494: e8 a1 ff ff ff   callq  40143a <explode_bomb>
820 401499: 48 83 c4 18      add     $0x18,%rsp
821 40149d: c3              retq
822
823 000000000040149e <read_line>:
824 40149e: 48 83 ec 08      sub     $0x8,%rsp
825 4014a2: b8 00 00 00 00   mov     $0x0,%eax
826 4014a7: e8 4d ff ff ff   callq  4013f9 <skip>
827 4014ac: 48 85 c0         test    %rax,%rax
828 4014af: 75 6e           jne     40151f <read_line+0x81>
829 4014b1: 48 8b 05 90 22 00 mov     0x202290(%rip),%rax      # 7
603748 <stdin@@GLIBC_2.2.5>
830 4014b8: 48 39 05 a9 22 00 cmp     %rax,0x2022a9(%rip)      # 7
603768 <infile>
831 4014bf: 75 14           jne     4014d5 <read_line+0x37>
832 4014c1: bf d5 25 40 00   mov     $0x4025d5,%edi
833 4014c6: e8 45 f6 ff ff   callq  400b10 <puts@plt>
834 4014cb: bf 08 00 00 00   mov     $0x8,%edi
835 4014d0: e8 4b f7 ff ff   callq  400c20 <exit@plt>
836 4014d5: bf f3 25 40 00   mov     $0x4025f3,%edi
837 4014da: e8 01 f6 ff ff   callq  400ae0 <getenv@plt>
838 4014df: 48 85 c0         test    %rax,%rax
839 4014e2: 74 0a           je      4014ee <read_line+0x50>
840 4014e4: bf 00 00 00 00   mov     $0x0,%edi
841 4014e9: e8 32 f7 ff ff   callq  400c20 <exit@plt>
842 4014ee: 48 8b 05 53 22 00 mov     0x202253(%rip),%rax      # 7
603748 <stdin@@GLIBC_2.2.5>
843 4014f5: 48 89 05 6c 22 00 mov     %rax,0x20226c(%rip)      # 7
603768 <infile>
844 4014fc: b8 00 00 00 00   mov     $0x0,%eax
845 401501: e8 f3 fe ff ff   callq  4013f9 <skip>
846 401506: 48 85 c0         test    %rax,%rax
847 401509: 75 14           jne     40151f <read_line+0x81>
848 40150b: bf d5 25 40 00   mov     $0x4025d5,%edi
849 401510: e8 fb f5 ff ff   callq  400b10 <puts@plt>
850 401515: bf 00 00 00 00   mov     $0x0,%edi
851 40151a: e8 01 f7 ff ff   callq  400c20 <exit@plt>
852 40151f: 8b 15 3b 22 00   mov     0x20223b(%rip),%edx      # 7
603760 <num_input_strings>
853 401525: 48 63 c2        movslq  %edx,%rax
854 401528: 48 8d 34 80      lea     (%rax,%rax,4),%rsi

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855 40152c: 48 c1 e6 04      shl     $0x4,%rsi
856 401530: 48 81 c6 80 37 60 00 add     $0x603780,%rsi
857 401537: 48 89 f7         mov     %rsi,%rdi
858 40153a: b8 00 00 00 00   mov     $0x0,%eax
859 40153f: 48 c7 c1 ff ff ff ff mov     $0xffffffffffffffff,%rcx
860 401546: f2 ae          repnz  scas %es:(%rdi),%al
861 401548: 48 f7 d1        not     %rcx
862 40154b: 48 83 e9 01      sub     $0x1,%rcx
863 40154f: 83 f9 4e        cmp     $0x4e,%ecx
864 401552: 7e 46          jle     40159a <read_line+0xfc>
865 401554: bf fe 25 40 00   mov     $0x4025fe,%edi
866 401559: e8 b2 f5 ff ff   callq  400b10 <puts@plt>
867 40155e: 8b 05 fc 21 20 00 mov     0x2021fc(%rip),%eax      # ↗
      603760 <num_input_strings>
868 401564: 8d 50 01        lea     0x1(%rax),%edx
869 401567: 89 15 f3 21 20 00 mov     %edx,0x2021f3(%rip)      # ↗
      603760 <num_input_strings>
870 40156d: 48 98          cltq
871 40156f: 48 6b c0 50     imul    $0x50,%rax,%rax
872 401573: 48 bf 2a 2a 2a 74 72 movabs  $0x636e7572742a2a2a,%rdi
873 40157a: 75 6e 63        jnb     40157d
874 40157d: 48 89 b8 80 37 60 00 mov     %rdi,0x603780(%rax)
875 401584: 48 bf 61 74 65 64 2a movabs  $0x2a2a2a64657461,%rdi
876 40158b: 2a 2a 00        jnb     40158e
877 40158e: 48 89 b8 88 37 60 00 mov     %rdi,0x603788(%rax)
878 401595: e8 a0 fe ff ff   callq  40143a <explode_bomb>
879 40159a: 83 e9 01        sub     $0x1,%ecx
880 40159d: 48 63 c9        movslq  %ecx,%rcx
881 4015a0: 48 63 c2        movslq  %edx,%rax
882 4015a3: 48 8d 04 80     lea     (%rax,%rax,4),%rax
883 4015a7: 48 c1 e0 04     shl     $0x4,%rax
884 4015ab: c6 84 01 80 37 60 00 movb     $0x0,0x603780(%rcx,%rax,1)
885 4015b2: 00             jnb     4015b3
886 4015b3: 83 c2 01        add     $0x1,%edx
887 4015b6: 89 15 a4 21 20 00 mov     %edx,0x2021a4(%rip)      # ↗
      603760 <num_input_strings>
888 4015bc: 48 89 f0        mov     %rsi,%rax
889 4015bf: 48 83 c4 08     add     $0x8,%rsp
890 4015c3: c3             retq
891
892 00000000004015c4 <phase_defused>:
893 4015c4: 48 83 ec 78     sub     $0x78,%rsp
894 4015c8: 64 48 8b 04 25 28 00 mov     %fs:0x28,%rax
895 4015cf: 00 00          jnb     4015d1
896 4015d1: 48 89 44 24 68   mov     %rax,0x68(%rsp)
897 4015d6: 31 c0          xor     %eax,%eax
898 4015d8: 83 3d 81 21 20 00 06 cmpl    $0x6,0x202181(%rip)      # ↗
      603760 <num_input_strings>
899 4015df: 75 5e          jne     40163f <phase_defused+0x7b>
900 4015e1: 4c 8d 44 24 10   lea     0x10(%rsp),%r8
901 4015e6: 48 8d 4c 24 0c   lea     0xc(%rsp),%rcx
902 4015eb: 48 8d 54 24 08   lea     0x8(%rsp),%rdx
903 4015f0: be 19 26 40 00   mov     $0x402619,%esi

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904 4015f5: bf 70 38 60 00      mov     $0x603870,%edi
905 4015fa: e8 f1 f5 ff ff      callq  400bf0 <__isoc99_sscanf@plt>
906 4015ff: 83 f8 03            cmp     $0x3,%eax
907 401602: 75 31              jne     401635 <phase_defused+0x71>
908 401604: be 22 26 40 00      mov     $0x402622,%esi
909 401609: 48 8d 7c 24 10      lea     0x10(%rsp),%rdi
910 40160e: e8 25 fd ff ff      callq  401338 <strings_not_equal>
911 401613: 85 c0              test    %eax,%eax
912 401615: 75 1e              jne     401635 <phase_defused+0x71>
913 401617: bf f8 24 40 00      mov     $0x4024f8,%edi
914 40161c: e8 ef f4 ff ff      callq  400b10 <puts@plt>
915 401621: bf 20 25 40 00      mov     $0x402520,%edi
916 401626: e8 e5 f4 ff ff      callq  400b10 <puts@plt>
917 40162b: b8 00 00 00 00      mov     $0x0,%eax
918 401630: e8 0d fc ff ff      callq  401242 <secret_phase>
919 401635: bf 58 25 40 00      mov     $0x402558,%edi
920 40163a: e8 d1 f4 ff ff      callq  400b10 <puts@plt>
921 40163f: 48 8b 44 24 68      mov     0x68(%rsp),%rax
922 401644: 64 48 33 04 25 28 00 xor     %fs:0x28,%rax
923 40164b: 00 00
924 40164d: 74 05              je      401654 <phase_defused+0x90>
925 40164f: e8 dc f4 ff ff      callq  400b30 <__stack_chk_fail@plt>
926 401654: 48 83 c4 78        add     $0x78,%rsp
927 401658: c3                retq
928 401659: 90                nop
929 40165a: 90                nop
930 40165b: 90                nop
931 40165c: 90                nop
932 40165d: 90                nop
933 40165e: 90                nop
934 40165f: 90                nop
935
936 0000000000401660 <sigalrm_handler>:
937 401660: 48 83 ec 08        sub     $0x8,%rsp
938 401664: b9 00 00 00 00      mov     $0x0,%ecx
939 401669: ba 78 26 40 00      mov     $0x402678,%edx
940 40166e: be 01 00 00 00      mov     $0x1,%esi
941 401673: 48 8b 3d d6 20 20 00 mov     0x2020d6(%rip),%rdi      # 7
      603750 <stderr@@GLIBC_2.2.5>
942 40167a: b8 00 00 00 00      mov     $0x0,%eax
943 40167f: e8 bc f5 ff ff      callq  400c40 <__fprintf_chk@plt>
944 401684: bf 01 00 00 00      mov     $0x1,%edi
945 401689: e8 92 f5 ff ff      callq  400c20 <exit@plt>
946
947 000000000040168e <rio_readlineb>:
948 40168e: 41 57              push    %r15
949 401690: 41 56              push    %r14
950 401692: 41 55              push    %r13
951 401694: 41 54              push    %r12
952 401696: 55                push    %rbp
953 401697: 53                push    %rbx
954 401698: 48 83 ec 38        sub     $0x38,%rsp
955 40169c: 49 89 f6            mov     %rsi,%r14

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956 40169f: 48 89 54 24 18      mov     %rdx,0x18(%rsp)
957 4016a4: 48 83 fa 01         cmp     $0x1,%rdx
958 4016a8: 0f 86 c9 00 00 00    jbe     401777 <rio_readlineb+0xe9>
959 4016ae: 48 89 fb           mov     %rdi,%rbx
960 4016b1: 41 bd 01 00 00 00    mov     $0x1,%r13d
961 4016b7: 4c 8d 67 10         lea     0x10(%rdi),%r12
962 4016bb: eb 30             jmp     4016ed <rio_readlineb+0x5f>
963 4016bd: ba 00 20 00 00      mov     $0x2000,%edx
964 4016c2: 4c 89 e6           mov     %r12,%rsi
965 4016c5: 8b 3b             mov     (%rbx),%edi
966 4016c7: e8 94 f4 ff ff      callq   400b60 <read@plt>
967 4016cc: 89 43 04           mov     %eax,0x4(%rbx)
968 4016cf: 85 c0             test    %eax,%eax
969 4016d1: 79 12             jns     4016e5 <rio_readlineb+0x57>
970 4016d3: e8 18 f4 ff ff      callq   400af0 <__errno_location@plt>
971 4016d8: 83 38 04           cmpl    $0x4,(%rax)
972 4016db: 74 10             je      4016ed <rio_readlineb+0x5f>
973 4016dd: 0f 1f 00          nopl    (%rax)
974 4016e0: e9 a1 00 00 00      jmpq    401786 <rio_readlineb+0xf8>
975 4016e5: 85 c0             test    %eax,%eax
976 4016e7: 74 71             je      40175a <rio_readlineb+0xcc>
977 4016e9: 4c 89 63 08         mov     %r12,0x8(%rbx)
978 4016ed: 8b 6b 04           mov     0x4(%rbx),%ebp
979 4016f0: 85 ed             test    %ebp,%ebp
980 4016f2: 7e c9             jle     4016bd <rio_readlineb+0x2f>
981 4016f4: 85 ed             test    %ebp,%ebp
982 4016f6: 41 0f 95 c7         setne   %r15b
983 4016fa: 41 0f b6 c7         movzbl  %r15b,%eax
984 4016fe: 89 44 24 0c         mov     %eax,0xc(%rsp)
985 401702: 45 0f b6 ff         movzbl  %r15b,%r15d
986 401706: 48 8b 4b 08         mov     0x8(%rbx),%rcx
987 40170a: 48 89 ce           mov     %rcx,%rsi
988 40170d: b9 01 00 00 00      mov     $0x1,%ecx
989 401712: 4c 89 fa           mov     %r15,%rdx
990 401715: 48 89 74 24 10      mov     %rsi,0x10(%rsp)
991 40171a: 48 8d 7c 24 2f      lea     0x2f(%rsp),%rdi
992 40171f: e8 9c f4 ff ff      callq   400bc0 <__memcpy_chk@plt>
993 401724: 4c 03 7c 24 10      add     0x10(%rsp),%r15
994 401729: 4c 89 7b 08         mov     %r15,0x8(%rbx)
995 40172d: 8b 44 24 0c         mov     0xc(%rsp),%eax
996 401731: 29 c5             sub     %eax,%ebp
997 401733: 89 6b 04           mov     %ebp,0x4(%rbx)
998 401736: 83 f8 01           cmp     $0x1,%eax
999 401739: 75 13             jne     40174e <rio_readlineb+0xc0>
1000 40173b: 49 83 c6 01         add     $0x1,%r14
1001 40173f: 0f b6 44 24 2f      movzbl  0x2f(%rsp),%eax
1002 401744: 41 88 46 ff         mov     %al,-0x1(%r14)
1003 401748: 3c 0a             cmp     $0xa,%al
1004 40174a: 75 18             jne     401764 <rio_readlineb+0xd6>
1005 40174c: eb 2f             jmp     40177d <rio_readlineb+0xef>
1006 40174e: 83 7c 24 0c 00      cmpl    $0x0,0xc(%rsp)
1007 401753: 75 3a             jne     40178f <rio_readlineb+0x101>
1008 401755: 44 89 e8           mov     %r13d,%eax

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1009 401758: eb 03                jmp     40175d <rio_readlineb+0xcf>
1010 40175a: 44 89 e8            mov     %r13d,%eax
1011 40175d: 83 f8 01            cmp     $0x1,%eax
1012 401760: 75 1b                jne     40177d <rio_readlineb+0xef>
1013 401762: eb 34                jmp     401798 <rio_readlineb+0x10a>
1014 401764: 41 83 c5 01          add     $0x1,%r13d
1015 401768: 49 63 c5            movslq  %r13d,%rax
1016 40176b: 48 3b 44 24 18       cmp     0x18(%rsp),%rax
1017 401770: 73 0b                jae     40177d <rio_readlineb+0xef>
1018 401772: e9 76 ff ff ff      jmpq    4016ed <rio_readlineb+0x5f>
1019 401777: 41 bd 01 00 00 00    mov     $0x1,%r13d
1020 40177d: 41 c6 06 00          movb     $0x0,(%r14)
1021 401781: 49 63 c5            movslq  %r13d,%rax
1022 401784: eb 17                jmp     40179d <rio_readlineb+0x10f>
1023 401786: 48 c7 c0 ff ff ff ff mov     $0xffffffffffffffff,%rax
1024 40178d: eb 0e                jmp     40179d <rio_readlineb+0x10f>
1025 40178f: 48 c7 c0 ff ff ff ff mov     $0xffffffffffffffff,%rax
1026 401796: eb 05                jmp     40179d <rio_readlineb+0x10f>
1027 401798: b8 00 00 00 00      mov     $0x0,%eax
1028 40179d: 48 83 c4 38          add     $0x38,%rsp
1029 4017a1: 5b                  pop     %rbx
1030 4017a2: 5d                  pop     %rbp
1031 4017a3: 41 5c                pop     %r12
1032 4017a5: 41 5d                pop     %r13
1033 4017a7: 41 5e                pop     %r14
1034 4017a9: 41 5f                pop     %r15
1035 4017ab: c3                  retq
1036
1037 00000000004017ac <submitr>:
1038 4017ac: 41 57                push    %r15
1039 4017ae: 41 56                push    %r14
1040 4017b0: 41 55                push    %r13
1041 4017b2: 41 54                push    %r12
1042 4017b4: 55                  push    %rbp
1043 4017b5: 53                  push    %rbx
1044 4017b6: 48 81 ec 68 a0 00 00 sub     $0xa068,%rsp
1045 4017bd: 48 89 fd            mov     %rdi,%rbp
1046 4017c0: 41 89 f5            mov     %esi,%r13d
1047 4017c3: 48 89 54 24 10       mov     %rdx,0x10(%rsp)
1048 4017c8: 48 89 4c 24 18       mov     %rcx,0x18(%rsp)
1049 4017cd: 4d 89 c7            mov     %r8,%r15
1050 4017d0: 4c 89 cb            mov     %r9,%rbx
1051 4017d3: 4c 8b b4 24 a0 a0 00 mov     0xa0a0(%rsp),%r14
1052 4017da: 00
1053 4017db: 64 48 8b 04 25 28 00 mov     %fs:0x28,%rax
1054 4017e2: 00 00
1055 4017e4: 48 89 84 24 58 a0 00 mov     %rax,0xa058(%rsp)
1056 4017eb: 00
1057 4017ec: 31 c0                xor     %eax,%eax
1058 4017ee: c7 44 24 2c 00 00 00 movl     $0x0,0x2c(%rsp)
1059 4017f5: 00
1060 4017f6: ba 00 00 00 00      mov     $0x0,%edx
1061 4017fb: be 01 00 00 00      mov     $0x1,%esi

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1062	401800:	bf 02 00 00 00	mov	\$0x2,%edi
1063	401805:	e8 76 f4 ff ff	callq	400c80 <socket@plt>
1064	40180a:	41 89 c4	mov	%eax,%r12d
1065	40180d:	85 c0	test	%eax,%eax
1066	40180f:	79 50	jns	401861 <submitr+0xb5>
1067	401811:	48 b8 45 72 72 6f 72	movabs	\$0x43203a726f727245,%rax
1068	401818:	3a 20 43		
1069	40181b:	49 89 06	mov	%rax,(%r14)
1070	40181e:	48 b8 6c 69 65 6e 74	movabs	\$0x6e7520746e65696c,%rax
1071	401825:	20 75 6e		
1072	401828:	49 89 46 08	mov	%rax,0x8(%r14)
1073	40182c:	48 b8 61 62 6c 65 20	movabs	\$0x206f7420656c6261,%rax
1074	401833:	74 6f 20		
1075	401836:	49 89 46 10	mov	%rax,0x10(%r14)
1076	40183a:	48 b8 63 72 65 61 74	movabs	\$0x7320657461657263,%rax
1077	401841:	65 20 73		
1078	401844:	49 89 46 18	mov	%rax,0x18(%r14)
1079	401848:	41 c7 46 20 6f 63 6b	movl	\$0x656b636f,0x20(%r14)
1080	40184f:	65		
1081	401850:	66 41 c7 46 24 74 00	movw	\$0x74,0x24(%r14)
1082	401857:	b8 ff ff ff ff	mov	\$0xffffffff,%eax
1083	40185c:	e9 07 06 00 00	jmpq	401e68 <submitr+0x6bc>
1084	401861:	48 89 ef	mov	%rbp,%rdi
1085	401864:	e8 37 f3 ff ff	callq	400ba0 <gethostbyname@plt>
1086	401869:	48 85 c0	test	%rax,%rax
1087	40186c:	75 6b	jne	4018d9 <submitr+0x12d>
1088	40186e:	48 b8 45 72 72 6f 72	movabs	\$0x44203a726f727245,%rax
1089	401875:	3a 20 44		
1090	401878:	49 89 06	mov	%rax,(%r14)
1091	40187b:	48 b8 4e 53 20 69 73	movabs	\$0x6e7520736920534e,%rax
1092	401882:	20 75 6e		
1093	401885:	49 89 46 08	mov	%rax,0x8(%r14)
1094	401889:	48 b8 61 62 6c 65 20	movabs	\$0x206f7420656c6261,%rax
1095	401890:	74 6f 20		
1096	401893:	49 89 46 10	mov	%rax,0x10(%r14)
1097	401897:	48 b8 72 65 73 6f 6c	movabs	\$0x2065766c6f736572,%rax
1098	40189e:	76 65 20		
1099	4018a1:	49 89 46 18	mov	%rax,0x18(%r14)
1100	4018a5:	48 b8 73 65 72 76 65	movabs	\$0x6120726576726573,%rax
1101	4018ac:	72 20 61		
1102	4018af:	49 89 46 20	mov	%rax,0x20(%r14)
1103	4018b3:	41 c7 46 28 64 64 72	movl	\$0x65726464,0x28(%r14)
1104	4018ba:	65		
1105	4018bb:	66 41 c7 46 2c 73 73	movw	\$0x7373,0x2c(%r14)
1106	4018c2:	41 c6 46 2e 00	movb	\$0x0,0x2e(%r14)
1107	4018c7:	44 89 e7	mov	%r12d,%edi
1108	4018ca:	e8 81 f2 ff ff	callq	400b50 <close@plt>
1109	4018cf:	b8 ff ff ff ff	mov	\$0xffffffff,%eax
1110	4018d4:	e9 8f 05 00 00	jmpq	401e68 <submitr+0x6bc>
1111	4018d9:	48 c7 44 24 30 00 00	movq	\$0x0,0x30(%rsp)
1112	4018e0:	00 00		
1113	4018e2:	48 c7 44 24 38 00 00	movq	\$0x0,0x38(%rsp)
1114	4018e9:	00 00		


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1115 4018eb: 66 c7 44 24 30 02 00 movw $0x2,0x30(%rsp)
1116 4018f2: 48 63 50 14 movslq 0x14(%rax),%rdx
1117 4018f6: 48 8b 40 18 mov 0x18(%rax),%rax
1118 4018fa: 48 8d 7c 24 34 lea 0x34(%rsp),%rdi
1119 4018ff: b9 0c 00 00 00 mov $0xc,%ecx
1120 401904: 48 8b 30 mov (%rax),%rsi
1121 401907: e8 a4 f2 ff ff callq 400bb0 <__memmove_chk@plt>
1122 40190c: 66 41 c1 cd 08 ror $0x8,%r13w
1123 401911: 66 44 89 6c 24 32 mov %r13w,0x32(%rsp)
1124 401917: ba 10 00 00 00 mov $0x10,%edx
1125 40191c: 48 8d 74 24 30 lea 0x30(%rsp),%rsi
1126 401921: 44 89 e7 mov %r12d,%edi
1127 401924: e8 07 f3 ff ff callq 400c30 <connect@plt>
1128 401929: 85 c0 test %eax,%eax
1129 40192b: 79 5d jns 40198a <submitr+0x1de>
1130 40192d: 48 b8 45 72 72 6f 72 movabs $0x55203a726f727245,%rax
1131 401934: 3a 20 55
1132 401937: 49 89 06 mov %rax,(%r14)
1133 40193a: 48 b8 6e 61 62 6c 65 movabs $0x6f7420656c62616e,%rax
1134 401941: 20 74 6f
1135 401944: 49 89 46 08 mov %rax,0x8(%r14)
1136 401948: 48 b8 20 63 6f 6e 6e movabs $0x7463656e6e6f6320,%rax
1137 40194f: 65 63 74
1138 401952: 49 89 46 10 mov %rax,0x10(%r14)
1139 401956: 48 b8 20 74 6f 20 74 movabs $0x20656874206f7420,%rax
1140 40195d: 68 65 20
1141 401960: 49 89 46 18 mov %rax,0x18(%r14)
1142 401964: 41 c7 46 20 73 65 72 movl $0x76726573,0x20(%r14)
1143 40196b: 76
1144 40196c: 66 41 c7 46 24 65 72 movw $0x7265,0x24(%r14)
1145 401973: 41 c6 46 26 00 movb $0x0,0x26(%r14)
1146 401978: 44 89 e7 mov %r12d,%edi
1147 40197b: e8 d0 f1 ff ff callq 400b50 <close@plt>
1148 401980: b8 ff ff ff ff mov $0xffffffff,%eax
1149 401985: e9 de 04 00 00 jmpq 401e68 <submitr+0x6bc>
1150 40198a: 48 c7 c2 ff ff ff ff mov $0xffffffffffffffff,%rdx
1151 401991: 48 89 df mov %rbx,%rdi
1152 401994: b8 00 00 00 00 mov $0x0,%eax
1153 401999: 48 89 d1 mov %rdx,%rcx
1154 40199c: f2 ae repnz scas %es:(%rdi),%al
1155 40199e: 48 f7 d1 not %rcx
1156 4019a1: 48 89 ce mov %rcx,%rsi
1157 4019a4: 48 8b 7c 24 10 mov 0x10(%rsp),%rdi
1158 4019a9: 48 89 d1 mov %rdx,%rcx
1159 4019ac: f2 ae repnz scas %es:(%rdi),%al
1160 4019ae: 49 89 c8 mov %rcx,%r8
1161 4019b1: 48 8b 7c 24 18 mov 0x18(%rsp),%rdi
1162 4019b6: 48 89 d1 mov %rdx,%rcx
1163 4019b9: f2 ae repnz scas %es:(%rdi),%al
1164 4019bb: 48 f7 d1 not %rcx
1165 4019be: 49 89 c9 mov %rcx,%r9
1166 4019c1: 4c 89 ff mov %r15,%rdi
1167 4019c4: 48 89 d1 mov %rdx,%rcx

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1168 4019c7: f2 ae      repnz scas %es:(%rdi),%al
1169 4019c9: 4d 29 c1    sub     %r8,%r9
1170 4019cc: 49 29 c9    sub     %rcx,%r9
1171 4019cf: 48 8d 44 76 fd  lea     -0x3(%rsi,%rsi,2),%rax
1172 4019d4: 49 8d 44 01 7b  lea     0x7b(%r9,%rax,1),%rax
1173 4019d9: 48 3d 00 20 00 00  cmp     $0x2000,%rax
1174 4019df: 76 73      jbe     401a54 <submitr+0x2a8>
1175 4019e1: 48 b8 45 72 72 6f 72  movabs  $0x52203a726f727245,%rax
1176 4019e8: 3a 20 52
1177 4019eb: 49 89 06    mov     %rax,(%r14)
1178 4019ee: 48 b8 65 73 75 6c 74  movabs  $0x747320746c757365,%rax
1179 4019f5: 20 73 74
1180 4019f8: 49 89 46 08    mov     %rax,0x8(%r14)
1181 4019fc: 48 b8 72 69 6e 67 20  movabs  $0x6f6f7420676e6972,%rax
1182 401a03: 74 6f 6f
1183 401a06: 49 89 46 10    mov     %rax,0x10(%r14)
1184 401a0a: 48 b8 20 6c 61 72 67  movabs  $0x202e656772616c20,%rax
1185 401a11: 65 2e 20
1186 401a14: 49 89 46 18    mov     %rax,0x18(%r14)
1187 401a18: 48 b8 49 6e 63 72 65  movabs  $0x6573616572636e49,%rax
1188 401a1f: 61 73 65
1189 401a22: 49 89 46 20    mov     %rax,0x20(%r14)
1190 401a26: 48 b8 20 53 55 42 4d  movabs  $0x5254494d42555320,%rax
1191 401a2d: 49 54 52
1192 401a30: 49 89 46 28    mov     %rax,0x28(%r14)
1193 401a34: 48 b8 5f 4d 41 58 42  movabs  $0x46554258414d5f,%rax
1194 401a3b: 55 46 00
1195 401a3e: 49 89 46 30    mov     %rax,0x30(%r14)
1196 401a42: 44 89 e7      mov     %r12d,%edi
1197 401a45: e8 06 f1 ff ff  callq   400b50 <close@plt>
1198 401a4a: b8 ff ff ff ff  mov     $0xffffffff,%eax
1199 401a4f: e9 14 04 00 00  jmpq    401e68 <submitr+0x6bc>
1200 401a54: 48 8d 94 24 40 20 00  lea     0x2040(%rsp),%rdx
1201 401a5b: 00
1202 401a5c: b9 00 04 00 00    mov     $0x400,%ecx
1203 401a61: b8 00 00 00 00    mov     $0x0,%eax
1204 401a66: 48 89 d7      mov     %rdx,%rdi
1205 401a69: f3 48 ab      rep stos %rax,%es:(%rdi)
1206 401a6c: 48 89 df      mov     %rbx,%rdi
1207 401a6f: 48 c7 c1 ff ff ff ff  mov     $0xffffffffffffffff,%rcx
1208 401a76: f2 ae      repnz scas %es:(%rdi),%al
1209 401a78: 48 f7 d1      not     %rcx
1210 401a7b: 48 83 e9 01    sub     $0x1,%rcx
1211 401a7f: 85 c9      test    %ecx,%ecx
1212 401a81: 0f 84 fd 03 00 00  je      401e84 <submitr+0x6d8>
1213 401a87: 83 e9 01      sub     $0x1,%ecx
1214 401a8a: 4c 8d 6c 0b 01    lea     0x1(%rbx,%rcx,1),%r13
1215 401a8f: 48 89 d5      mov     %rdx,%rbp
1216 401a92: 44 0f b6 03    movzbl  (%rbx),%r8d
1217 401a96: 41 80 f8 2a    cmp     $0x2a,%r8b
1218 401a9a: 74 23      je      401abf <submitr+0x313>
1219 401a9c: 41 8d 40 d3    lea     -0x2d(%r8),%eax
1220 401aa0: 3c 01      cmp     $0x1,%al

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1221 401aa2: 76 1b          jbe 401abf <submitr+0x313>
1222 401aa4: 41 80 f8 5f    cmp $0x5f,%r8b
1223 401aa8: 74 15          je 401abf <submitr+0x313>
1224 401aaa: 41 8d 40 d0    lea -0x30(%r8),%eax
1225 401aae: 3c 09          cmp $0x9,%al
1226 401ab0: 76 0d          jbe 401abf <submitr+0x313>
1227 401ab2: 44 89 c0       mov %r8d,%eax
1228 401ab5: 83 e0 df       and $0xffffffff,%eax
1229 401ab8: 83 e8 41       sub $0x41,%eax
1230 401abb: 3c 19          cmp $0x19,%al
1231 401abd: 77 0a          ja 401ac9 <submitr+0x31d>
1232 401abf: 48 8d 45 01    lea 0x1(%rbp),%rax
1233 401ac3: 44 88 45 00    mov %r8b,0x0(%rbp)
1234 401ac7: eb 6c          jmp 401b35 <submitr+0x389>
1235 401ac9: 41 80 f8 20    cmp $0x20,%r8b
1236 401acd: 75 0a          jne 401ad9 <submitr+0x32d>
1237 401acf: 48 8d 45 01    lea 0x1(%rbp),%rax
1238 401ad3: c6 45 00 2b    movb $0x2b,0x0(%rbp)
1239 401ad7: eb 5c          jmp 401b35 <submitr+0x389>
1240 401ad9: 41 8d 40 e0    lea -0x20(%r8),%eax
1241 401add: 3c 5f          cmp $0x5f,%al
1242 401adf: 76 0a          jbe 401aeb <submitr+0x33f>
1243 401ae1: 41 80 f8 09    cmp $0x9,%r8b
1244 401ae5: 0f 85 02 04 00 00 jne 401eed <submitr+0x741>
1245 401aeb: 45 0f b6 c0    movzbl %r8b,%r8d
1246 401aef: b9 48 27 40 00 mov $0x402748,%ecx
1247 401af4: ba 08 00 00 00 mov $0x8,%edx
1248 401af9: be 01 00 00 00 mov $0x1,%esi
1249 401afe: 48 8d bc 24 40 80 00 lea 0x8040(%rsp),%rdi
1250 401b05: 00
1251 401b06: b8 00 00 00 00 mov $0x0,%eax
1252 401b0b: e8 60 f1 ff ff callq 400c70 <__sprintf_chk@plt>
1253 401b10: 0f b6 84 24 40 80 00 movzbl 0x8040(%rsp),%eax
1254 401b17: 00
1255 401b18: 88 45 00       mov %al,0x0(%rbp)
1256 401b1b: 0f b6 84 24 41 80 00 movzbl 0x8041(%rsp),%eax
1257 401b22: 00
1258 401b23: 88 45 01       mov %al,0x1(%rbp)
1259 401b26: 48 8d 45 03    lea 0x3(%rbp),%rax
1260 401b2a: 0f b6 94 24 42 80 00 movzbl 0x8042(%rsp),%edx
1261 401b31: 00
1262 401b32: 88 55 02       mov %dl,0x2(%rbp)
1263 401b35: 48 83 c3 01    add $0x1,%rbx
1264 401b39: 4c 39 eb       cmp %r13,%rbx
1265 401b3c: 0f 84 42 03 00 00 je 401e84 <submitr+0x6d8>
1266 401b42: 48 89 c5       mov %rax,%rbp
1267 401b45: e9 48 ff ff ff jmpq 401a92 <submitr+0x2e6>
1268 401b4a: 48 89 da       mov %rbx,%rdx
1269 401b4d: 48 89 ee       mov %rbp,%rsi
1270 401b50: 44 89 e7       mov %r12d,%edi
1271 401b53: e8 c8 ef ff ff callq 400b20 <write@plt>
1272 401b58: 48 85 c0       test %rax,%rax
1273 401b5b: 7f 0f          jg 401b6c <submitr+0x3c0>

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1274 401b5d: e8 8e ef ff ff      callq 400af0 <__errno_location@plt>
1275 401b62: 83 38 04            cmpl  $0x4, (%rax)
1276 401b65: 75 12              jne  401b79 <submitr+0x3cd>
1277 401b67: b8 00 00 00 00      mov  $0x0,%eax
1278 401b6c: 48 01 c5          add  %rax,%rbp
1279 401b6f: 48 29 c3          sub  %rax,%rbx
1280 401b72: 75 d6              jne  401b4a <submitr+0x39e>
1281 401b74: 4d 85 ed          test %r13,%r13
1282 401b77: 79 5f              jns  401bd8 <submitr+0x42c>
1283 401b79: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax
1284 401b80: 3a 20 43
1285 401b83: 49 89 06          mov  %rax, (%r14)
1286 401b86: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax
1287 401b8d: 20 75 6e
1288 401b90: 49 89 46 08      mov  %rax, 0x8(%r14)
1289 401b94: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax
1290 401b9b: 74 6f 20
1291 401b9e: 49 89 46 10      mov  %rax, 0x10(%r14)
1292 401ba2: 48 b8 77 72 69 74 65 movabs $0x6f74206574697277,%rax
1293 401ba9: 20 74 6f
1294 401bac: 49 89 46 18      mov  %rax, 0x18(%r14)
1295 401bb0: 48 b8 20 74 68 65 20 movabs $0x7265732065687420,%rax
1296 401bb7: 73 65 72
1297 401bba: 49 89 46 20      mov  %rax, 0x20(%r14)
1298 401bbe: 41 c7 46 28 76 65 72 movl  $0x726576, 0x28(%r14)
1299 401bc5: 00
1300 401bc6: 44 89 e7          mov  %r12d,%edi
1301 401bc9: e8 82 ef ff ff      callq 400b50 <close@plt>
1302 401bce: b8 ff ff ff ff      mov  $0xffffffff,%eax
1303 401bd3: e9 90 02 00 00      jmpq 401e68 <submitr+0x6bc>
1304 401bd8: 44 89 a4 24 40 80 00 mov  %r12d, 0x8040(%rsp)
1305 401bdf: 00
1306 401be0: c7 84 24 44 80 00 00 movl  $0x0, 0x8044(%rsp)
1307 401be7: 00 00 00 00
1308 401beb: 48 8d 84 24 50 80 00 lea  0x8050(%rsp),%rax
1309 401bf2: 00
1310 401bf3: 48 89 84 24 48 80 00 mov  %rax, 0x8048(%rsp)
1311 401bfa: 00
1312 401bfb: ba 00 20 00 00      mov  $0x2000,%edx
1313 401c00: 48 8d 74 24 40      lea  0x40(%rsp),%rsi
1314 401c05: 48 8d bc 24 40 80 00 lea  0x8040(%rsp),%rdi
1315 401c0c: 00
1316 401c0d: e8 7c fa ff ff      callq 40168e <rio_readlineb>
1317 401c12: 48 85 c0          test %rax,%rax
1318 401c15: 7f 74            jg  401c8b <submitr+0x4df>
1319 401c17: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax
1320 401c1e: 3a 20 43
1321 401c21: 49 89 06          mov  %rax, (%r14)
1322 401c24: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax
1323 401c2b: 20 75 6e
1324 401c2e: 49 89 46 08      mov  %rax, 0x8(%r14)
1325 401c32: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax
1326 401c39: 74 6f 20

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1327 401c3c: 49 89 46 10      mov    %rax,0x10(%r14)
1328 401c40: 48 b8 72 65 61 64 20  movabs $0x7269662064616572,%rax
1329 401c47: 66 69 72
1330 401c4a: 49 89 46 18      mov    %rax,0x18(%r14)
1331 401c4e: 48 b8 73 74 20 68 65  movabs $0x6564616568207473,%rax
1332 401c55: 61 64 65
1333 401c58: 49 89 46 20      mov    %rax,0x20(%r14)
1334 401c5c: 48 b8 72 20 66 72 6f  movabs $0x73206d6f72662072,%rax
1335 401c63: 6d 20 73
1336 401c66: 49 89 46 28      mov    %rax,0x28(%r14)
1337 401c6a: 41 c7 46 30 65 72 76  movl   $0x65767265,0x30(%r14)
1338 401c71: 65
1339 401c72: 66 41 c7 46 34 72 00  movw   $0x72,0x34(%r14)
1340 401c79: 44 89 e7         mov    %r12d,%edi
1341 401c7c: e8 cf ee ff ff   callq 400b50 <close@plt>
1342 401c81: b8 ff ff ff ff   mov    $0xffffffff,%eax
1343 401c86: e9 dd 01 00 00   jmpq   401e68 <submitr+0x6bc>
1344 401c8b: 4c 8d 84 24 40 60 00  lea    0x6040(%rsp),%r8
1345 401c92: 00
1346 401c93: 48 8d 4c 24 2c   lea    0x2c(%rsp),%rcx
1347 401c98: 48 8d 94 24 40 40 00  lea    0x4040(%rsp),%rdx
1348 401c9f: 00
1349 401ca0: be 4f 27 40 00   mov    $0x40274f,%esi
1350 401ca5: 48 8d 7c 24 40   lea    0x40(%rsp),%rdi
1351 401caa: b8 00 00 00 00   mov    $0x0,%eax
1352 401caf: e8 3c ef ff ff   callq 400bf0 <__isoc99_sscanf@plt>
1353 401cb4: 44 8b 44 24 2c   mov    0x2c(%rsp),%r8d
1354 401cb9: 41 81 f8 c8 00 00 00  cmp    $0xc8,%r8d
1355 401cc0: 0f 84 be 00 00 00  je     401d84 <submitr+0x5d8>
1356 401cc6: 4c 8d 8c 24 40 60 00  lea    0x6040(%rsp),%r9
1357 401ccd: 00
1358 401cce: b9 a0 26 40 00   mov    $0x4026a0,%ecx
1359 401cd3: 48 c7 c2 ff ff ff ff  mov    $0xffffffffffffffff,%rdx
1360 401cda: be 01 00 00 00   mov    $0x1,%esi
1361 401cdf: 4c 89 f7         mov    %r14,%rdi
1362 401ce2: b8 00 00 00 00   mov    $0x0,%eax
1363 401ce7: e8 84 ef ff ff   callq 400c70 <__sprintf_chk@plt>
1364 401cec: 44 89 e7         mov    %r12d,%edi
1365 401cef: e8 5c ee ff ff   callq 400b50 <close@plt>
1366 401cf4: b8 ff ff ff ff   mov    $0xffffffff,%eax
1367 401cf9: e9 6a 01 00 00   jmpq   401e68 <submitr+0x6bc>
1368 401cfe: ba 00 20 00 00   mov    $0x2000,%edx
1369 401d03: 48 8d 74 24 40   lea    0x40(%rsp),%rsi
1370 401d08: 48 8d bc 24 40 80 00  lea    0x8040(%rsp),%rdi
1371 401d0f: 00
1372 401d10: e8 79 f9 ff ff   callq 40168e <rio_readlineb>
1373 401d15: 48 85 c0         test   %rax,%rax
1374 401d18: 7f 6a         jg     401d84 <submitr+0x5d8>
1375 401d1a: 48 b8 45 72 72 6f 72  movabs $0x43203a726f727245,%rax
1376 401d21: 3a 20 43
1377 401d24: 49 89 06         mov    %rax,(%r14)
1378 401d27: 48 b8 6c 69 65 6e 74  movabs $0x6e7520746e65696c,%rax
1379 401d2e: 20 75 6e

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1380 401d31: 49 89 46 08      mov    %rax,0x8(%r14)
1381 401d35: 48 b8 61 62 6c 65 20  movabs $0x206f7420656c6261,%rax
1382 401d3c: 74 6f 20
1383 401d3f: 49 89 46 10      mov    %rax,0x10(%r14)
1384 401d43: 48 b8 72 65 61 64 20  movabs $0x6165682064616572,%rax
1385 401d4a: 68 65 61
1386 401d4d: 49 89 46 18      mov    %rax,0x18(%r14)
1387 401d51: 48 b8 64 65 72 73 20  movabs $0x6f72662073726564,%rax
1388 401d58: 66 72 6f
1389 401d5b: 49 89 46 20      mov    %rax,0x20(%r14)
1390 401d5f: 48 b8 6d 20 73 65 72  movabs $0x726576726573206d,%rax
1391 401d66: 76 65 72
1392 401d69: 49 89 46 28      mov    %rax,0x28(%r14)
1393 401d6d: 41 c6 46 30 00    movb   $0x0,0x30(%r14)
1394 401d72: 44 89 e7         mov    %r12d,%edi
1395 401d75: e8 d6 ed ff ff   callq  400b50 <close@plt>
1396 401d7a: b8 ff ff ff ff   mov    $0xffffffff,%eax
1397 401d7f: e9 e4 00 00 00   jmpq   401e68 <submitr+0x6bc>
1398 401d84: 80 7c 24 40 0d   cmpb   $0xd,0x40(%rsp)
1399 401d89: 0f 85 6f ff ff ff jne     401cfe <submitr+0x552>
1400 401d8f: 80 7c 24 41 0a   cmpb   $0xa,0x41(%rsp)
1401 401d94: 0f 85 64 ff ff ff jne     401cfe <submitr+0x552>
1402 401d9a: 80 7c 24 42 00   cmpb   $0x0,0x42(%rsp)
1403 401d9f: 0f 85 59 ff ff ff jne     401cfe <submitr+0x552>
1404 401da5: ba 00 20 00 00   mov    $0x2000,%edx
1405 401daa: 48 8d 74 24 40   lea     0x40(%rsp),%rsi
1406 401daf: 48 8d bc 24 40 80 00 lea     0x8040(%rsp),%rdi
1407 401db6: 00
1408 401db7: e8 d2 f8 ff ff   callq  40168e <rio_readlineb>
1409 401dbc: 48 85 c0         test    %rax,%rax
1410 401dbf: 7f 70         jg      401e31 <submitr+0x685>
1411 401dc1: 48 b8 45 72 72 6f 72  movabs $0x43203a726f727245,%rax
1412 401dc8: 3a 20 43
1413 401dcb: 49 89 06         mov    %rax,(%r14)
1414 401dce: 48 b8 6c 69 65 6e 74  movabs $0x6e7520746e65696c,%rax
1415 401dd5: 20 75 6e
1416 401dd8: 49 89 46 08      mov    %rax,0x8(%r14)
1417 401ddc: 48 b8 61 62 6c 65 20  movabs $0x206f7420656c6261,%rax
1418 401de3: 74 6f 20
1419 401de6: 49 89 46 10      mov    %rax,0x10(%r14)
1420 401dea: 48 b8 72 65 61 64 20  movabs $0x6174732064616572,%rax
1421 401df1: 73 74 61
1422 401df4: 49 89 46 18      mov    %rax,0x18(%r14)
1423 401df8: 48 b8 74 75 73 20 6d  movabs $0x7373656d20737574,%rax
1424 401dff: 65 73 73
1425 401e02: 49 89 46 20      mov    %rax,0x20(%r14)
1426 401e06: 48 b8 61 67 65 20 66  movabs $0x6d6f726620656761,%rax
1427 401e0d: 72 6f 6d
1428 401e10: 49 89 46 28      mov    %rax,0x28(%r14)
1429 401e14: 48 b8 20 73 65 72 76  movabs $0x72657672657320,%rax
1430 401e1b: 65 72 00
1431 401e1e: 49 89 46 30      mov    %rax,0x30(%r14)
1432 401e22: 44 89 e7         mov    %r12d,%edi

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1433 401e25: e8 26 ed ff ff      callq 400b50 <close@plt>
1434 401e2a: b8 ff ff ff ff      mov    $0xffffffff,%eax
1435 401e2f: eb 37               jmp    401e68 <submitr+0x6bc>
1436 401e31: 48 8d 74 24 40      lea    0x40(%rsp),%rsi
1437 401e36: 4c 89 f7           mov    %r14,%rdi
1438 401e39: e8 c2 ec ff ff      callq 400b00 <strcpy@plt>
1439 401e3e: 44 89 e7           mov    %r12d,%edi
1440 401e41: e8 0a ed ff ff      callq 400b50 <close@plt>
1441 401e46: 41 0f b6 06        movzbl (%r14),%eax
1442 401e4a: 83 e8 4f           sub    $0x4f,%eax
1443 401e4d: 75 0f             jne    401e5e <submitr+0x6b2>
1444 401e4f: 41 0f b6 46 01      movzbl 0x1(%r14),%eax
1445 401e54: 83 e8 4b           sub    $0x4b,%eax
1446 401e57: 75 05             jne    401e5e <submitr+0x6b2>
1447 401e59: 41 0f b6 46 02      movzbl 0x2(%r14),%eax
1448 401e5e: 85 c0             test   %eax,%eax
1449 401e60: 0f 95 c0           setne  %al
1450 401e63: 0f b6 c0           movzbl %al,%eax
1451 401e66: f7 d8             neg    %eax
1452 401e68: 48 8b 94 24 58 a0 00 mov    0xa058(%rsp),%rdx
1453 401e6f: 00
1454 401e70: 64 48 33 14 25 28 00 xor     %fs:0x28,%rdx
1455 401e77: 00 00
1456 401e79: 0f 84 00 01 00 00   je     401f7f <submitr+0x7d3>
1457 401e7f: e9 f6 00 00 00     jmpq   401f7a <submitr+0x7ce>
1458 401e84: 48 8d 84 24 40 20 00 lea    0x2040(%rsp),%rax
1459 401e8b: 00
1460 401e8c: 48 89 44 24 08      mov    %rax,0x8(%rsp)
1461 401e91: 4c 89 3c 24         mov    %r15, (%rsp)
1462 401e95: 4c 8b 4c 24 18      mov    0x18(%rsp),%r9
1463 401e9a: 4c 8b 44 24 10      mov    0x10(%rsp),%r8
1464 401e9f: b9 d0 26 40 00      mov    $0x4026d0,%ecx
1465 401ea4: ba 00 20 00 00      mov    $0x2000,%edx
1466 401ea9: be 01 00 00 00      mov    $0x1,%esi
1467 401eae: 48 8d 7c 24 40      lea    0x40(%rsp),%rdi
1468 401eb3: b8 00 00 00 00      mov    $0x0,%eax
1469 401eb8: e8 b3 ed ff ff      callq 400c70 <__sprintf_chk@plt>
1470 401ebd: 48 8d 7c 24 40      lea    0x40(%rsp),%rdi
1471 401ec2: b8 00 00 00 00      mov    $0x0,%eax
1472 401ec7: 48 c7 c1 ff ff ff ff mov    $0xffffffffffffffff,%rcx
1473 401ece: f2 ae             repnz  scas %es:(%rdi),%al
1474 401ed0: 48 f7 d1           not    %rcx
1475 401ed3: 48 83 e9 01        sub    $0x1,%rcx
1476 401ed7: 49 89 cd           mov    %rcx,%r13
1477 401eda: 0f 84 f8 fc ff ff   je     401bd8 <submitr+0x42c>
1478 401ee0: 48 89 cb           mov    %rcx,%rbx
1479 401ee3: 48 8d 6c 24 40      lea    0x40(%rsp),%rbp
1480 401ee8: e9 5d fc ff ff      jmpq   401b4a <submitr+0x39e>
1481 401eed: 48 b8 45 72 72 6f 72 movabs $0x52203a726f727245,%rax
1482 401ef4: 3a 20 52
1483 401ef7: 49 89 06           mov    %rax, (%r14)
1484 401efa: 48 b8 65 73 75 6c 74 movabs $0x747320746c757365,%rax
1485 401f01: 20 73 74

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1486 401f04: 49 89 46 08      mov    %rax,0x8(%r14)
1487 401f08: 48 b8 72 69 6e 67 20  movabs $0x6e6f6320676e6972,%rax
1488 401f0f: 63 6f 6e
1489 401f12: 49 89 46 10      mov    %rax,0x10(%r14)
1490 401f16: 48 b8 74 61 69 6e 73  movabs $0x6e6120736e696174,%rax
1491 401f1d: 20 61 6e
1492 401f20: 49 89 46 18      mov    %rax,0x18(%r14)
1493 401f24: 48 b8 20 69 6c 6c 65  movabs $0x6c6167656c6c6920,%rax
1494 401f2b: 67 61 6c
1495 401f2e: 49 89 46 20      mov    %rax,0x20(%r14)
1496 401f32: 48 b8 20 6f 72 20 75  movabs $0x72706e7520726f20,%rax
1497 401f39: 6e 70 72
1498 401f3c: 49 89 46 28      mov    %rax,0x28(%r14)
1499 401f40: 48 b8 69 6e 74 61 62  movabs $0x20656c6261746e69,%rax
1500 401f47: 6c 65 20
1501 401f4a: 49 89 46 30      mov    %rax,0x30(%r14)
1502 401f4e: 48 b8 63 68 61 72 61  movabs $0x6574636172616863,%rax
1503 401f55: 63 74 65
1504 401f58: 49 89 46 38      mov    %rax,0x38(%r14)
1505 401f5c: 66 41 c7 46 40 72 2e  movw   $0x2e72,0x40(%r14)
1506 401f63: 41 c6 46 42 00    movb   $0x0,0x42(%r14)
1507 401f68: 44 89 e7          mov    %r12d,%edi
1508 401f6b: e8 e0 eb ff ff    callq  400b50 <close@plt>
1509 401f70: b8 ff ff ff ff    mov     $0xffffffff,%eax
1510 401f75: e9 ee fe ff ff    jmpq   401e68 <submitr+0x6bc>
1511 401f7a: e8 b1 eb ff ff    callq  400b30 <__stack_chk_fail@plt>
1512 401f7f: 48 81 c4 68 a0 00 00  add     $0xa068,%rsp
1513 401f86: 5b               pop     %rbx
1514 401f87: 5d               pop     %rbp
1515 401f88: 41 5c            pop     %r12
1516 401f8a: 41 5d            pop     %r13
1517 401f8c: 41 5e            pop     %r14
1518 401f8e: 41 5f            pop     %r15
1519 401f90: c3              retq
1520
1521 0000000000401f91 <init_timeout>:
1522 401f91: 53              push    %rbx
1523 401f92: 89 fb           mov     %edi,%ebx
1524 401f94: 85 ff           test    %edi,%edi
1525 401f96: 74 1e           je      401fb6 <init_timeout+0x25>
1526 401f98: be 60 16 40 00  mov     $0x401660,%esi
1527 401f9d: bf 0e 00 00 00  mov     $0xe,%edi
1528 401fa2: e8 e9 eb ff ff  callq   400b90 <signal@plt>
1529 401fa7: 85 db           test    %ebx,%ebx
1530 401fa9: bf 00 00 00 00  mov     $0x0,%edi
1531 401fae: 0f 49 fb         cmovns  %ebx,%edi
1532 401fb1: e8 8a eb ff ff  callq   400b40 <alarm@plt>
1533 401fb6: 5b              pop     %rbx
1534 401fb7: c3              retq
1535
1536 0000000000401fb8 <init_driver>:
1537 401fb8: 55              push    %rbp
1538 401fb9: 53              push    %rbx

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1539 401fba: 48 83 ec 28          sub    $0x28,%rsp
1540 401fbe: 48 89 fd            mov    %rdi,%rbp
1541 401fc1: 64 48 8b 04 25 28 00 mov    %fs:0x28,%rax
1542 401fc8: 00 00
1543 401fca: 48 89 44 24 18      mov    %rax,0x18(%rsp)
1544 401fcf: 31 c0              xor    %eax,%eax
1545 401fd1: be 01 00 00 00      mov    $0x1,%esi
1546 401fd6: bf 0d 00 00 00      mov    $0xd,%edi
1547 401fdb: e8 b0 eb ff ff      callq 400b90 <signal@plt>
1548 401fe0: be 01 00 00 00      mov    $0x1,%esi
1549 401fe5: bf 1d 00 00 00      mov    $0x1d,%edi
1550 401fea: e8 a1 eb ff ff      callq 400b90 <signal@plt>
1551 401fef: be 01 00 00 00      mov    $0x1,%esi
1552 401ff4: bf 1d 00 00 00      mov    $0x1d,%edi
1553 401ff9: e8 92 eb ff ff      callq 400b90 <signal@plt>
1554 401ffe: ba 00 00 00 00      mov    $0x0,%edx
1555 402003: be 01 00 00 00      mov    $0x1,%esi
1556 402008: bf 02 00 00 00      mov    $0x2,%edi
1557 40200d: e8 6e ec ff ff      callq 400c80 <socket@plt>
1558 402012: 89 c3              mov    %eax,%ebx
1559 402014: 85 c0              test   %eax,%eax
1560 402016: 79 4f              jns    402067 <init_driver+0xaf>
1561 402018: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax
1562 40201f: 3a 20 43
1563 402022: 48 89 45 00          mov    %rax,0x0(%rbp)
1564 402026: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax
1565 40202d: 20 75 6e
1566 402030: 48 89 45 08          mov    %rax,0x8(%rbp)
1567 402034: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax
1568 40203b: 74 6f 20
1569 40203e: 48 89 45 10          mov    %rax,0x10(%rbp)
1570 402042: 48 b8 63 72 65 61 74 movabs $0x7320657461657263,%rax
1571 402049: 65 20 73
1572 40204c: 48 89 45 18          mov    %rax,0x18(%rbp)
1573 402050: c7 45 20 6f 63 6b 65 movl    $0x656b636f,0x20(%rbp)
1574 402057: 66 c7 45 24 74 00    movw    $0x74,0x24(%rbp)
1575 40205d: b8 ff ff ff ff      mov    $0xffffffff,%eax
1576 402062: e9 0a 01 00 00      jmpq    402171 <init_driver+0x1b9>
1577 402067: bf 60 27 40 00      mov    $0x402760,%edi
1578 40206c: e8 2f eb ff ff      callq 400ba0 <gethostbyname@plt>
1579 402071: 48 85 c0              test   %rax,%rax
1580 402074: 75 68              jne    4020de <init_driver+0x126>
1581 402076: 48 b8 45 72 72 6f 72 movabs $0x44203a726f727245,%rax
1582 40207d: 3a 20 44
1583 402080: 48 89 45 00          mov    %rax,0x0(%rbp)
1584 402084: 48 b8 4e 53 20 69 73 movabs $0x6e7520736920534e,%rax
1585 40208b: 20 75 6e
1586 40208e: 48 89 45 08          mov    %rax,0x8(%rbp)
1587 402092: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax
1588 402099: 74 6f 20
1589 40209c: 48 89 45 10          mov    %rax,0x10(%rbp)
1590 4020a0: 48 b8 72 65 73 6f 6c movabs $0x2065766c6f736572,%rax
1591 4020a7: 76 65 20

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1592 4020aa: 48 89 45 18      mov    %rax,0x18(%rbp)
1593 4020ae: 48 b8 73 65 72 76 65  movabs $0x6120726576726573,%rax
1594 4020b5: 72 20 61
1595 4020b8: 48 89 45 20      mov    %rax,0x20(%rbp)
1596 4020bc: c7 45 28 64 64 72 65  movl   $0x65726464,0x28(%rbp)
1597 4020c3: 66 c7 45 2c 73 73  movw   $0x7373,0x2c(%rbp)
1598 4020c9: c6 45 2e 00      movb   $0x0,0x2e(%rbp)
1599 4020cd: 89 df           mov    %ebx,%edi
1600 4020cf: e8 7c ea ff ff   callq  400b50 <close@plt>
1601 4020d4: b8 ff ff ff ff   mov    $0xffffffff,%eax
1602 4020d9: e9 93 00 00 00   jmpq   402171 <init_driver+0x1b9>
1603 4020de: 48 c7 04 24 00 00 00  movq   $0x0,(%rsp)
1604 4020e5: 00
1605 4020e6: 48 c7 44 24 08 00 00  movq   $0x0,0x8(%rsp)
1606 4020ed: 00 00
1607 4020ef: 66 c7 04 24 02 00  movw   $0x2,(%rsp)
1608 4020f5: 48 63 50 14      movslq 0x14(%rax),%rdx
1609 4020f9: 48 8b 40 18      mov    0x18(%rax),%rax
1610 4020fd: 48 8d 7c 24 04   lea    0x4(%rsp),%rdi
1611 402102: b9 0c 00 00 00   mov    $0xc,%ecx
1612 402107: 48 8b 30         mov    (%rax),%rsi
1613 40210a: e8 a1 ea ff ff   callq  400bb0 <__memmove_chk@plt>
1614 40210f: 66 c7 44 24 02 3b 6e  movw   $0x6e3b,0x2(%rsp)
1615 402116: ba 10 00 00 00   mov    $0x10,%edx
1616 40211b: 48 89 e6         mov    %rsp,%rsi
1617 40211e: 89 df           mov    %ebx,%edi
1618 402120: e8 0b eb ff ff   callq  400c30 <connect@plt>
1619 402125: 85 c0           test   %eax,%eax
1620 402127: 79 32           jns    40215b <init_driver+0x1a3>
1621 402129: 41 b8 60 27 40 00  mov    $0x402760,%r8d
1622 40212f: b9 20 27 40 00   mov    $0x402720,%ecx
1623 402134: 48 c7 c2 ff ff ff ff  mov    $0xffffffffffffffff,%rdx
1624 40213b: be 01 00 00 00   mov    $0x1,%esi
1625 402140: 48 89 ef         mov    %rbp,%rdi
1626 402143: b8 00 00 00 00   mov    $0x0,%eax
1627 402148: e8 23 eb ff ff   callq  400c70 <__sprintf_chk@plt>
1628 40214d: 89 df           mov    %ebx,%edi
1629 40214f: e8 fc e9 ff ff   callq  400b50 <close@plt>
1630 402154: b8 ff ff ff ff   mov    $0xffffffff,%eax
1631 402159: eb 16           jmp     402171 <init_driver+0x1b9>
1632 40215b: 89 df           mov    %ebx,%edi
1633 40215d: e8 ee e9 ff ff   callq  400b50 <close@plt>
1634 402162: 66 c7 45 00 4f 4b   movw   $0x4b4f,0x0(%rbp)
1635 402168: c6 45 02 00      movb   $0x0,0x2(%rbp)
1636 40216c: b8 00 00 00 00   mov    $0x0,%eax
1637 402171: 48 8b 4c 24 18   mov    0x18(%rsp),%rcx
1638 402176: 64 48 33 0c 25 28 00  xor    %fs:0x28,%rcx
1639 40217d: 00 00
1640 40217f: 74 05           je     402186 <init_driver+0x1ce>
1641 402181: e8 aa e9 ff ff   callq  400b30 <__stack_chk_fail@plt>
1642 402186: 48 83 c4 28      add    $0x28,%rsp
1643 40218a: 5b             pop    %rbx
1644 40218b: 5d             pop    %rbp

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1645  40218c:  c3                      retq
1646
1647  000000000040218d <driver_post>:
1648  40218d:  53                      push   %rbx
1649  40218e:  48 83 ec 10             sub    $0x10,%rsp
1650  402192:  48 89 cb                mov    %rcx,%rbx
1651  402195:  85 d2                   test   %edx,%edx
1652  402197:  74 27                   je     4021c0 <driver_post+0x33>
1653  402199:  48 89 f2                mov    %rsi,%rdx
1654  40219c:  be 78 27 40 00          mov    $0x402778,%esi
1655  4021a1:  bf 01 00 00 00          mov    $0x1,%edi
1656  4021a6:  b8 00 00 00 00          mov    $0x0,%eax
1657  4021ab:  e8 50 ea ff ff          callq  400c00 <__printf_chk@plt>
1658  4021b0:  66 c7 03 4f 4b          movw   $0x4b4f, (%rbx)
1659  4021b5:  c6 43 02 00             movb   $0x0, 0x2(%rbx)
1660  4021b9:  b8 00 00 00 00          mov    $0x0,%eax
1661  4021be:  eb 3e                   jmp     4021fe <driver_post+0x71>
1662  4021c0:  48 85 ff                test   %rdi,%rdi
1663  4021c3:  74 2b                   je     4021f0 <driver_post+0x63>
1664  4021c5:  80 3f 00                cmpb   $0x0, (%rdi)
1665  4021c8:  74 26                   je     4021f0 <driver_post+0x63>
1666  4021ca:  48 89 0c 24             mov    %rcx, (%rsp)
1667  4021ce:  49 89 f1                mov    %rsi,%r9
1668  4021d1:  41 b8 ec 22 40 00       mov    $0x4022ec,%r8d
1669  4021d7:  48 89 f9                mov    %rdi,%rcx
1670  4021da:  ba 8f 27 40 00          mov    $0x40278f,%edx
1671  4021df:  be 6e 3b 00 00          mov    $0x3b6e,%esi
1672  4021e4:  bf 60 27 40 00          mov    $0x402760,%edi
1673  4021e9:  e8 be f5 ff ff          callq  4017ac <submitr>
1674  4021ee:  eb 0e                   jmp     4021fe <driver_post+0x71>
1675  4021f0:  66 c7 03 4f 4b          movw   $0x4b4f, (%rbx)
1676  4021f5:  c6 43 02 00             movb   $0x0, 0x2(%rbx)
1677  4021f9:  b8 00 00 00 00          mov    $0x0,%eax
1678  4021fe:  48 83 c4 10             add    $0x10,%rsp
1679  402202:  5b                      pop     %rbx
1680  402203:  c3                      retq
1681  402204:  90                      nop
1682  402205:  90                      nop
1683  402206:  90                      nop
1684  402207:  90                      nop
1685  402208:  90                      nop
1686  402209:  90                      nop
1687  40220a:  90                      nop
1688  40220b:  90                      nop
1689  40220c:  90                      nop
1690  40220d:  90                      nop
1691  40220e:  90                      nop
1692  40220f:  90                      nop
1693
1694  0000000000402210 <__libc_csu_init>:
1695  402210:  48 89 6c 24 d8          mov    %rbp, -0x28(%rsp)
1696  402215:  4c 89 64 24 e0          mov    %r12, -0x20(%rsp)
1697  40221a:  48 8d 2d df 0b 20 00     lea    0x200bdf(%rip),%rbp

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```

        602e00 <__do_global_dtors_aux_fini_array_entry>
1698  402221:  4c 8d 25 d0 0b 20 00    lea    0x200bd0(%rip),%r12    #  ↗
        602df8 <__frame_dummy_init_array_entry>
1699  402228:  4c 89 6c 24 e8          mov     %r13,-0x18(%rsp)
1700  40222d:  4c 89 74 24 f0          mov     %r14,-0x10(%rsp)
1701  402232:  4c 89 7c 24 f8          mov     %r15,-0x8(%rsp)
1702  402237:  48 89 5c 24 d0          mov     %rbx,-0x30(%rsp)
1703  40223c:  48 83 ec 38             sub     $0x38,%rsp
1704  402240:  4c 29 e5                sub     %r12,%rbp
1705  402243:  41 89 fd                mov     %edi,%r13d
1706  402246:  49 89 f6                mov     %rsi,%r14
1707  402249:  48 c1 fd 03             sar     $0x3,%rbp
1708  40224d:  49 89 d7                mov     %rdx,%r15
1709  402250:  e8 6b e8 ff ff          callq   400ac0 <_init>
1710  402255:  48 85 ed                test    %rbp,%rbp
1711  402258:  74 1c                  je      402276 <__libc_csu_init+0x66>
1712  40225a:  31 db                  xor     %ebx,%ebx
1713  40225c:  0f 1f 40 00            nopl    0x0(%rax)
1714  402260:  4c 89 fa                mov     %r15,%rdx
1715  402263:  4c 89 f6                mov     %r14,%rsi
1716  402266:  44 89 ef                mov     %r13d,%edi
1717  402269:  41 ff 14 dc             callq   *(%r12,%rbx,8)
1718  40226d:  48 83 c3 01            add     $0x1,%rbx
1719  402271:  48 39 eb                cmp     %rbp,%rbx
1720  402274:  75 ea                  jne     402260 <__libc_csu_init+0x50>
1721  402276:  48 8b 5c 24 08          mov     0x8(%rsp),%rbx
1722  40227b:  48 8b 6c 24 10          mov     0x10(%rsp),%rbp
1723  402280:  4c 8b 64 24 18          mov     0x18(%rsp),%r12
1724  402285:  4c 8b 6c 24 20          mov     0x20(%rsp),%r13
1725  40228a:  4c 8b 74 24 28          mov     0x28(%rsp),%r14
1726  40228f:  4c 8b 7c 24 30          mov     0x30(%rsp),%r15
1727  402294:  48 83 c4 38             add     $0x38,%rsp
1728  402298:  c3                     retq
1729  402299:  0f 1f 80 00 00 00 00    nopl    0x0(%rax)
1730
1731  00000000004022a0 <__libc_csu_fini>:
1732  4022a0:  f3 c3                  repz retq
1733  4022a2:  90                     nop
1734  4022a3:  90                     nop
1735
1736  Disassembly of section .fini:
1737
1738  00000000004022a4 <_fini>:
1739  4022a4:  48 83 ec 08             sub     $0x8,%rsp
1740  4022a8:  48 83 c4 08             add     $0x8,%rsp
1741  4022ac:  c3                     retq
1742

```