

# **CYE-STM32F4-C**

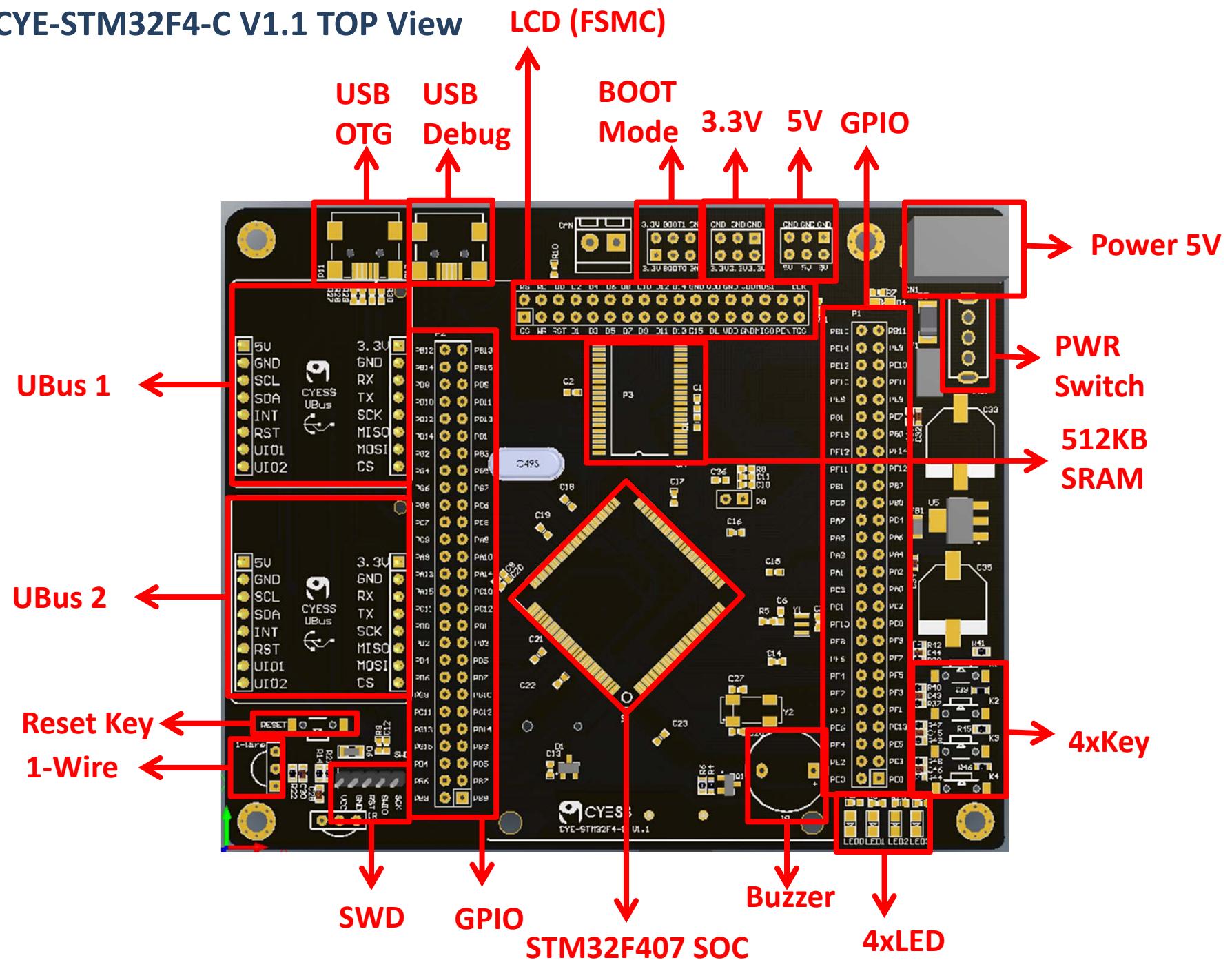
## **Peripheral Pin Define**

Howard Lin

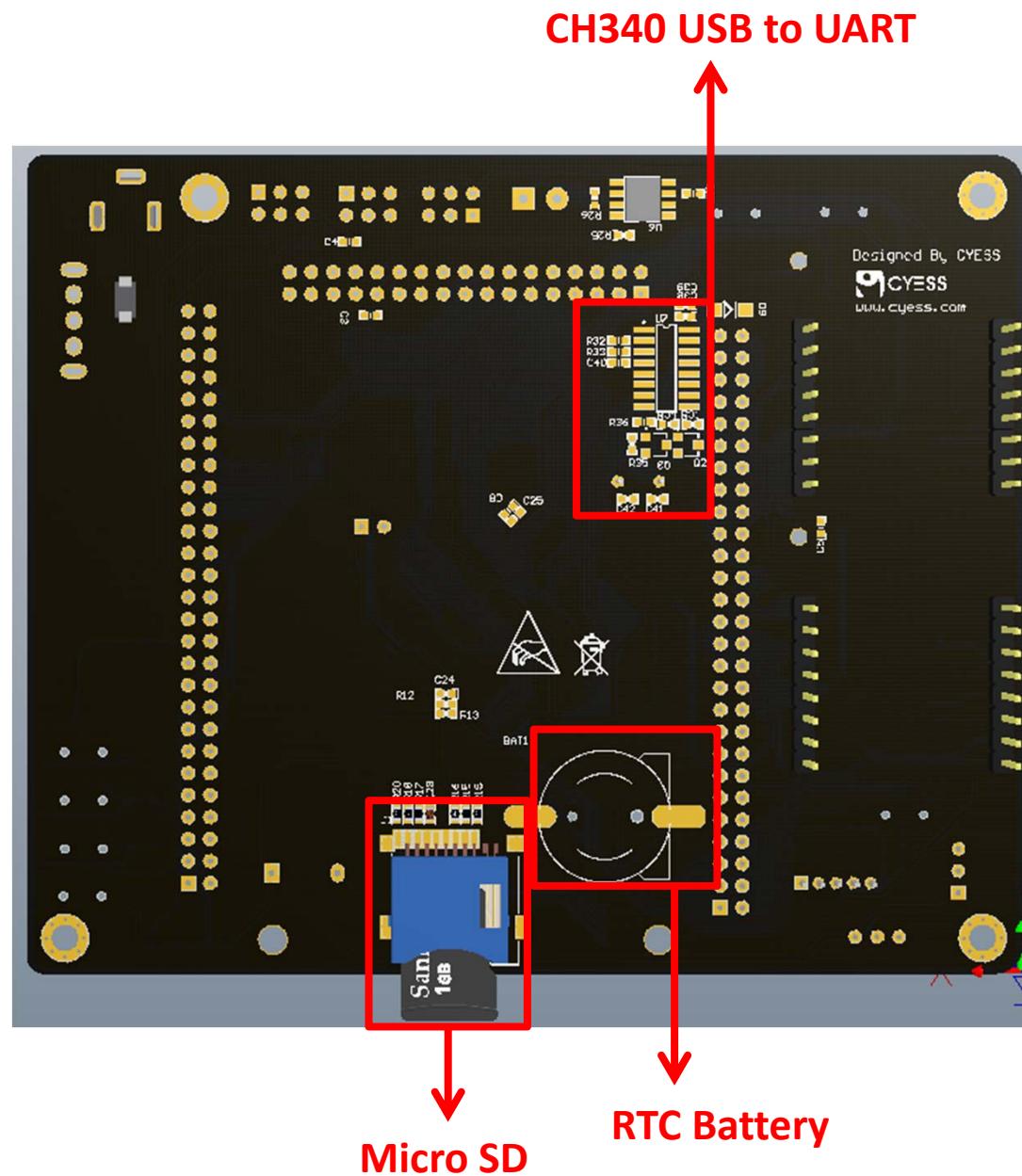
[www.cyess.com](http://www.cyess.com)

howard.lin@cyess.com

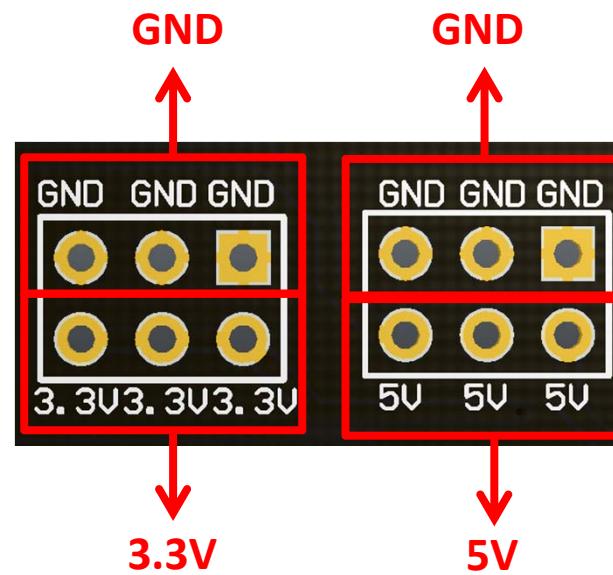
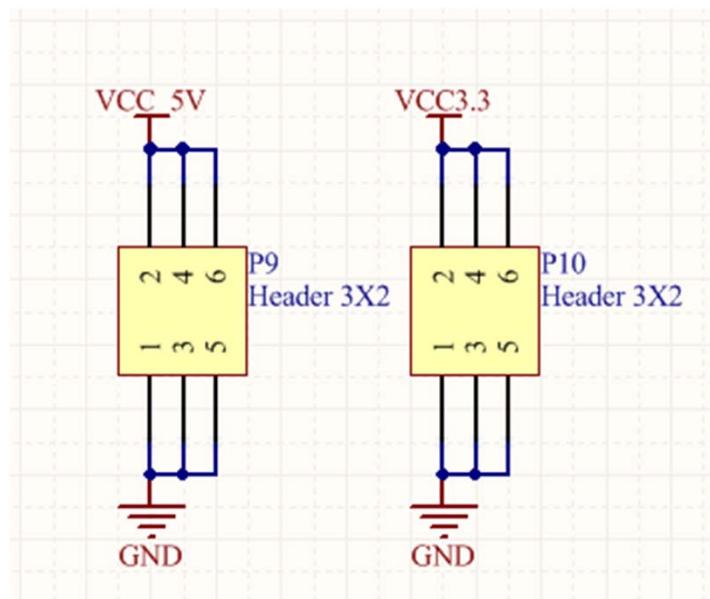
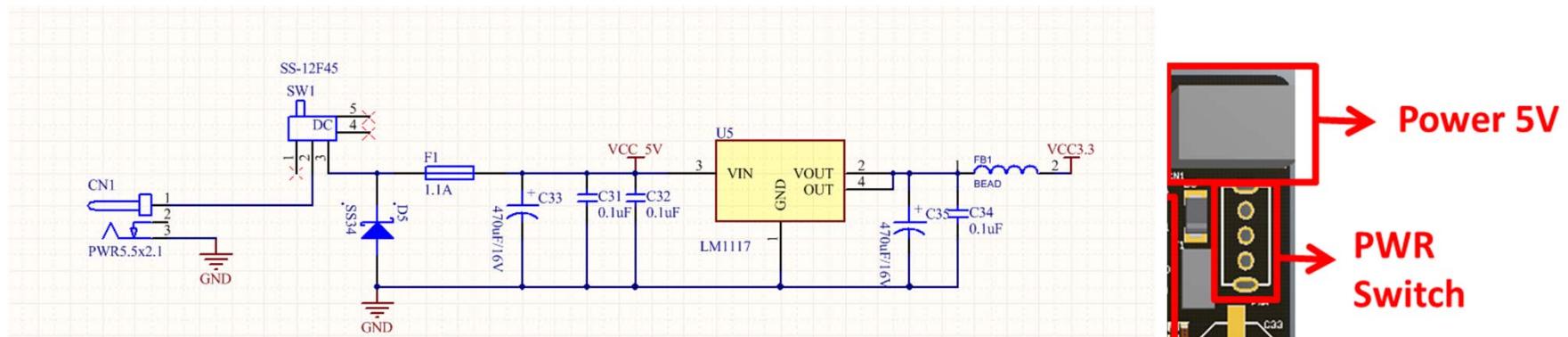
# CYE-STM32F4-C V1.1 TOP View



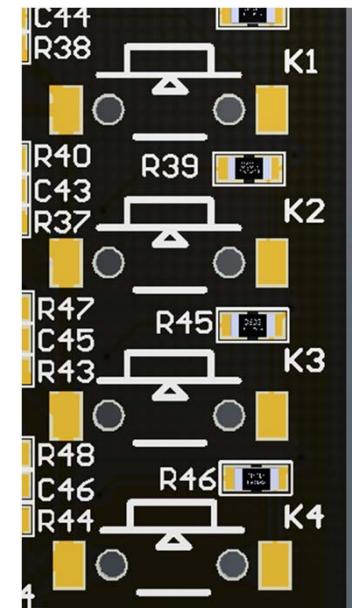
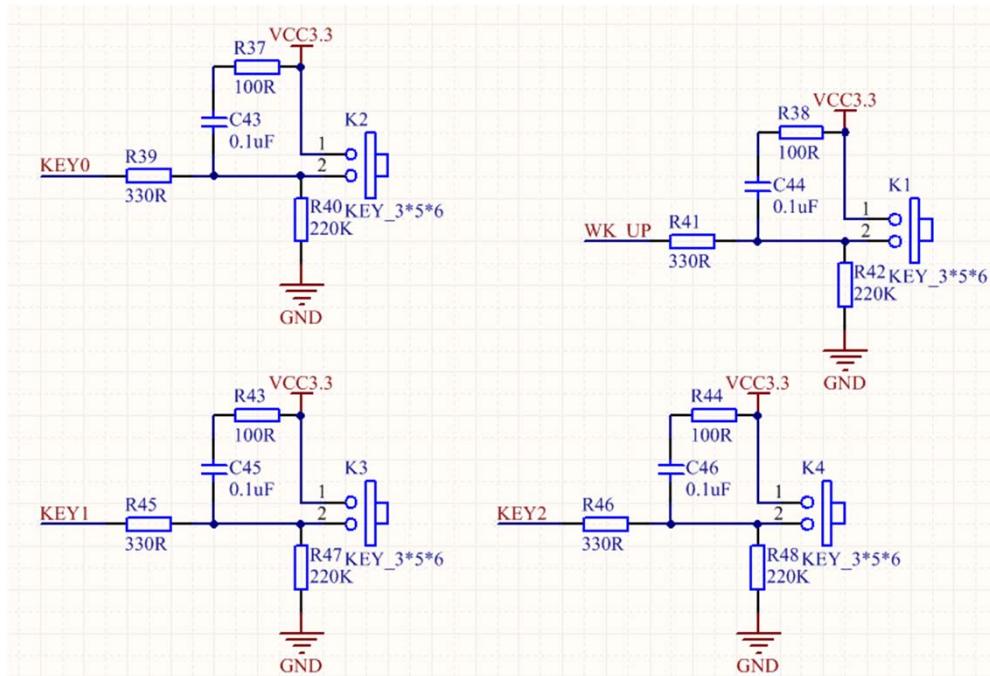
## CYE-STM32F4-C V1.1 Bottom View



# CYE-STM32F4-C V1.1 Power



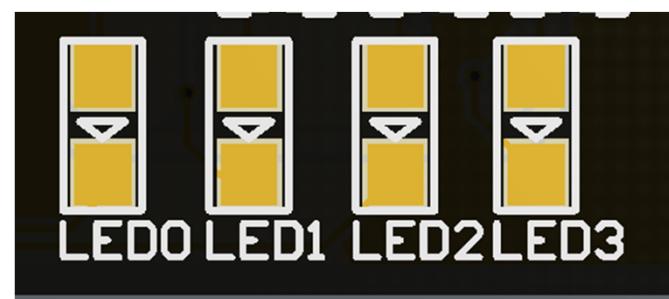
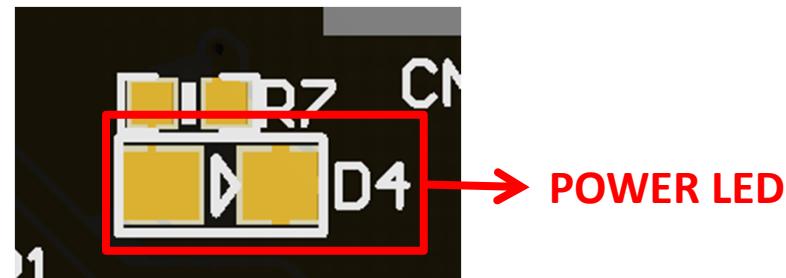
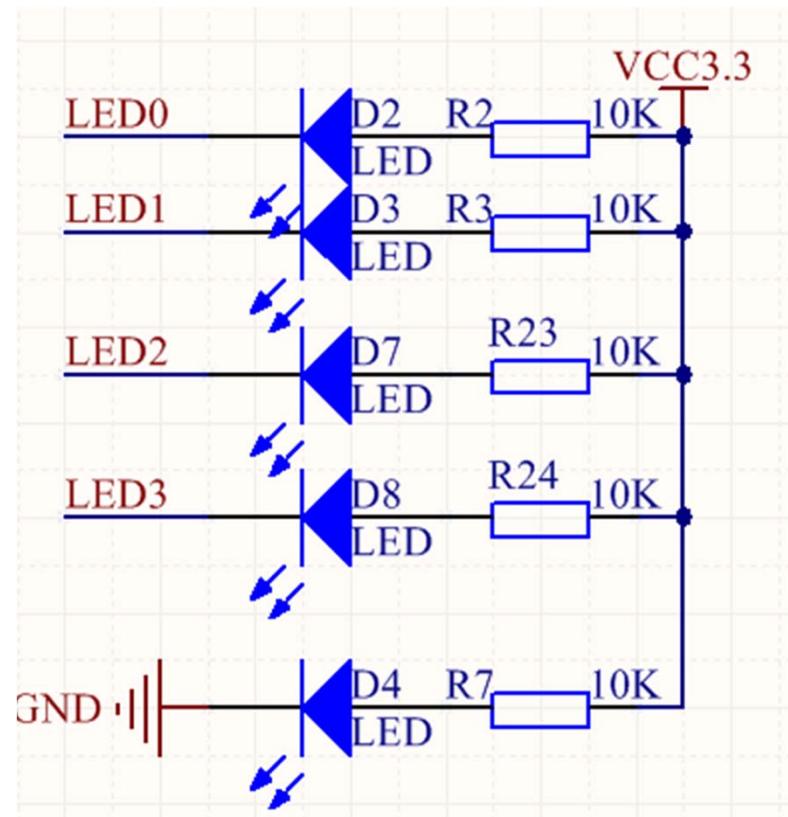
# CYE-STM32F4-C V1.1 KEY



PE1/FSMC_NBL1/DCMI_D5	1	PE2	KEY2
PE2/TRACECLK/FSMC_A23/ETH_MII_TXD3	2	PE3	KEY1
PE3/TRACED0/FSMC_A19	3	PE4	KEY0
PE4/TRACED1/FSMC_A20/DCMI_D4			

U1  
 WK\_UP PA0 34 PA0-WKUP/U2\_CTS/U4\_TX/ETH\_MII CRS/TIM2\_CH1\_ETR/TIM5\_CH1/TIM8\_ETR/ADC123\_IN0  
 STM ADC PA1 35

## CYE-STM32F4-C V1.1 LED



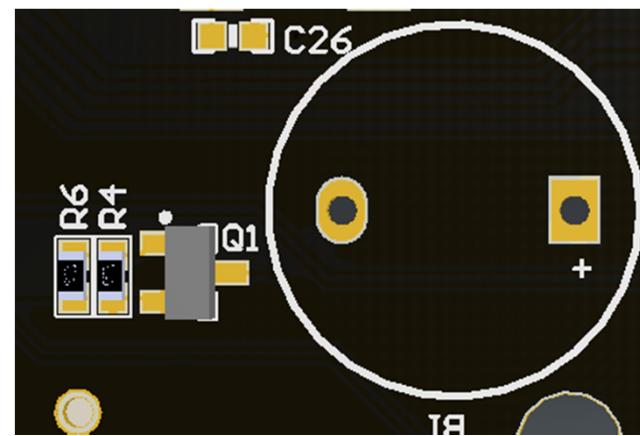
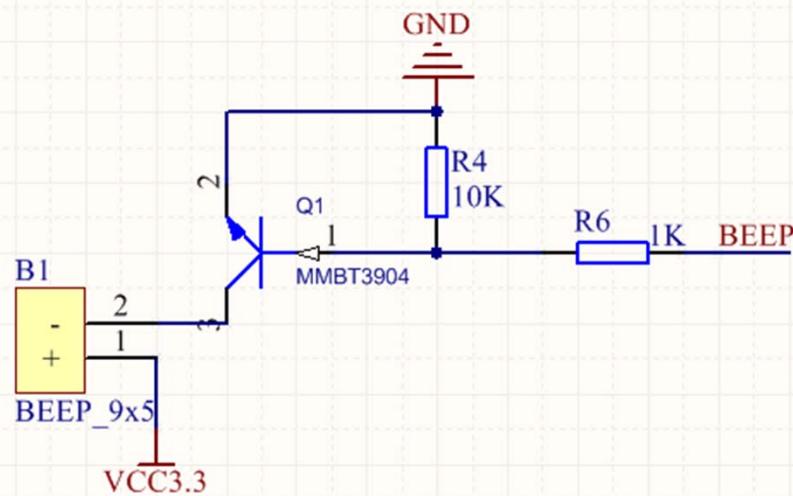
PE5	PE6	PE7	PE8	PE9
LED0	BB2	I32	BB3/FSMC A21/TIM9 CH1/DCMI D6	
PE5	BB4	I34	BB5/TIM10 CH1/FSMC NIORD/ADC3 IN4	

PE4/TRACE	PE5/FSMC A20/DCMI D4	4	PE5	LED1
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PE10/TZSS	SDT1/TSMC D5/DWUS H12	18	PF6	LED3
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## CYE-STM32F4-C V1.1 Buzzer

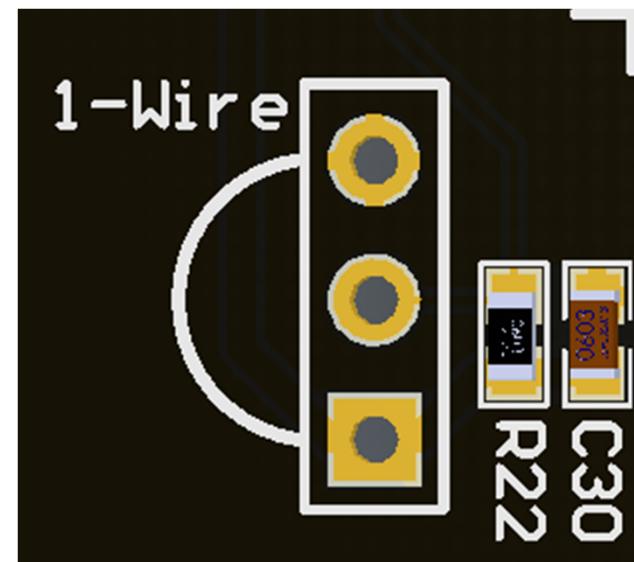
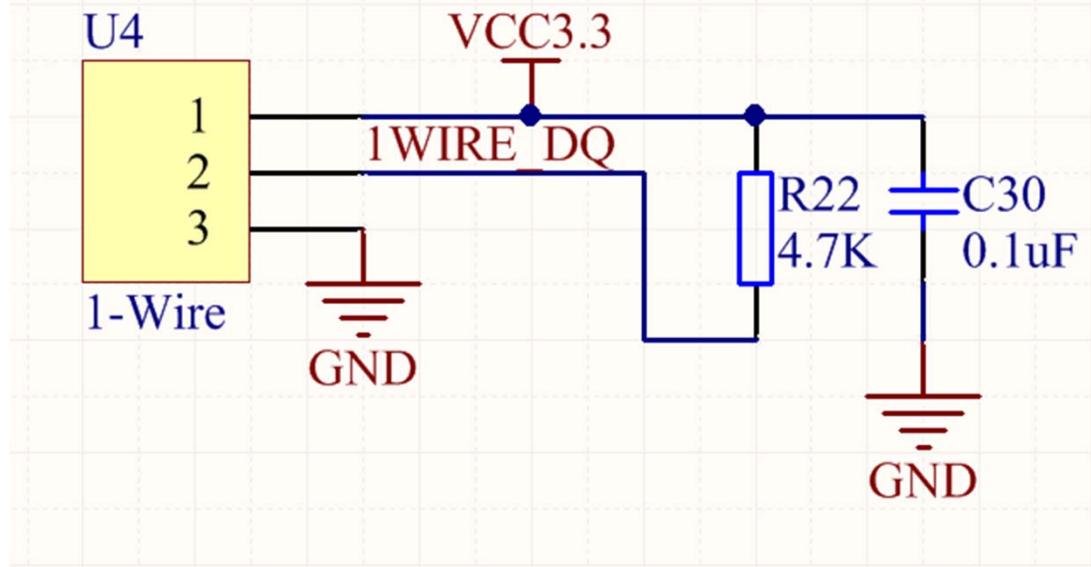
# Buzzer



BEEP	PB3	133	PB2/BOOT1
LED2	DD4	124	PB3/JTDO/TRACESWO/TIM2_CH2/SPI1_SCK/SPI3_SCK/I2S3_CK

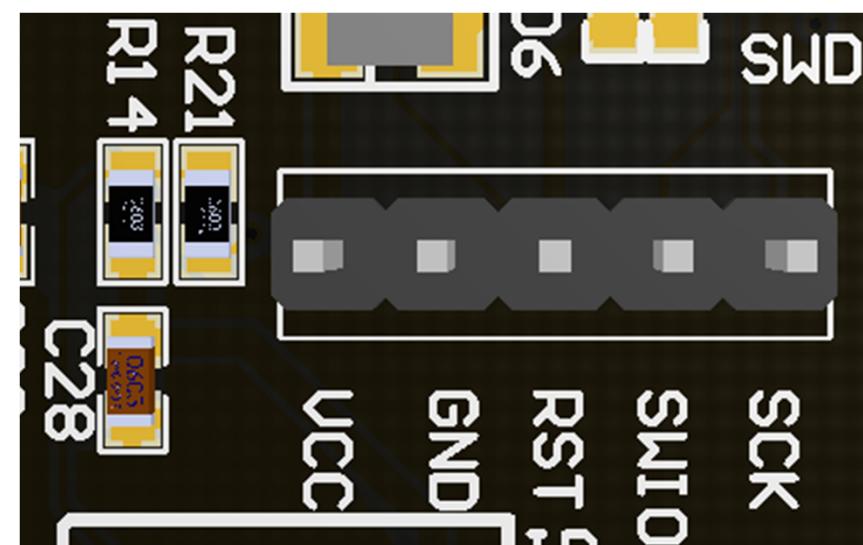
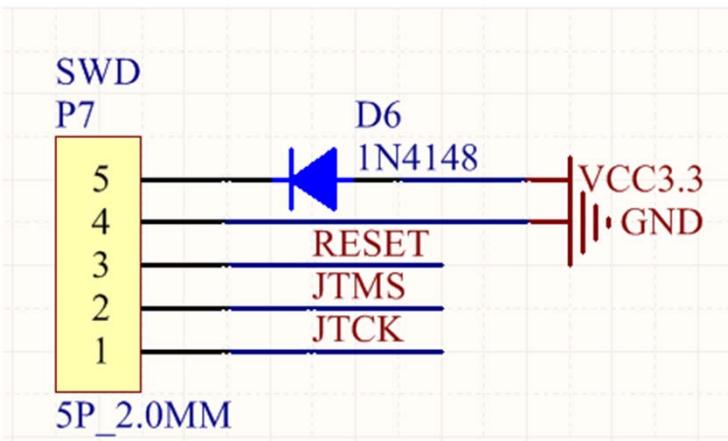
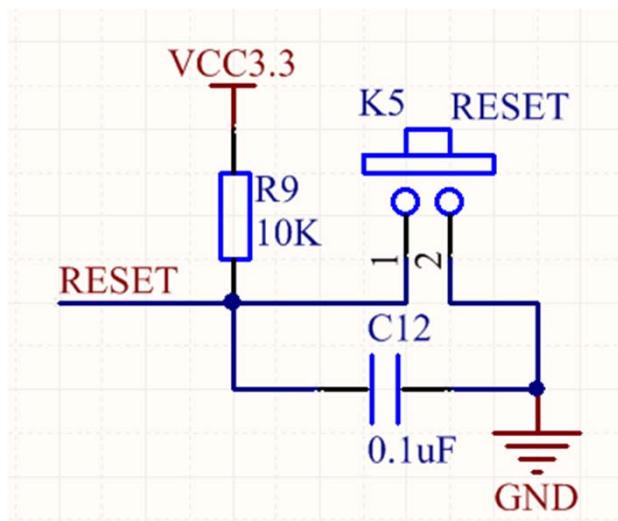
## CYE-STM32F4-C V1.1 1-Wire

# 1-Wire



PG10/FSMC_NCE4_1/FSMC_NE3	126	PG11	1WIRE_DQ
PG11/FSMC_NCE4_2/ETH_MII_TX_EN/ETH_RMII_TX_EN	127	PG12	FSMC_NE4

## CYE-STM32F4-C V1.1 SWD & Reset



JTMS	PA13 105	PA12/TIM1_ETR/OT_R
JTCK	PA14 109	PA13/JTMS_SWDIO
SD_DET#	PA15 110	PA14/JTCK_SWCLK

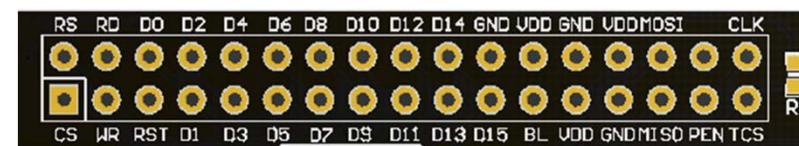
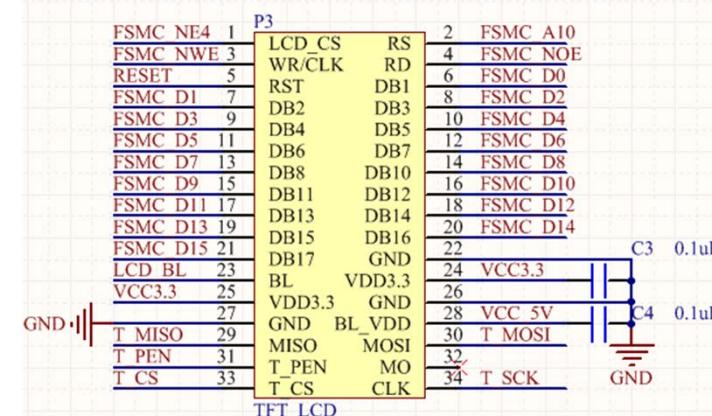
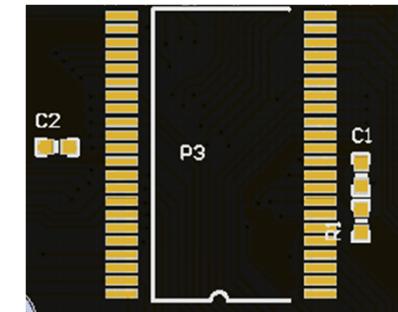
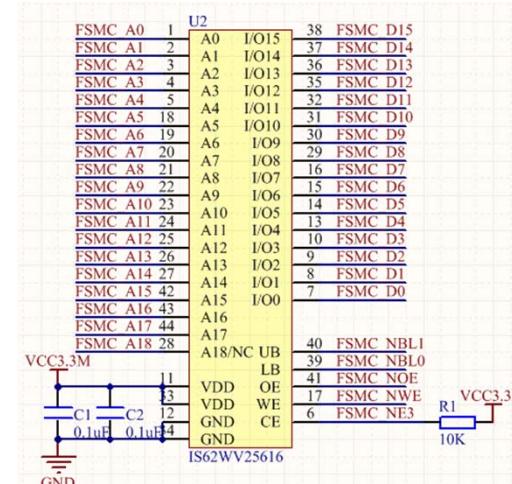


# CYE-STM32F4-C V1.1 FSMC (LCD, NAND Flash,SRAM)

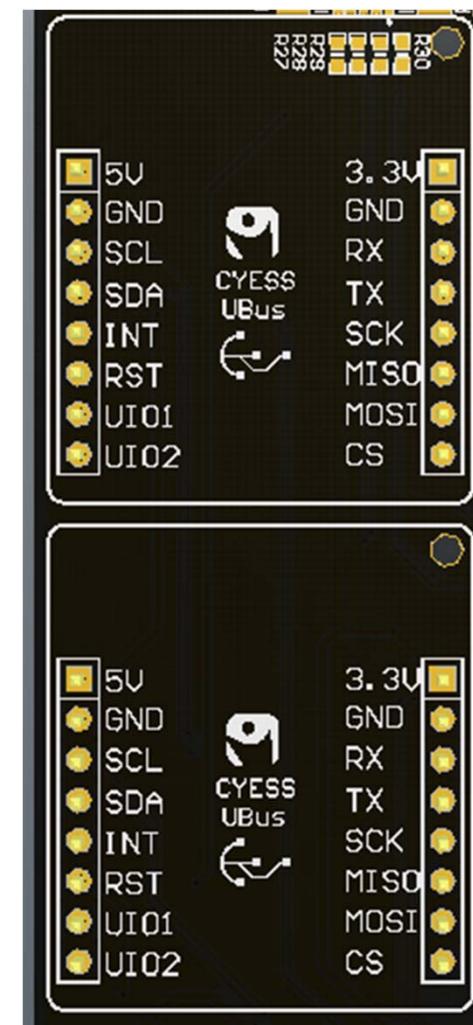
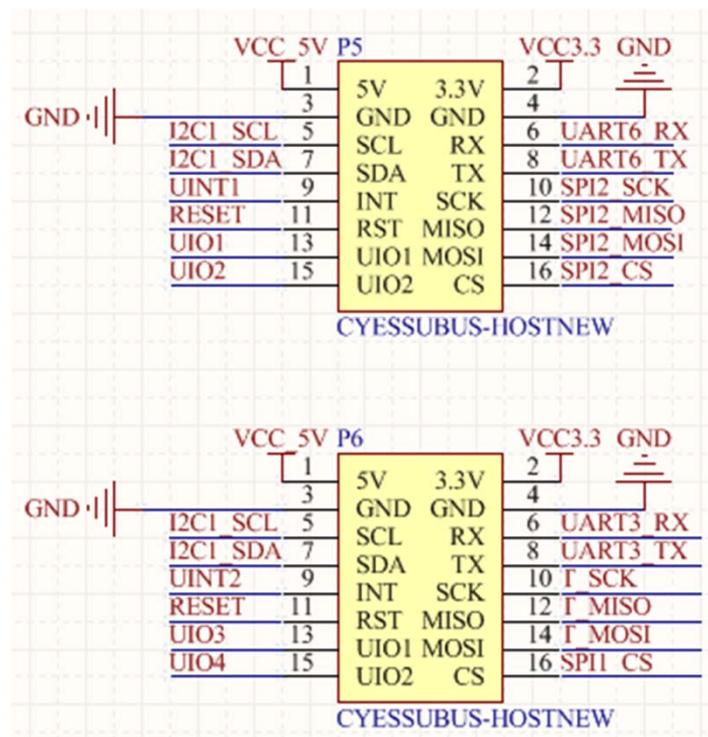
IC123_IN0	PE0/TIM4_ETR/FSMC_NBL0/DCMI_D2
ADC123_IN1	PE1/FSMC_NBL1/DCMI_D3
	PE2/TRACECLK/FSMC_A23/ETH_MII_TXD3
I2S_IN3	PE3/TRACEDE0/FSMC_A19
I5S	PE4/TRADED1/FSMC_A20/DCMI_D4
I2S_IN6	PE5/TRADED2/FSMC_A21/TIM9_CH1/DCMI_D6
ETH_RMII_CRS_DV/ADC12_IN7	PE6/TRADED3/FSMC_A22/TIM9_CH2/DCMI_D7
	PE7/FSMC_D5/TIM1_CH1
	PE8/FSMC_D5/TIM1_CH1
	PE9/FSMC_D6/TIM1_CH1
	PE10/FSMC_D7/TIM1_CH2N
	PE11/FSMC_D8/TIM1_CH2
	PE12/FSMC_D9/TIM1_CH3N
	PE13/FSMC_D10/TIM1_CH3
	PE14/FSMC_D11/TIM1_CH4
	PE15/FSMC_D12/TIM1_BKIN
JN8	PF0/FSMC_A0/I2C2_SDA
	PF1/FSMC_A1/I2C2_SCL
	PF2/FSMC_A2/SMBA
	PF3/FSMC_A3/ADC3_IN9
	PF4/FSMC_A4/ADC3_IN14
H_PPS_OUT/DCMI_D10/I2S3_SDPP5/FSMC_A5/ADC3_IN15	PF5/FSMC_A5
PF6/TIM10_CH1/FSMC_NIORD/ADC3_IN4	PF6
PF7/TIM11_CH1/FSMC_NIREG/ADC3_IN5	PF7
PF8/TIM13_CH1/FSMC_NIOWR/ADC3_IN6	PF8
PF9/TIM14_CH1/FSMC_CD/ADC3_IN7	PF9
PF10/FSMC_INTR/ADC3_IN8	PF10
	PF11/DCMI_D12
IS_ID/ETH_MII_TXD0/ETH_RMII_TXD0/2SP_NFSMC_A6	PF12/FSMC_A6
ETH_MII_TXD1/ETH_RMII_TXD1/I2S2_CK	PF13/FSMC_A7
SD	PF14/FSMC_A8
	PF15/FSMC_A9
	PF16/FSMC_A10
	PG1/FSMC_A11
	PG2/FSMC_A12
	PG3/FSMC_A13
	PG4/FSMC_A14
	PG5/FSMC_A15
	PG6/FSMC_INT2
	PG7/FSMC_INT3/U6_C1
	PG8/U6_RTS/ETH_PPS_OUT
PG9/FSMC_NE2/FSMC_NCE3/U6_RX	PG9
PG10/FSMC_NCE4_1/FSMC_NE3	PG10
PG11/FSMC_NCE4_2/ETH_MII_TX_EN/ETH_RMII_TX_EN	PG11
	PG12/FSMC_NE4/T16_RTS

FSMC_D2	PD0	114
FSMC_D3	PD1	115
SD_CS	SDIO_CMD	PD2
	PD3	116
	PD4	117
FSMC_NOE	PD4	118
FSMC_NWEPD5	PD5	119
	PD6	122
	PD7	123
FSMC_D13	PD8	77
FSMC_D14	PD9	78
FSMC_D15	PD10	79
FSMC_A16	PD11	80
FSMC_A17	PD12	81
FSMC_A18	PD13	82
FSMC_D0	PD14	85
FSMC_D1	PD15	86

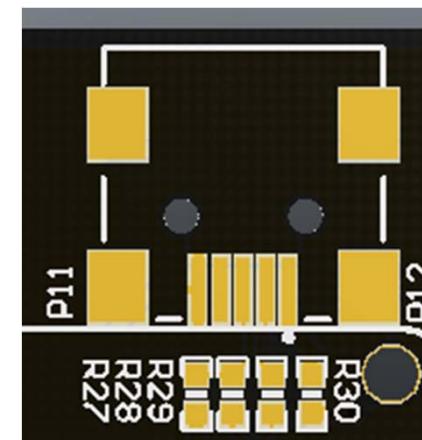
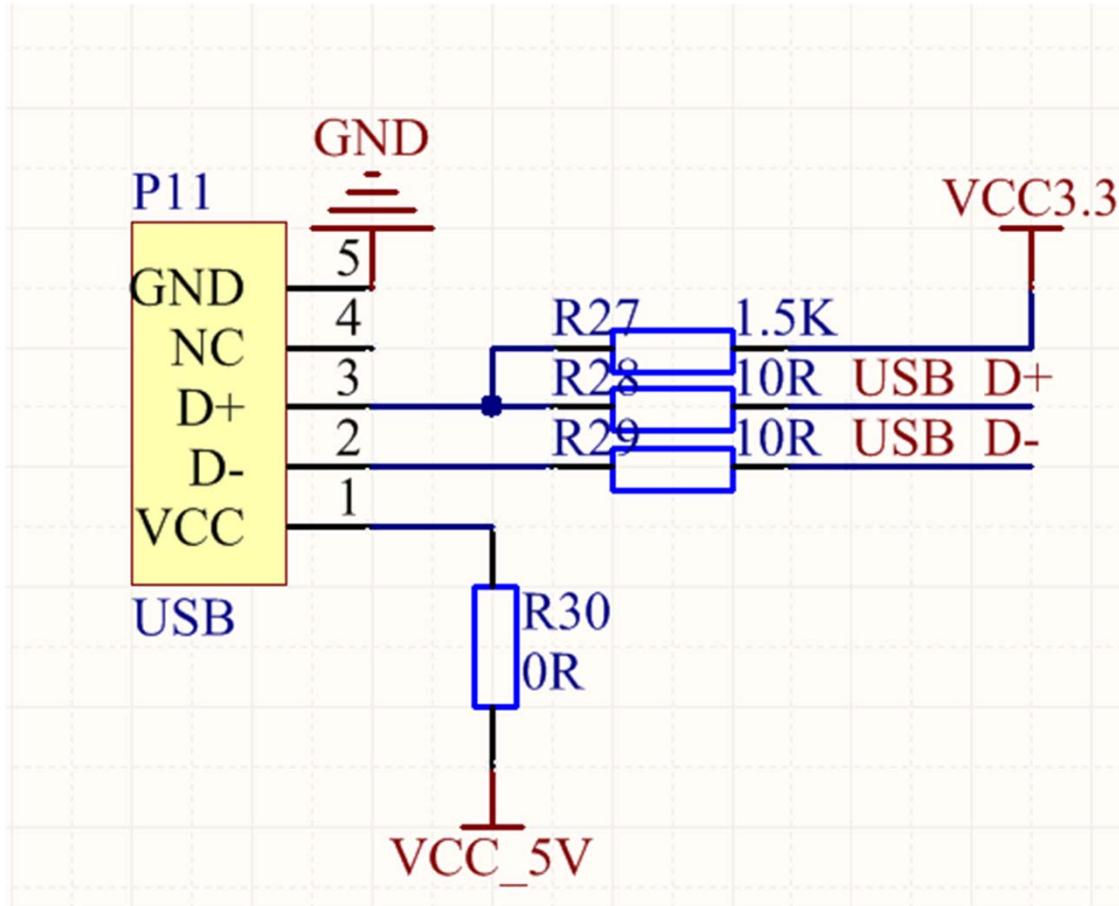
P00/FSMC\_D2/CAN1\_RX  
P01/FSMC\_D3/CAN1\_TX  
P02/TIM3\_ETR/U5\_RX/SDIO\_CMD/DCMI\_D11  
P03/FSMC\_CLK/U2\_CTS  
P04/FSMC\_NOE/U2\_RTS  
P05/FSMC\_NWE/U2\_TX  
P06/FSMC\_NWAIT/U2\_RX  
P07/FSMC\_NE1/FSMC\_NCE2/U2\_CK  
P08/FSMC\_D13/U3\_TX  
P09/FSMC\_D14/U3\_RX  
P10/FSMC\_D15/U3\_CK  
P11/FSMC\_A16/FSMC\_CLE/U3\_CTS  
P12/FSMC\_A17/FSMC\_ALE/TIM4\_CH1/U3\_RTS  
P13/FSMC\_A18/TIM4\_CH2  
P14/FSMC\_D0/TIM4\_CH3  
P15/FSMC\_D1/TIM4\_CH4



CYE-STM32F4-C V1.1 CYESS UBus Socket

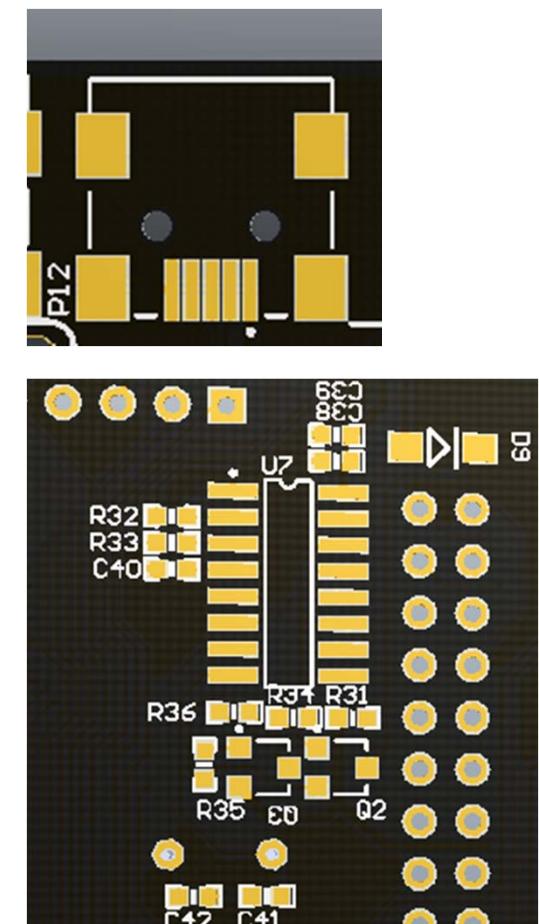
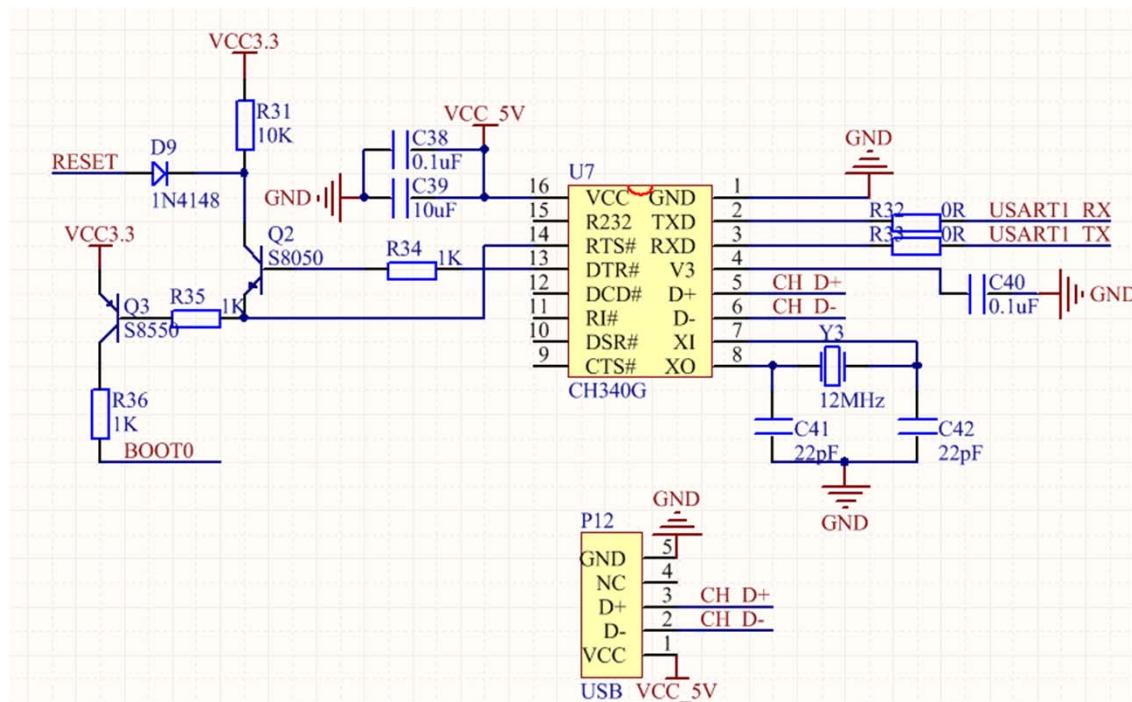


# CYE-STM32F4-C V1.1 USB OTG



USB D-	PA11 103	PA10/TIM1_CH5/U1_RX/OTG_FS_ID/DCMI_D1
USB D+	PA12 104	PA11/TIM1_CH4/U1_CTS/CAN1_RX/OTG_FS_DM
	PA10 105	PA12/TIM1_ETR/U1_RTS/CAN1_TX/OTG_FS_DP

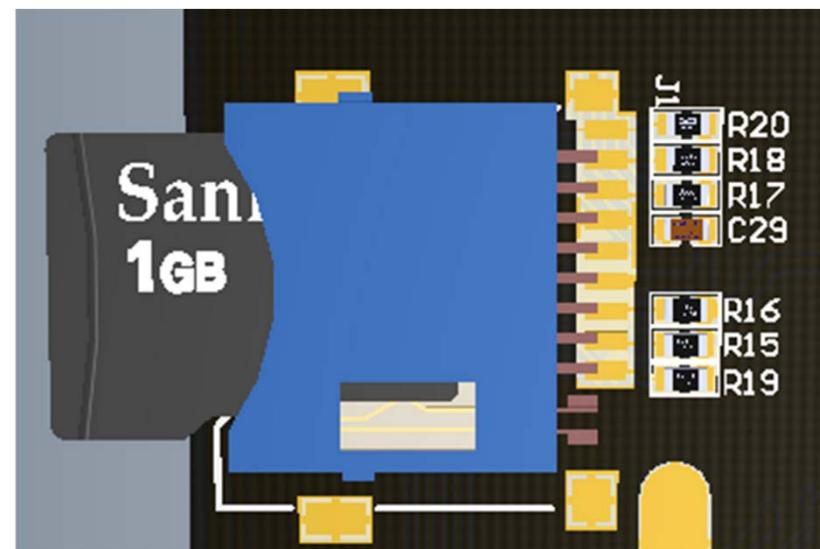
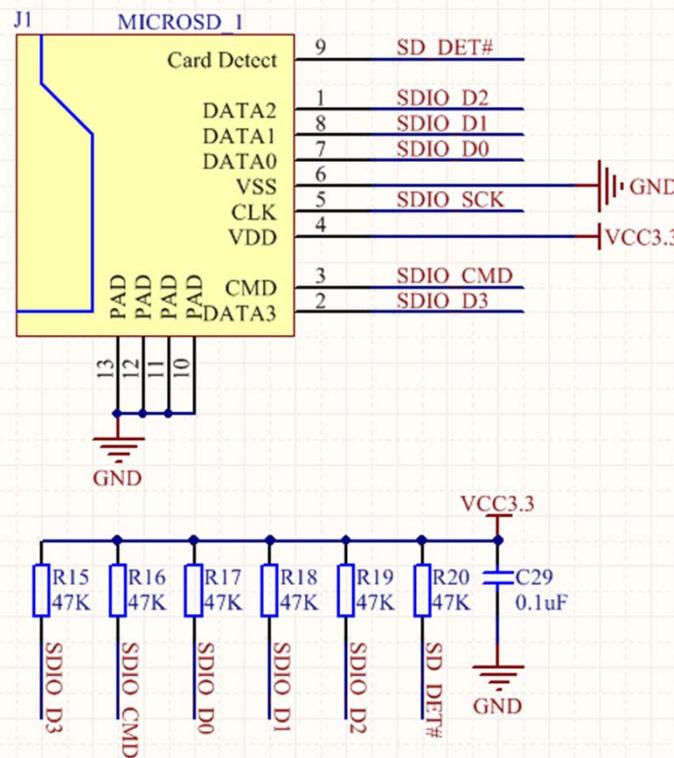
# CYE-STM32F4-C V1.1 Debug (USB to UART)



USART1_TXPA9	101	PA9/TIM1_CH1/U1_RX/I2C3_SMBA/OTG_FS_VBUS/DCMI_D0
USART1_RXPA10	102	PA10/TIM1_CH3/U1_RX/OTG_FS_ID/DCMI_D1
IISB_D-	PA11	103

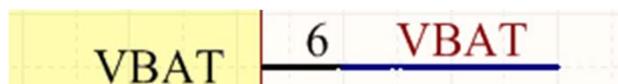
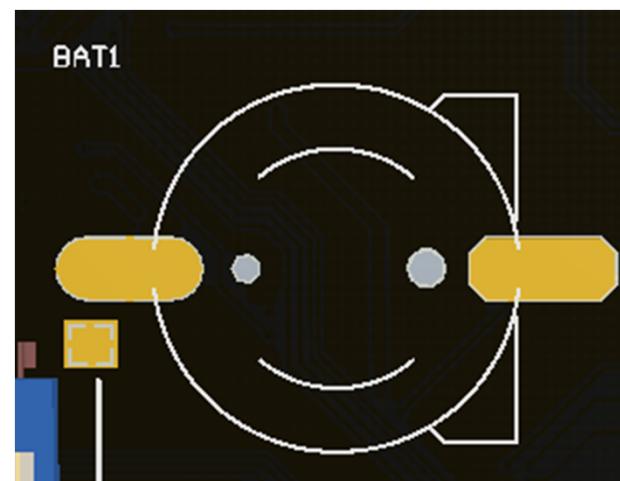
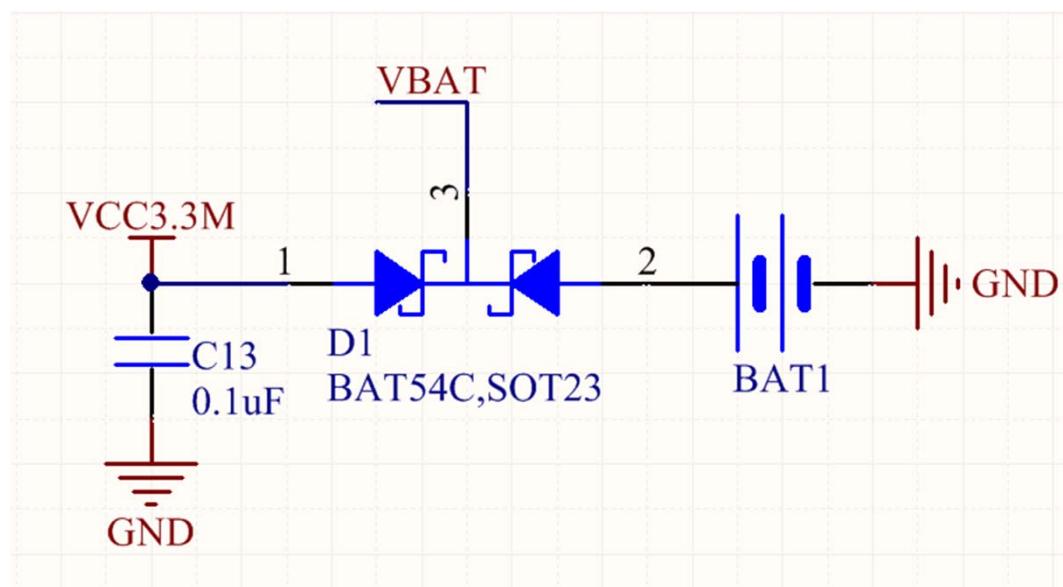
# CYE-STM32F4-C V1.1 microSD

## microSD Card



SD DET# PA15 110	PA14/JTICK_SWCLK PA15/JTDI/SPI1_NSS/SPI3_NSS/TIM2_CH1_ETR/I2S3_WS
SDIO D0 PC8 98	PC12/ TIM3_CH2/TIM8_CH2/U6_KA/SDIO_D//DCMI_D1/I2S3_MCK
SDIO D1 PC9 99	PC8/TIM3_CH3/TIM8_CH3/U6_CK/SDIO_D0/DCMI_D2
SDIO D2 PC10 111	PC9/TIM3_CH4/TIM8_CH4/I2C3_SDA/SDIO_D1/DCMI_D3/MCO2/I2S_CKIN
SDIO D3 PC11 112	PC10/SPI3_SCK/U3_TX/U4_RX/SDIO_D2/DCMI_D8/I2S3_CK
SDIO SCK PC12 113	PC11/SPI3_MISO/U3_RX/U4_RX/SDIO_D3/DCMI_D4/I2S3ext_SD
PC12/SPI3_MOSI/U3_CK/U5_TX/SDIO_CK/DCMI_D9/I2S3_SD	
SD CS SDIO CMD PD2 116	PD1/FSMC_D3/CAN1_TX
PD2 117	PD2/TIM3_ETR/U5_RX/SDIO_CMD/DCMI_D11

# CYE-STM32F4-C V1.1 RTC Backup



# CYE-STM32F4-C V1.1 GPIO

PE1	P1	PE0	PB9	P2	PE2	PB7	PB5	PB4	PB8
PE3	1 2	PE4	PB2	1 2	PE5	PB4	PB6	PB15	PB10
PE5	3 4	PE6	PB3	3 4	PE7	PB5	PB6	PB14	PB11
PC13	5 6	PF0	PG14	5 6	PE8	PB7	PB4	PB13	PE14
PF1	7 8	PF2	PG12	7 8	PE9	PB8	PG15	PG13	PE12
PF3	9 10	PF4	PG10	9 10	PE10	PB9	PG11	PG11	PE10
PF5	11 12	PF6	PD7	11 12	PE11	PB10	PG9	PG9	PE9
PF7	13 14	PF8	PD5	13 14	PE12	PB11	PD6	PD6	PC1
PF9	15 16	PF10	PD3	15 16	PE13	PB12	PD4	PD4	PF15
PC0	17 18	PC1	PD1	17 18	PE14	PB13	PD2	PD2	PF13
PC2	19 20	PC3	PC12	19 20	PE15	PB14	PD0	PD0	PE11
PA0	21 22	PA1	PC10	21 22	PE16	PB15	PC11	PC11	PE6
PA2	23 24	PA3	PA14	23 24	PE17	PB16	PA15	PA15	PC8
PA4	25 26	PA5	PA10	25 26	PE18	PB17	PA13	PA13	PE7
PA6	27 28	PA7	PA14	27 28	PE19	PB18	PA9	PA9	PE5
PC4	29 30	PA8	PA10	29 30	PE20	PB19	PC9	PC9	PE3
PB0	31 32	PC5	PA8	31 32	PE21	PB20	PC7	PC7	PC8
PB2	33 34	PB1	PC6	33 34	PE22	PB21	PG8	PG8	PC6
PF12	35 36	PF11	PG7	35 36	PE23	PB22	PG6	PG6	PC7
PF14	37 38	PF13	PG5	37 38	PE24	PB23	PG4	PG4	PC8
PG0	39 40	PF15	PG3	39 40	PE25	PB24	PG2	PG2	PA9
PE7	41 42	PG1	PD15	41 42	PE26	PB25	PD14	PD14	PA10
PE9	43 44	PE8	PD13	43 44	PE27	PB26	PD12	PD12	PA13
PE11	45 46	PE10	PD11	45 46	PE28	PB27	PD10	PD10	PA15
PE13	47 48	PE12	PD9	47 48	PE29	PB28	PD8	PD8	PA13
PE15	49 50	PE14	PB15	49 50	PE30	PB29	PB14	PB14	PA11
PB11	51 52	PB10	PB13	51 52	PE31	PB30	PB12	PB12	PA12
	53 54		PB13	53 54	PE32	PB31	PB10	PB10	PA0

HEAD2\*27

HEAD2\*27

