sistemas electrónicos

ADC-DAC: resultados

- 1. a) n = 7 bits
 - b) $\partial v = 39,37 \text{mV}$
 - c) b = 1011010
- 2. a) 1 LSB $\rightarrow v_a = 0.1 \text{V}$
 - b) $R_1 = 200 \mathrm{k}\Omega$; $R_2 = 100 \mathrm{k}\Omega$; $R_3 = 50 \mathrm{k}\Omega$; $R_4 = 25 \mathrm{k}\Omega$; $R_5 = 12,5 \mathrm{k}\Omega$; $R_F = 8 \mathrm{k}\Omega$
 - c) b = $100001 -> I = 412,5\mu$ A
- 3. a) I_7 = 1mA; I_0 = 7,8125 μ A
 - b) 1 LSB $-> v_O = 39 \text{mV}$
 - c) b = $01000010 \rightarrow v_O = 2,578125V$
- 4. a) 1mS ; b) 34μ s
- 5. 5 comparadores; b = 101
- 6. b = 110000 ; tempo de conversão = n+1 clocks = $7\mu s$