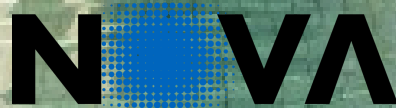


TITLE

João Pedro Marcelino

A thesis submitted in fulfillment of the requirements
for the degree of *Master of Science*
in Micro and Nanotechnology



FACULDADE DE
CIÊNCIAS E TECNOLOGIA

NOVA School of Science and Technology

Month Year

Test [1]

Acknowledgements

List of Abbreviations and Notations

List of Figures

List of Tables

Introduction

State of the Art

Materials and Methods

Results and Discussion

Conclusions and Future Perspectives

|A

Annexes

Bibliography

- [1] M.A. Abas and G. Russell. Built-in time measurement circuits – a comparative design study. *IET Computers Digital Techniques*, 1(2):87–97, 2007.



Declaration

I hereby declare that this thesis is my original work and has not been submitted elsewhere.

Name: Your Name

Signature: _____

Date: _____