non_project_mode_example

• File: top_level.vhd

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Diagram

STD_LOGIC_VECTOR(4 downto 0) — PB_IN LEDS_OUT — STD_LOGIC_VECTOR(4 downto 0)

Description

This is a basic example of a non project mode workflow. Set for the ZCU102, this module will synthesize to light LEDs on the click of a pushbutton.

Generics and ports

Table 1.1 Generics

Table 1.2 Ports

Port name	Direction	Туре	Description
PB_IN	11[1	STD_LOGIC_VECTOR(4 downto 0)	Incoming Pushbuttons (0-LS 1-RS 2-US 3-DS 4-MS)
LEDS_OUT	IOUT		LEDs out light based on the pushbutton pressed (0-LS 1-RS 2-US 3-DS 4-MS).